This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts. Customer Notice:Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customers responsibility to verify proper and reliable operation in the actual application, Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear Applications Engineering for assistance. U1 LTC3025EDC ЕЗ ● Vout VIN OUT 300 mA (max) COUT1 COUT2 CINL CIN JP1 JP2 JP3 JP4 COUT _1uF --6.3V 1. 2V O + 100uF +10uF -1uF 6.3V 0.8v O 1.5V O 1.8V -1uF 6.3V BIAS -10V ~25V (Opt) (Opt) R1 E2 GND ● E4 GND ENABLE 40.2K 40.2K 30.1K 30.1K 1% GND R5 30.2K ● E6 GND E5 BIAS C1 JP5 Vout Enable PCB Layout R5 10 M R1 U1 CIN COUT * CIN1 is an optional capacitor. It is inserted on the DC741 to dampen the (possible) ringing voltage due to the use of long 1630 McCarthy Bivd., Milpitas, CA Linear Technology Corp. 95035-7487 input leads. On a normal, typical PCB, with short traces, Phone: (408)432-1900 Fax (408)434-0507 CIN1 is not needed. VLDO Linear Regulator Document Number Rev Demo Circuit 741A Wednesday, September 29, 2004Sheet