

C1206T100K5GCLTU

Aliases (C1206T100K5GCL7800)

SMD COTS COG, Ceramic, 10 pF, 10%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 1206



Click here for the 3D model.

| Dimensions | |
|------------|------------------|
| Chip Size | 1206 |
| L | 3.2mm +/-0.2mm |
| W | 1.6mm +/-0.2mm |
| Т | 0.78mm +/-0.10mm |
| В | 0.5mm +/-0.25mm |

| Packaging Specifications | |
|--------------------------|--------------------------|
| Packaging | T&R, 180mm, Plastic Tape |
| Packaging Quantity | 4000 |

| General Informat | ion |
|---------------------|---|
| Series | SMD COTS COG |
| Style | SMD Chip |
| Description | SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I |
| Features | Ultra-Stable, Low Loss, Class I |
| RoHS | No |
| Prop 65 | ▲ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov. |
| Termination | Lead (SnPb) |
| Marking | No |
| Failure Rate | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA- 469, Humidity per MIL-STD-202, Method 103, Condition A |
| AEC-Q200 | No |
| Component Weight | 15 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Specifications | |
|--|------------------------------|
| Capacitance | 10 pF |
| Measurement Condition | 1 MHz 1.0Vrms |
| Capacitance Tolerance | 10% |
| Voltage DC | 50 VDC |
| Dielectric Withstanding Voltage | 125 VDC |
| Temperature Range | -55/+125°C |
| Temperature Coefficient | COG |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1MegaHz 1.0Vrms |
| Dissipation Factor | 0.1% 1 MHz 1.0Vrms |
| Aging Rate | 0% Loss/Decade Hour |
| Insulation Resistance | 100 GOhms |

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