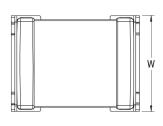
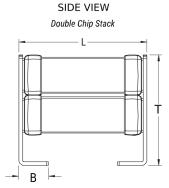






TOP VIEW Single or Double Chip Stack





Click here for the 3D model.

Dimensions	
L	6mm +/-0.5mm
W	5mm +/-0.5mm
T	5mm +/-0.50mm
В	1.6mm +/-0.3mm

Packaging Specifications			
Packaging	T&R, 180mm, Plastic Tape		
Packaging Quantity	200		

General Information		
Series	KPS SMD Comm X7R SnPb	
Style	Stacked Chip	
Description	SMD, MLCC, Stacked, Double Chip, Temperature Stable	
Features	Temperature Stable	
RoHS	No	
Prop 65	▲ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.	
SCIP Number	795b247e-78a9-405c-8728-223c2d9782a2	
Termination	Lead (SnPb)	
AEC-Q200	No	
Component Weight	800 mg	
Chip Size	2220-2	
Shelf Life	78 Weeks	
MSL	1	

Charifications			
Specifications			
Capacitance	4.7 uF		
Measurement Condition	1 kHz 1.0Vrms		
Capacitance Tolerance	20%		
Voltage DC	100 VDC		
Dielectric Withstanding Voltage	250 VDC		
Temperature Range	-55/+125°C		
Temperature Coefficient	X7R		
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms		
Dissipation Factor	2.5% 1 kHz 1.0Vrms		
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours		
Insulation Resistance	212.8 MOhms		

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.