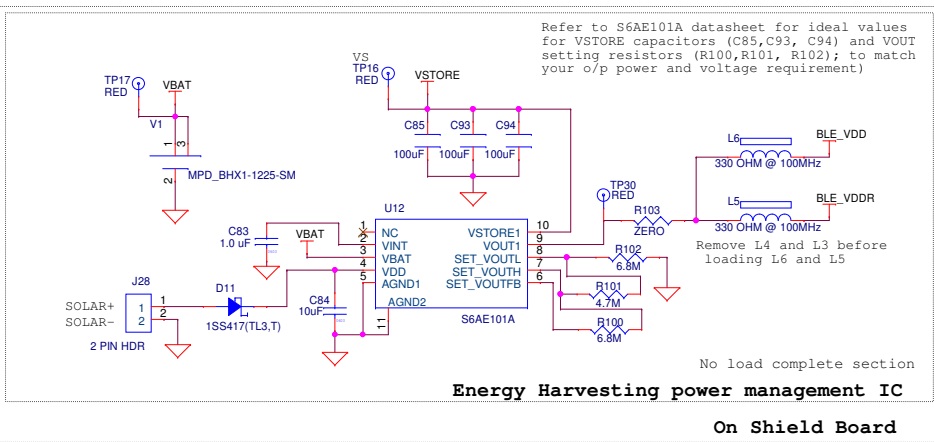
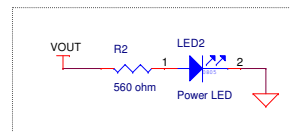
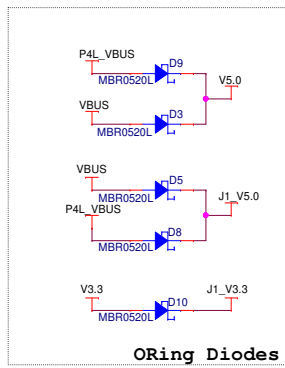
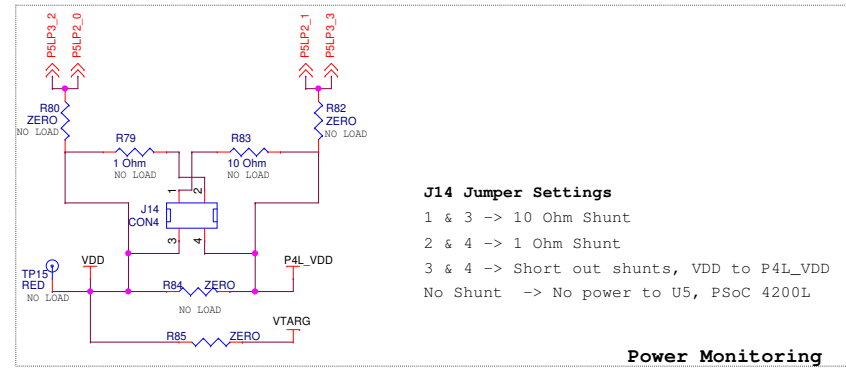
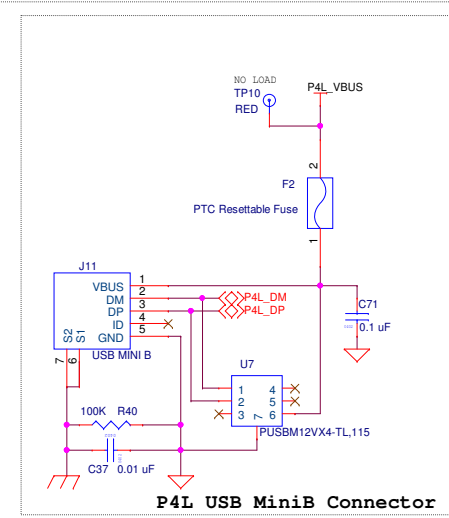
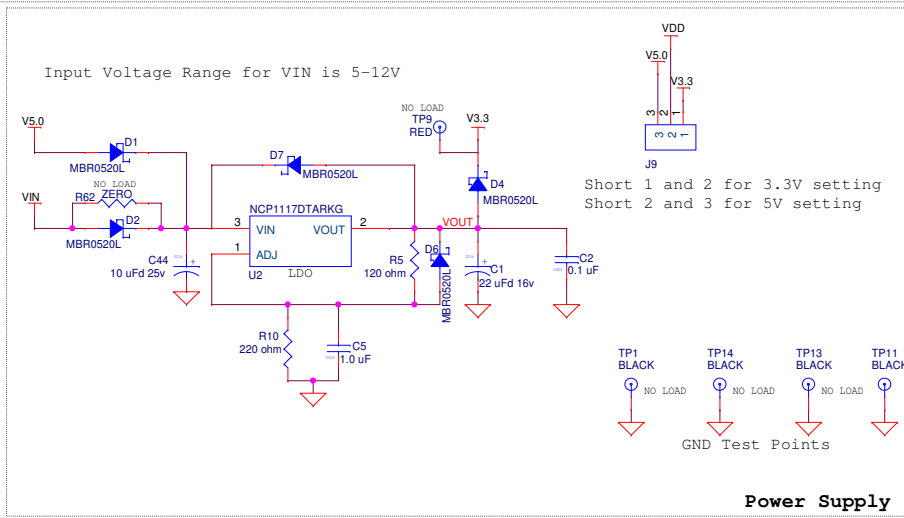
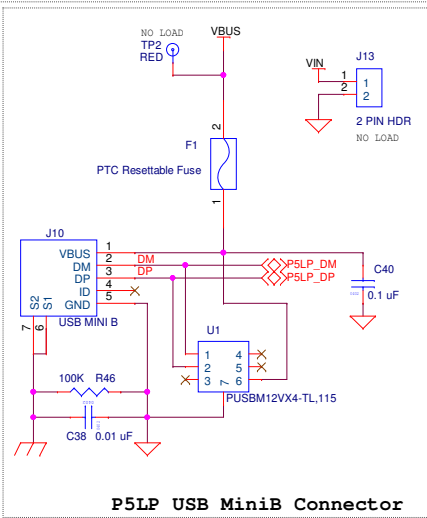


PCBA: 121-60244-01  
 PCB: 600-60276-01  
 FAB DRW: 610-60265-01  
 ASSY DRW: 620-60273-01

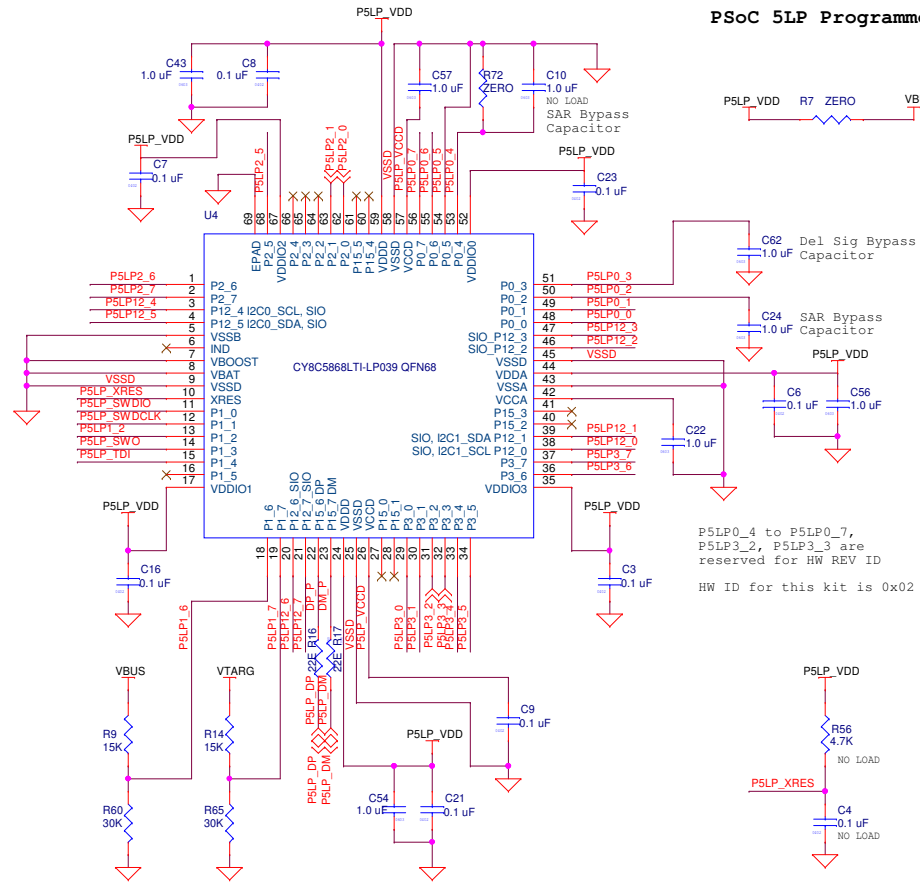
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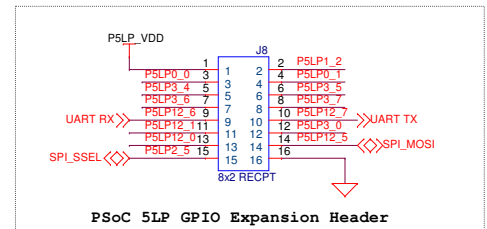
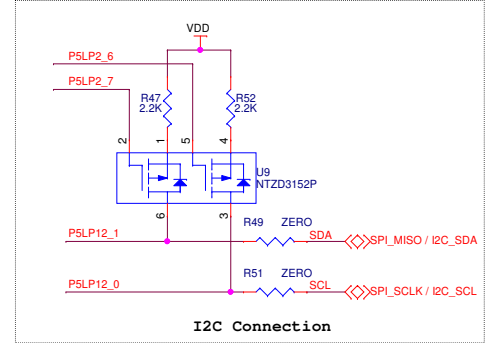
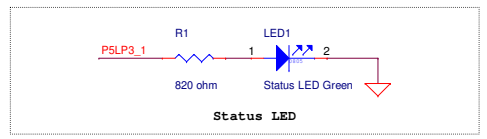
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ASSY DRW: 620-60273-01

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# PSoc 5LP Programmer / Debugger

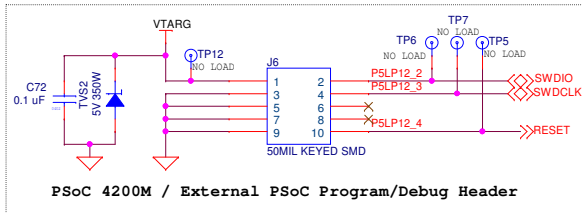
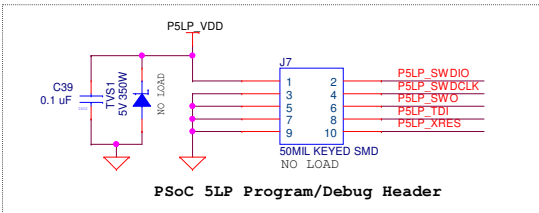


P5LP0\_4 to P5LP0\_7,  
P5LP3\_2, P5LP3\_3 are  
reserved for HW REV ID  
HW ID for this kit is 0x02



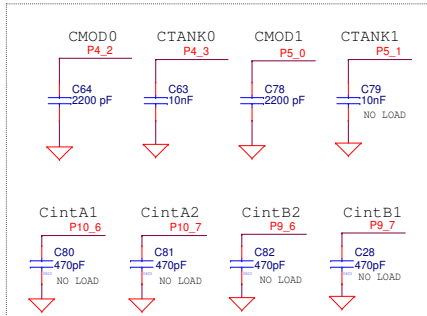
### USB-Serial Bridge Connections

P5LP12_6	-----	UART RX
P5LP12_7	-----	UART TX
P5LP12_0	-----	SPI SCLK / I2C SCL
P5LP12_1	-----	SPI MISO / I2C SDA
P5LP12_5	-----	SPI MOSI
P5LP2_5	-----	SPI SSEL

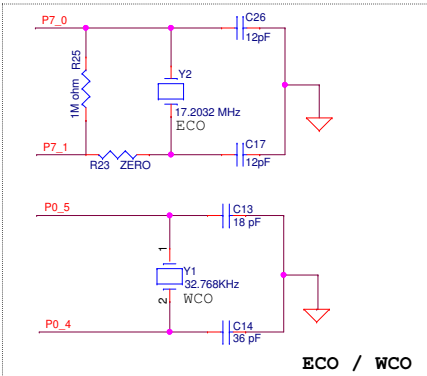


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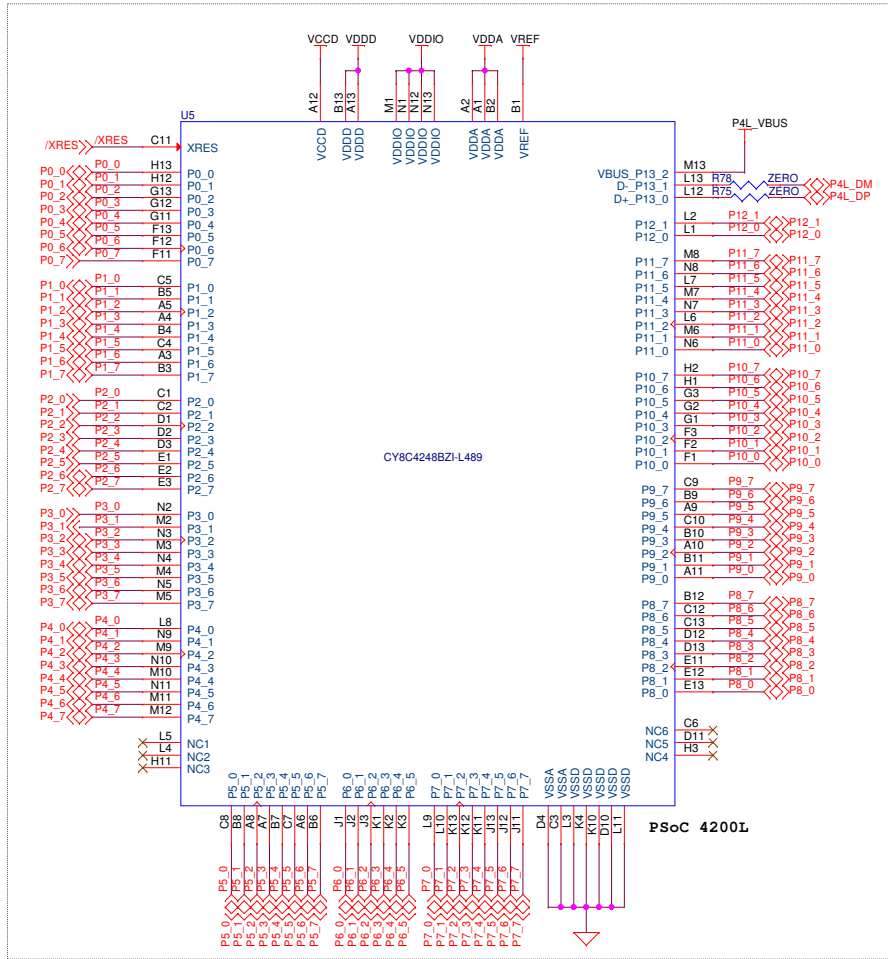
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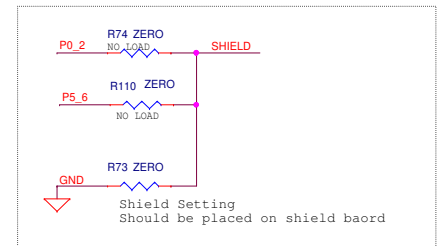
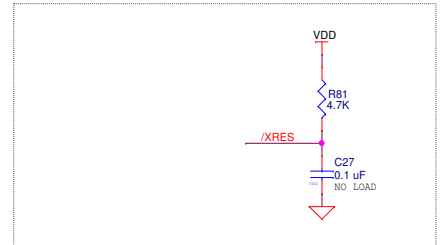
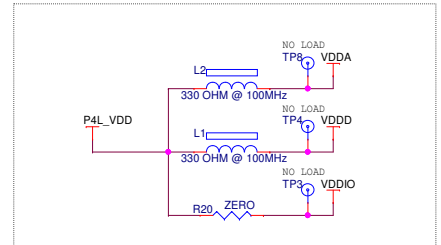
Refer kit user guide for details on Cint capacitors



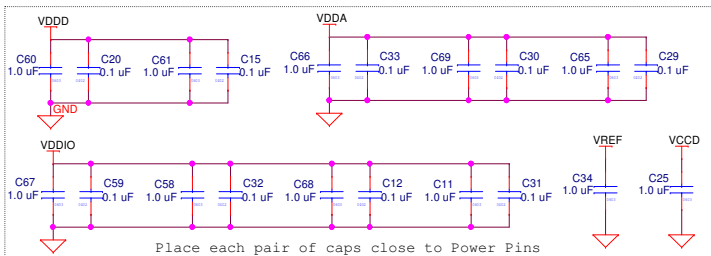
ECO / WCO



PSoc 4200L



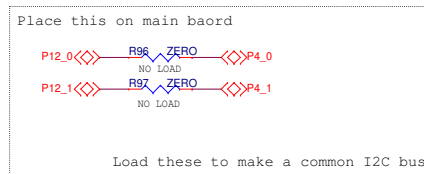
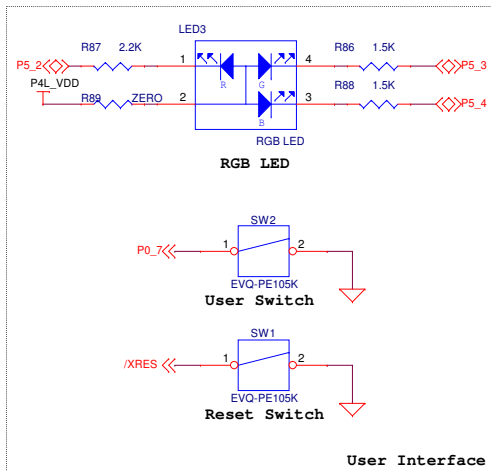
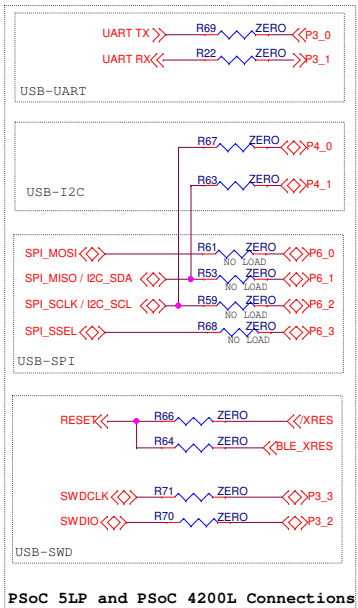
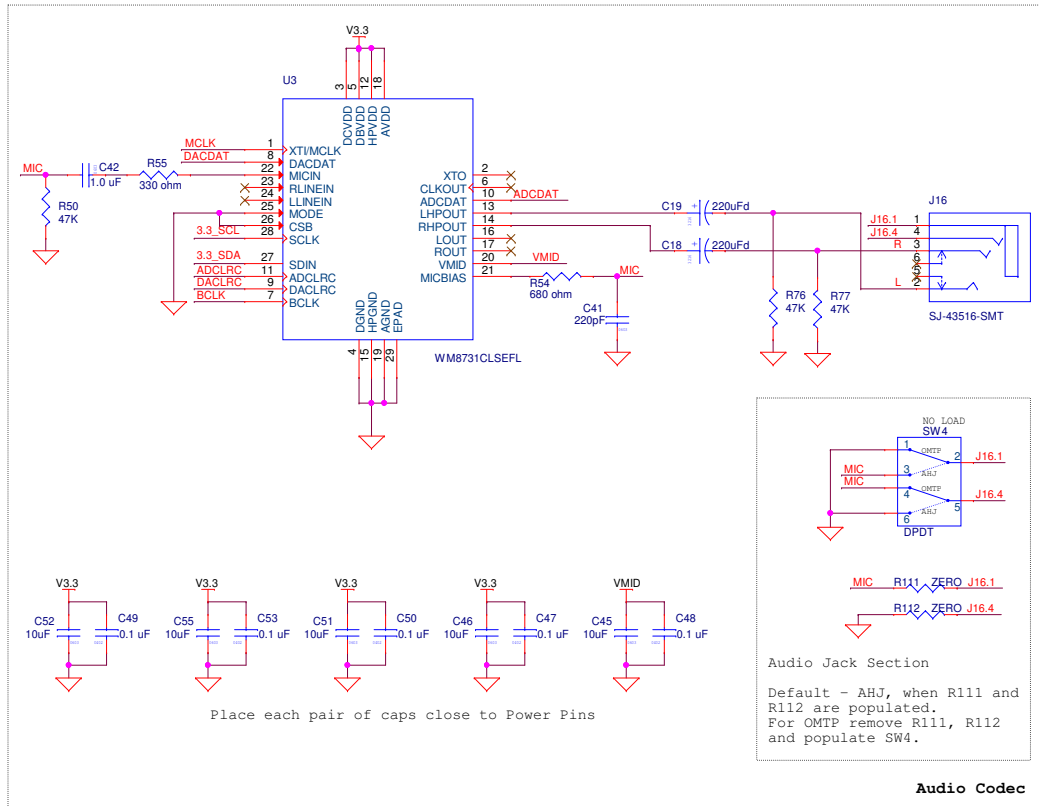
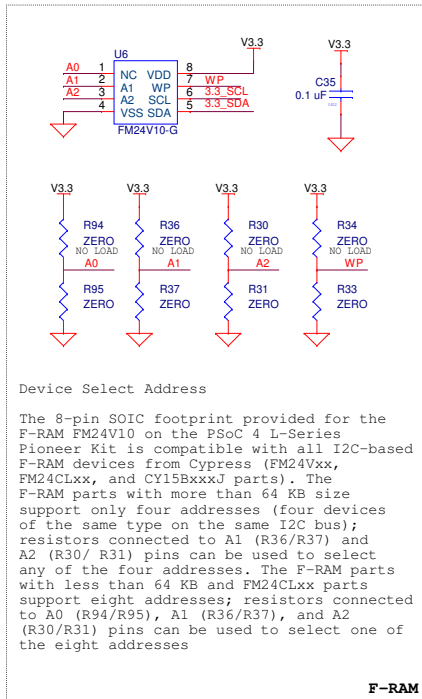
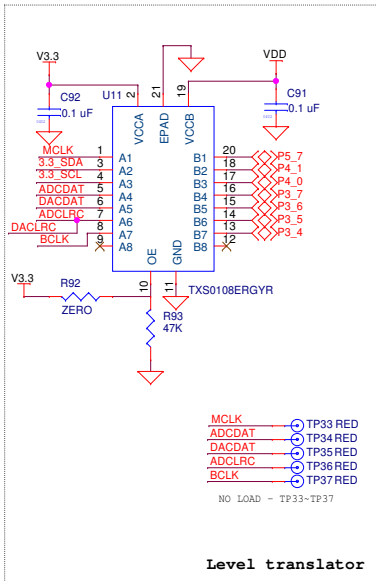
R73 is loaded by default to connect hatched pattern to GND  
 Remove R73 and load R74 to connect hatched pattern to P0\_2  
 Remove R73/ R74 and load R110 to connect hatched pattern to P0\_3 (on main board); after the shield board is snapped off



Place each pair of caps close to Power Pins

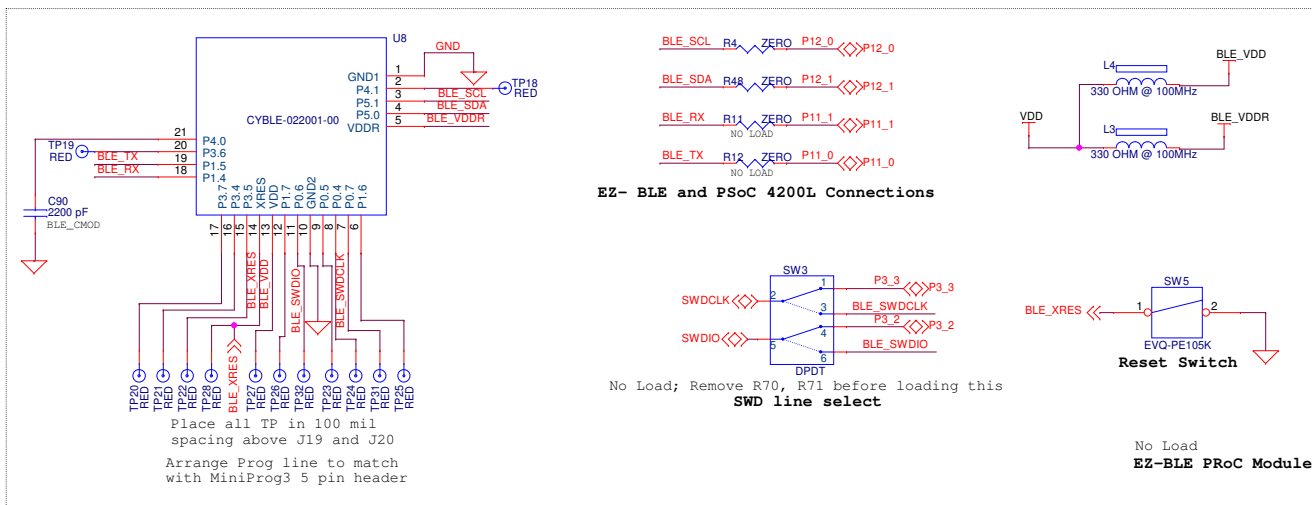
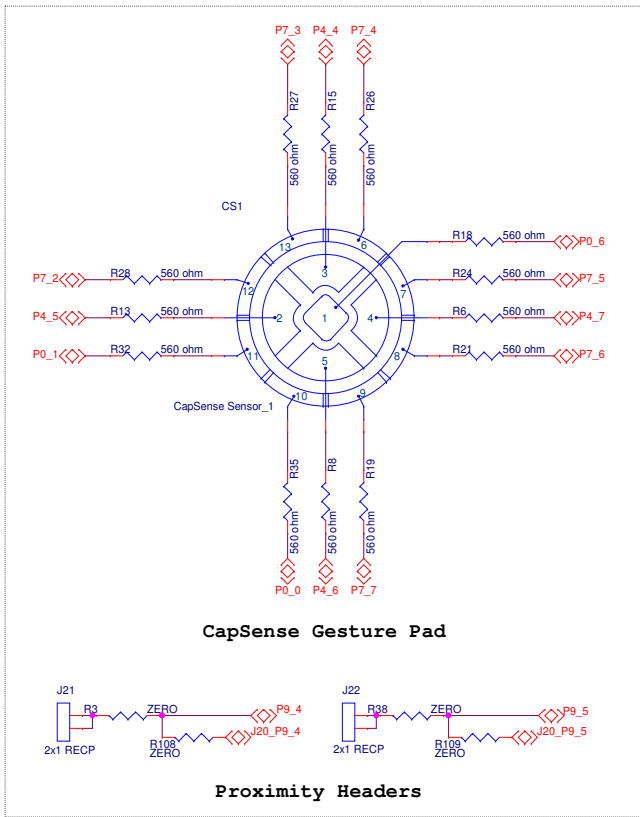
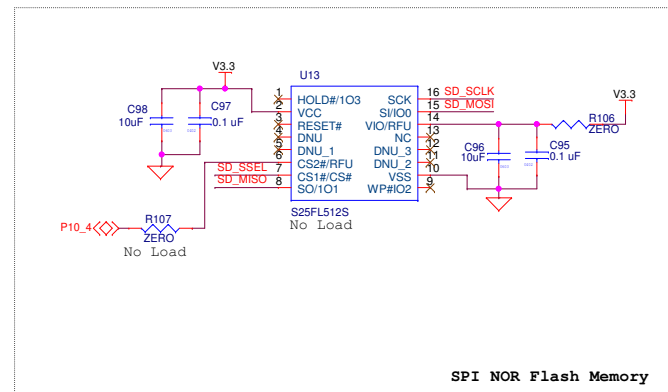
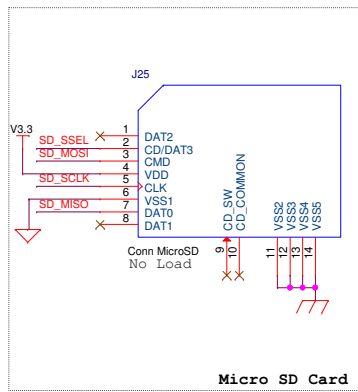
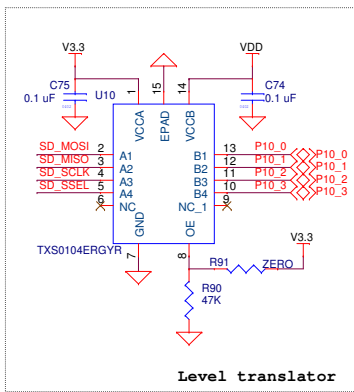
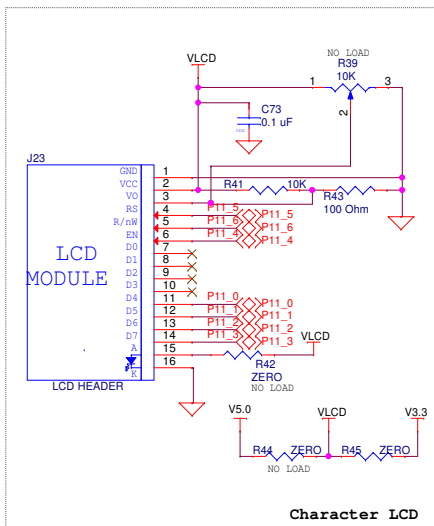
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 PCB: 600-60276-01  
 FAB DRW: 610-60265-01  
 ASSY DRW: 620-60273-01

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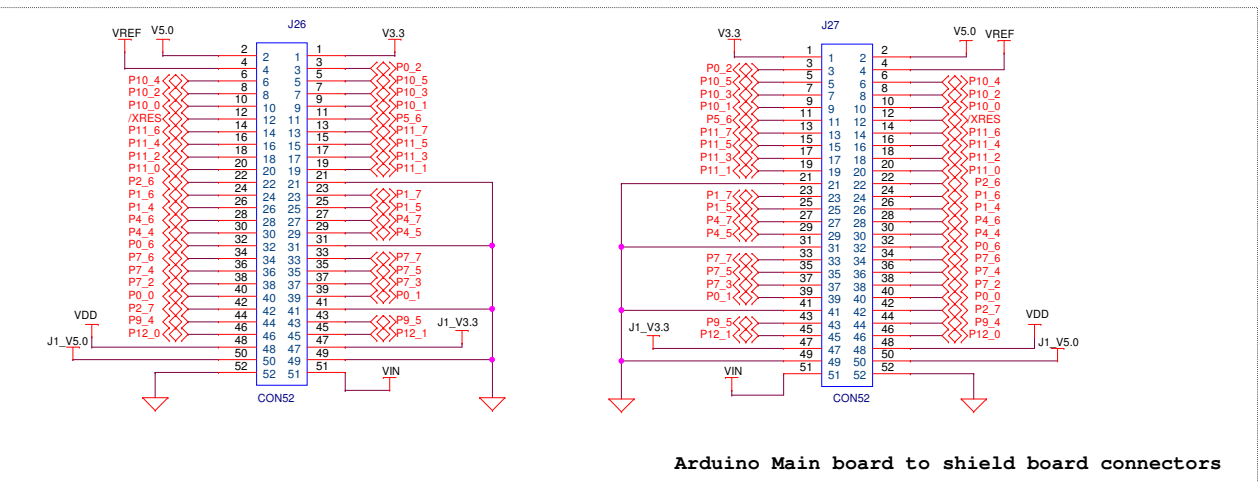
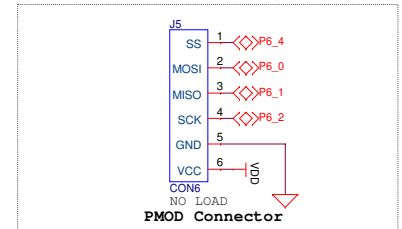
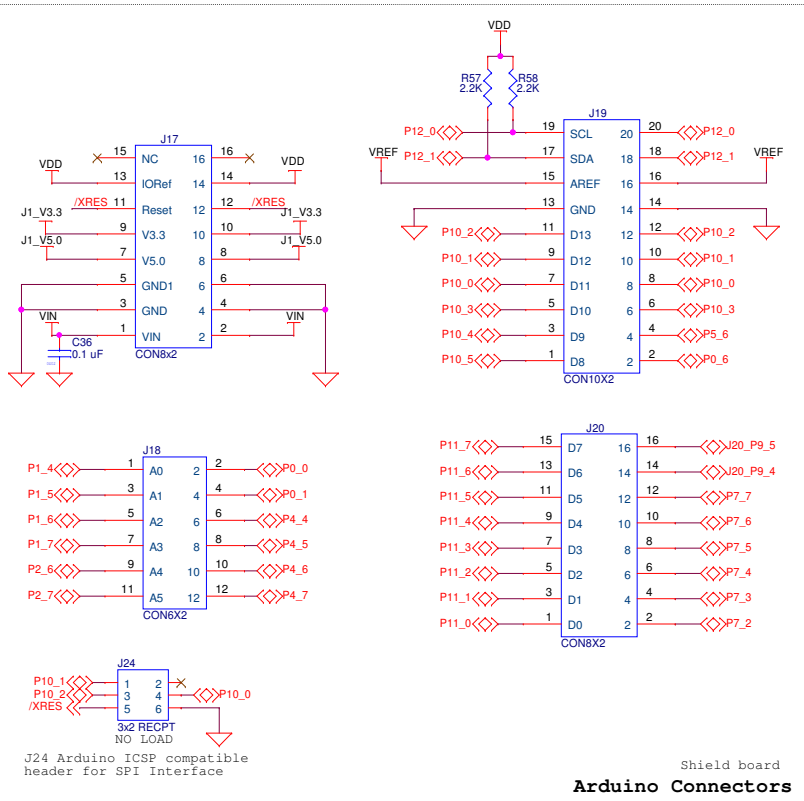
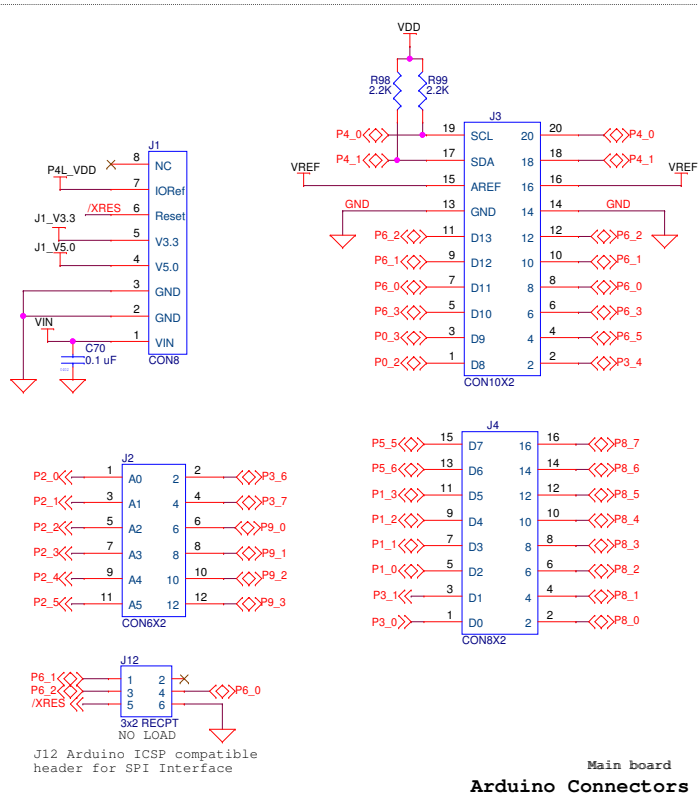


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