


PCN Number:	20190111000.0	PCN Date:	January 16, 2019
Title:	Datasheet for AM3352		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process
Notification Details			
Description of Change:			
<p>Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.</p>			
		AM3359, AM3358, AM3357, AM3356, AM3354, AM3352, AM3351 <small>SPRS717K – OCTOBER 2011 – REVISED DECEMBER 2018</small>	

Changes from April 1, 2016 to December 15, 2018 (from J Revision (April 2016) to K Revision)	Page
• Updated/Changed OTG to DRD (Dual-Role Device)	2
• Removed OpenVG 1.0	2
• Updated Description text	4
• Updated/Changed "OTG" to "DRD"	5
• Removed 600 MHz from Frequency and 1200 from MIPS for AM3359	8
• Added DEV_FEATURE register value to Device Comparison	9
• Added 125°C Latch-up performance info	79
• Updated DDR3/DDR3L maximum frequency for industrial extended temperature	82
• Added Industrial Extended operating temperature range	85
• Updated/Changed footnote for Output Capacitor Characteristics from "RTC_KLDO_ENn" to "RTC_KALDO_ENn" ..	94
• Added ADC clock frequency to TSC_ADC Electrical Parameters	97
• Updated Sampling Dynamics section of TSC_ADC Electrical Parameters table	97
• Added new footnotes	99
• Added DCAN Timing Conditions	116
• Added DMTimer Timing Conditions	117
• Remove NO. 4, Transition time, MDC	118
• Updated Switching Characteristics for MDIO_DATA	119
• Removed content from Switching Characteristics for RMII[x]_TXD[1:0], and RMII[x]_TXEN - RMII Mode	123
• Removed Transition time, RD and Transition time, RX_CTL from Timing Requirements for RGMII[x]_RD[3:0], and RGMII[x]_RCTL - RGMII Mode	124
• Removed Transition time, TXC from Switching Characteristics for RGMII[x]_TCLK - RGMII Mode	125
• Removed Transition time, TD and Transition time, TX_CTL from Switching Characteristics for RGMII[x]_TD[3:0], and RGMII[x]_TCTL - RGMII Mode	125
• Removed content from GPMC and NOR Flash Switching Characteristics—Synchronous Mode	127
• Updated No. F11 values	127
• Swapped F22 and F21 in GPMC and NOR Flash—Synchronous Burst Read—4x16-Bit (GpmcFCLKDivider = 0) ..	132
• Removed content from GPMC and NOR Flash Switching Characteristics—Asynchronous Mode	136
• Removed content from GPMC and NAND Flash Switching Characteristics—Asynchronous Mode	145
• Removed Rise time and Fall time info from Switching Characteristics for I ² C Output Timings	190
• Added JTAG Timing Conditions	191
• Added content to LCD Controller Timing Conditions	192
• Removed content from Switching Characteristics for LCD LIDD Mode	193
• Removed content from Switching Characteristics for LCD Raster Mode	202
• Removed content from Switching Characteristics for McSPI Output Timings – Master Mode	216
• Removed content from Switching Characteristics for MMC[x]_CLK	220
• Removed content from PRU-ICSS PRU Switching Requirements – Direct Output Mode	222
• Removed content from PRU-ICSS PRU Switching Requirements - Shift Out Mode	224
• Removed content from PRU-ICSS ECAT Switching Requirements - Digital I/Os	227
• Removed transition time, MDC from PRU-ICSS MDIO Switching Characteristics - MDIO_CLK	227
• Updated PRU-ICSS MDIO Switching Characteristics - MDIO_DATA	228
• Updated Note in PRU-ICSS MII_RT Electrical Data and Timing	228
• Added UART Timing Conditions	230
• Added UART Timing Conditions	231
• Added Carrier Type to Figure 8-1 , AM335x Device Nomenclature	235

The datasheet number will be changing.

Device Family	Change From:	Change To:
AM3352	SPRS717J	SPRS717K

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/AM3352>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

AM3352BZCZT60	AM3352BZCZT60R		
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com