MJD42CQ





100V PNP MEDIUM POWER TRANSISTOR IN TO252

Features

- BV_{CEO} > -100V
- I_C = -6A Continuous Collector Current
- I_{CM} = -10A Peak Pulse Current
- Ideal for Power Switching or Amplification Applications
- Complementary NPN Type: MJD41CQ
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
 The MJD42CQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

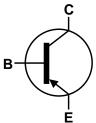
Mechanical Data

- Package: TO252 (DPAK)
- Package Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.34 grams (Approximate)

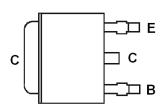




Top View



Device Schematic



Pin Out Configuration Top View

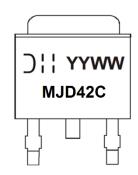
Ordering Information (Note 4)

Orderable	Packago	Marking	Reel size (inches) Tape width (mm)		Packing		
Part Number	Package	wai king	Reel Size (Illelies)	rape width (IIIII)	Qty.	Carrier	
MJD42CQ-13	TO252	MJD42C	13	16	2,500	Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



MJD42C = Product Type Marking Code

Oli = Manufacturers' Code Marking

YYWW = Date Code Marking

YY = Last Digit of Year (ex: 23 = 2023)

WW = Week Code (01 - 53)



Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-120	V
Collector-Emitter Voltage	V _{CEO}	-100	V
Emitter-Base Voltage	V _{EBO}	-7	V
Continuous Collector Current	Ic	-6	A
Peak Pulse Collector Current	I _{CM}	-10	А
Continuous Base Current	Ι _Β	-2	A

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
	(Note 5)		2.7		
Power Dissipation	(Note 6)	P_{D}	2.4	W	
	(Note 7)		1.5		
	(Note 5)		46		
Thermal Resistance, Junction to Ambient Air	(Note 6)	$R_{ heta JA}$	52	°C/W	
	(Note 7)		83		
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C		

ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes:

- 5. For a device mounted with the exposed collector pad on 25mm x 25mm 2oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
- 6. Same as note (5), except mounted on 25mm x 25mm 1oz copper.
- 7. Same as note (5), except mounted on minimum recommended pad (MRP) layout.
- 8. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics

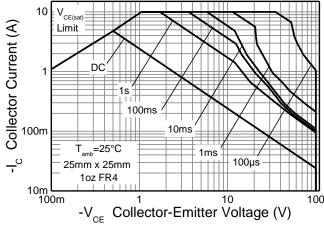


Figure 1. Safe Operating Area

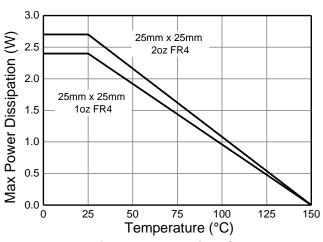


Figure 2. Derating Curve

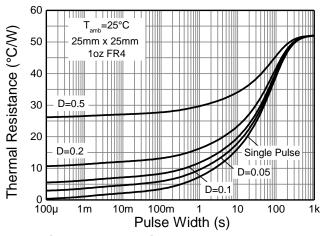


Figure 3. Transient Thermal Impedance

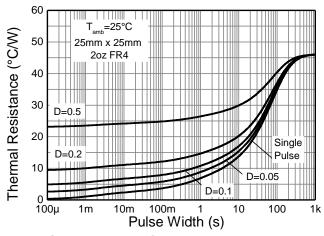


Figure 4. Transient Thermal Impedance

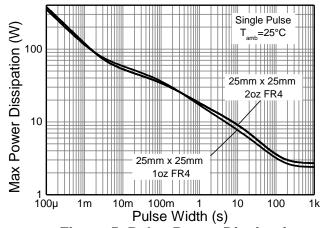


Figure 5. Pulse Power Dissipation



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
Collector-Base Breakdown Voltage	BV _{CBO}	-120			V	$I_C = -100uA$	
Collector-Emitter Breakdown Voltage (Note 9)	BV _{CEO}	-100			V	I _C = -10mA	
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	_	_	V	I _E = -100uA	
Collector Cut-off Current	I _{CES}	1		-1	uA	V _{CE} = -100V	
Collector-Base Cut-off Current	I _{CBO}	_	_	-100	nA	V _{CB} = -100V	
Emitter Cut-off Current	I _{EBO}			-1	uA	$V_{EB} = -6V$	
Collector-Emitter Saturation Voltage (Note 9)	V _{CE(sat)}	_	_	-1.5	V	I _C = -6A, I _B = -600mA	
Base-Emitter Saturation Voltage (Note 9)	V _{BE(sat)}	_	_	-1.4	V	I _C = -6A, I _B = -600mA	
Base-Emitter Turn-On Voltage (Note 9)	V _{BE(on)}	1		-2	V	$I_{C} = -6A$, $V_{CE} = -4V$	
DC Current Gain (Note 9)	h _{FE}	30				$V_{CE} = -4V, I_{C} = -0.3A$	
De Guiteili Gaiii (Note 9)		15	_			$V_{CE} = -4V$, $I_C = -3A$	
Small Signal Current Gain	h _{fe}	20	_	_	_	$V_{CE} = -10V$, $I_{C} = -0.5A$, $f = 1kHz$	
Current Gain-Bandwidth Product	f _T	3			MHz	$I_C = -0.5A$, $V_{CE} = -10V$, $f = 100MHz$	
Output Capacitance	C _{obo}	1	185		pF	$V_{CB} = -10V$, $f = 1MHz$	
Input Capacitance	C _{ibo}	_	80	_	pF	$V_{EB} = -0.5V, f = 1MHz$	
Delay Time	t _d	_	3	_	ns		
Rise Time	t _r	_	209	_	ns	$I_C = -2A$, $V_{CC} = -10V$ $-I_{B1} = I_{B2} = -200mA$	
Storage Time	ts	_	183	_	ns		
Fall Time	t _f	_	623	_	ns		

Note: 9. Measured under pulsed conditions. Pulse width $\leq 300 \mu s$. Duty cycle $\leq 2\%$.



Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

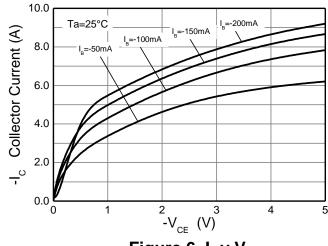


Figure 6. $I_{c} v V_{ce}$

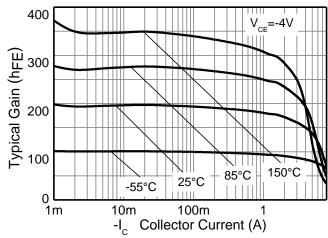


Figure 7. $h_{FE} v I_{C}$

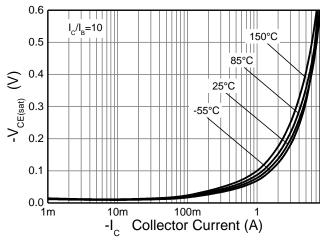


Figure 8. V_{CE(sat)} v I_C

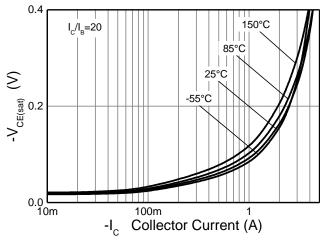
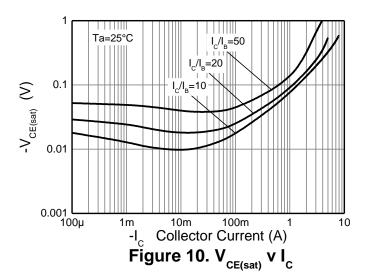


Figure 9. V_{CE(sat)} v I_C



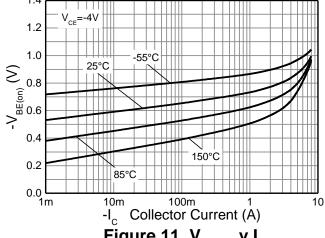


Figure 11. V_{BE(on)} v I_C



Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

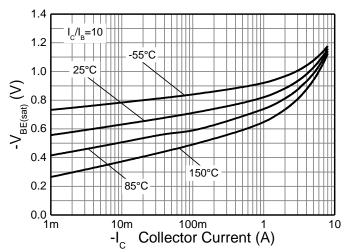


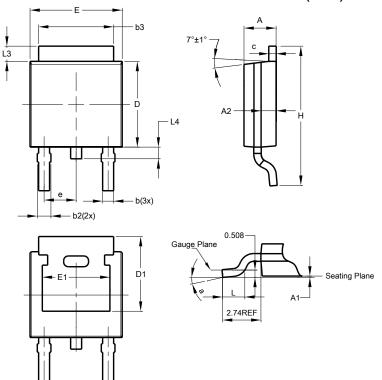
Figure 12. $V_{BE(sat)} v I_{C}$



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)

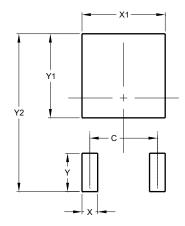


TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A 1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.50	5.33		
O	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21				
е	2.286 BSC				
Е	6.45	6.70	6.58		
E1	4.32				
Н	9.40	10.41	9.91		
٦	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°			
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)
С	4.572
Х	1.060
X1	5.632
Υ	2.600
Y1	5.700
Y2	10.700



IMPORTANT NOTICE

- 1. DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes' products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.
- 9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-and-conditions/important-notice

The Diodes logo is a registered trademark of Diodes Incorporated in the United States and other countries. All other trademarks are the property of their respective owners.

© 2023 Diodes Incorporated. All Rights Reserved.

www.diodes.com