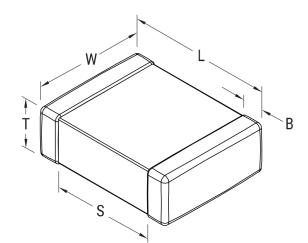


C0402T103K5RCCTU

Aliases (C0402T103K5RCC7867)

SMD COTS X7R, Ceramic, 0.01 uF, 10%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 0402



Click here for the 3D model.

Dimensions	
Chip Size	0402
L	1mm +/-0.05mm
W	0.5mm +/-0.05mm
Т	0.5mm +/-0.05mm
S	0.3mm MIN
В	0.3mm +/-0.1mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	10000

SMD COTS X7R
SIND COTS A/R
SMD Chip
SMD, MLCC, COTS, Temperature Stable, Class II
Temperature Stable, Class II
Yes
Tin
No
Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
No
1.21 mg
78 Weeks
1

Specifications	
Capacitance	0.01 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	100 GOhms

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