SQ3456CEV

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Automotive N-Channel 30 V (D-S) 175 °C MOSFET

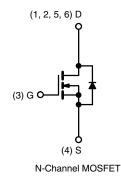


Marking Code: 9Gxxx

PRODUCT SUMMARY				
V _{DS} (V)	30			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.035			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0.052			
I _D (A)	7.8			
Configuration	Single			

FEATURES

- TrenchFET[®] power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





COMPLIANT HALOGEN

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3456CEV (for detailed order number please see <u>www.vishay.com/doc?79771</u>)

ABSOLUTE MAXIMUM RATING	GS (T _C = 25 °C, unless	otherwise noted	(k		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	30	V	
Gate-source voltage		V _{GS}	± 20	- V	
Continuous drain current	T _C = 25 °C	I _D -	7.8		
	T _C = 125 °C		4.5		
Continuous source current (diode conduction)		I _S	5	А	
Pulsed drain current ^a		I _{DM}	31		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	10		
Single pulse avalanche energy	L = 0.1 MH	E _{AS}	5	mJ	
Maximum power dissipation	T _C = 25 °C	PD	4	W	
	T _C = 125 °C		1.3	vv	
Operating junction and storage temperatur	e range	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction to ambient	PCB mount ^b	R _{thJA}	110	°C/W	
Junction to foot (drain)		R _{thJF}	38	0/10	

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$

b. When mounted on 1" square PCB (FR-4 material)

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static		•					1	
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA 30		-	-			
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μΑ	1.5	2.0	2.5	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	-	± 100	nA	
Zero gate voltage drain current		$V_{GS} = 0 V$	V _{DS} = 30 V	-	-	1		
	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 30 V, T _J = 125 °C	-	-	50	μA	
		$V_{GS} = 0 V$	V _{DS} = 30 V, T _J = 175 °C	-	-	150		
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	10	-	-	Α	
D		$V_{GS} = 10 V$	I _D = 6 A	-	0.028	0.035		
	P	$V_{GS} = 4.5 V$	I _D = 4.9 A	-	0.036	0.052	Ω	
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 6 A, T _J = 125 °C	-	-	0.054		
		V _{GS} = 10 V	I _D = 6 A, T _J = 175 °C	-	-	0.064		
Forward transconductance b	g _{fs}	V _{DS}	= 15 V, I _D = 5 A	-	21	-	S	
Dynamic ^b								
Input capacitance	C _{iss}		V _{DS} = 15 V, f = 1 MHz	-	295	370	pF	
Output capacitance	C _{oss}	$V_{GS} = 0 V$		-	73	85		
Reverse transfer capacitance	C _{rss}			-	25	35		
Total gate charge ^c	Qg		V _{DS} = 15 V, I _D = 6 A	-	6	10	nC	
Gate-source charge ^c	Q _{gs}	$V_{GS} = 10 V$		-	1.2	-		
Gate-drain charge ^c	Q _{gd}			-	1	-		
Gate resistance	Rg	f = 1 MHz		3.0	6.65	11	Ω	
Turn-on delay time ^c	t _{d(on)}			-	6	9		
Rise time ^c	t _r	$\label{eq:VDD} \begin{array}{l} V_{\text{DD}} = 15 \ \text{V}, \ R_{\text{L}} = 2.5 \ \Omega \\ I_{\text{D}} \cong \ 6 \ \text{A}, \ V_{\text{GEN}} = 10 \ \text{V}, \ R_{\text{g}} = 1 \ \Omega \end{array}$		-	12	18	ns	
Turn-off delay time ^c	t _{d(off)}			-	13	20		
Fall time ^c	t _f			-	8	12		
Source-Drain Diode Ratings and Charact	eristics ^b				•			
Pulsed current ^a	I _{SM}			-	-	31	Α	
Forward voltage	V _{SD}	I _F = 3 A, V _{GS} = 0 V		-	0.8	1.1	V	
Body diode reverse recovery time	t _{rr}			-	10	20	ns	
Body diode reverse recovery charge	Q _{rr}	I _F = 5 A, di/dt = 100 A/μs		-	5	10	nC	
Reverse recovery fall time	ta			-	7	-	ns	
Reverse recovery rise time	t _b			-	3	-		
Body diode peak reverse recovery current	I _{RM(REC)}			-	-0.88	-	A	

Notes

a. Pulse test; pulse width $\leq 300~\mu\text{s},~\text{duty}~\text{cycle} \leq 2~\%$

b. Guaranteed by design, not subject to production testing

c. Independent of operating temperature

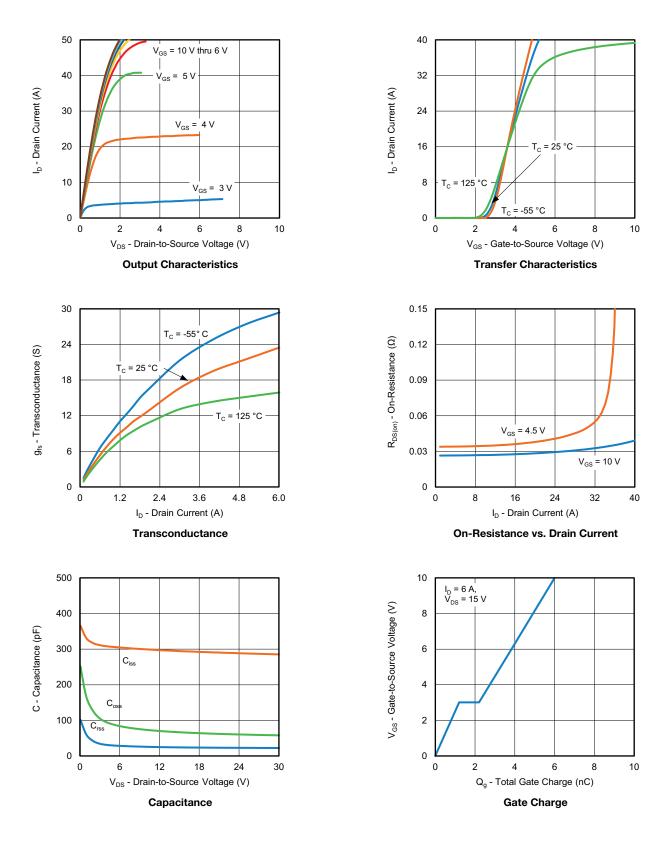
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



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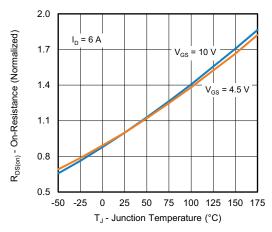
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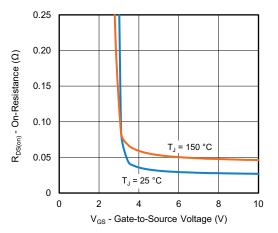
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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



On-Resistance vs. Junction Temperature

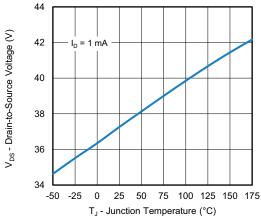


On-Resistance vs. Gate-to-Source Voltage



100

10



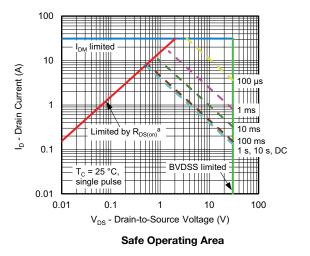
Drain Source Breakdown vs. Junction Temperature

4



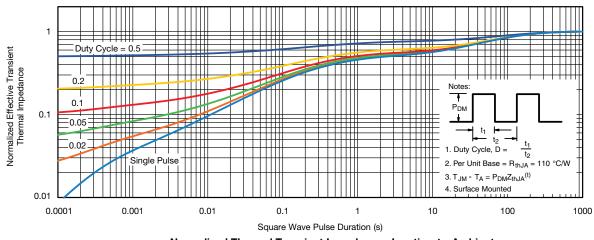
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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified



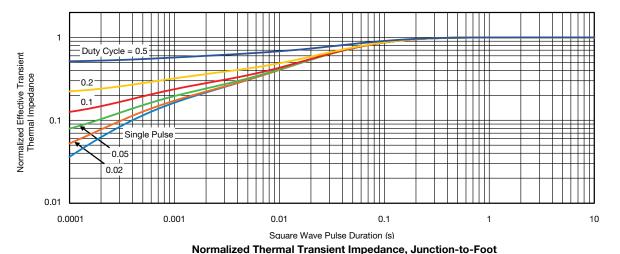
Normalized Thermal Transient Impedance, Junction-to-Ambient



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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Note

The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62060.



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