

C1812T105M1RCLTU

Aliases (C1812T105M1RCL7800)

SMD COTS X7R, Ceramic, 1 uF, 20%, 100 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1812



Click [here](#) for the 3D model.

| Dimensions | |
|------------|------------------|
| Chip Size | 1812 |
| L | 4.5mm +/-0.3mm |
| W | 3.2mm +/-0.3mm |
| T | 1.55mm +/-0.10mm |
| B | 0.6mm +/-0.35mm |

| Packaging Specifications | |
|--------------------------|--------------------------|
| Packaging | T&R, 180mm, Plastic Tape |
| Packaging Quantity | 1000 |

| General Information | |
|---------------------|--|
| Series | SMD COTS X7R |
| Style | SMD Chip |
| Description | SMD, MLCC, COTS, Temperature Stable, Class II |
| Features | Temperature Stable, Class II |
| RoHS | No |
| Prop 65 | ⚠ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov . |
| SCIP Number | 2d771165-5336-48a3-96fa-3663929fd828 |
| Termination | Lead (SnPb) |
| Marking | No |
| Failure Rate | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A |
| AEC-Q200 | No |
| Component Weight | 95 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Specifications | |
|--|---|
| Capacitance | 1 uF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Capacitance Tolerance | 20% |
| Voltage DC | 100 VDC |
| Dielectric Withstanding Voltage | 250 VDC |
| Temperature Range | -55/+125°C |
| Temperature Coefficient | X7R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms |
| Dissipation Factor | 2.5% 1kHz 1.0Vrms |
| Aging Rate | 3% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance | 1 GOhms |