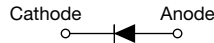


## Hyperfast Rectifier, 2 A FRED Pt<sup>®</sup>


**SMA (DO-214AC)**


### FEATURES

- Hyperfast recovery time, reduced  $Q_{rr}$ , and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
 COMPLIANT  
 HALOGEN  
**FREE**

### LINKS TO ADDITIONAL RESOURCES


[3D Models](#)

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2 A
$V_R$	100 V
$V_F$ at $I_F$	0.75 V
$t_{rr}$	25 ns
$T_J$ max.	175 °C
Package	SMA (DO-214AC)
Circuit configuration	Single

### DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers, and freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element.

### MECHANICAL DATA

**Case:** SMA (DO-214AC)

Molding compound meets UL 94 V-0 flammability rating

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

**Polarity:** color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		100	V
Average rectified forward current	$I_{F(AV)}$	$T_{Sp} = 138\text{ °C}$	2	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$ , 6 ms square pulse	50	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-55 to +175	°C



ELECTRICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	100	-	-	V
Forward voltage, per diode	V <sub>F</sub>	I <sub>F</sub> = 2 A I <sub>F</sub> = 2 A, T <sub>J</sub> = 125 °C	-	0.88 0.75	0.95 0.82	
Reverse leakage current, per diode	I <sub>R</sub>	5 T <sub>J</sub> = 125 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	- 0.6	2 8	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 100 V	-	8.5	-	pF

DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 1 A, di/dt = 50 A/μs, V <sub>R</sub> = 30 V	-	24	-	ns
		I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A	-	-	25	
		T <sub>J</sub> = 25 °C	-	16	-	
		T <sub>J</sub> = 125 °C	-	22	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	-	2	-	A
		T <sub>J</sub> = 125 °C	-	3	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	-	16	-	nC
		T <sub>J</sub> = 125 °C	-	30	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to lead	R <sub>thJL</sub>	Device mounted on PCB with 2 x 3.5 mm soldering lands	-	11	21	°C/W
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Device mounted on PCB with recommended pad size	-	-	125	°C/W
Approximate weight			0.07			g
			0.002			
Marking device		Case style SMA (DO-214AC)	2H1			

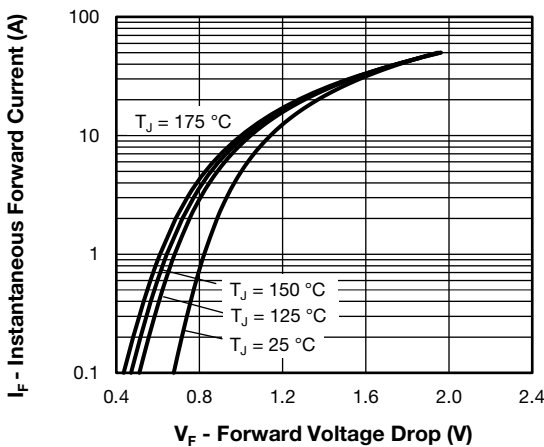


Fig. 1 - Typical Forward Voltage Drop Characteristics

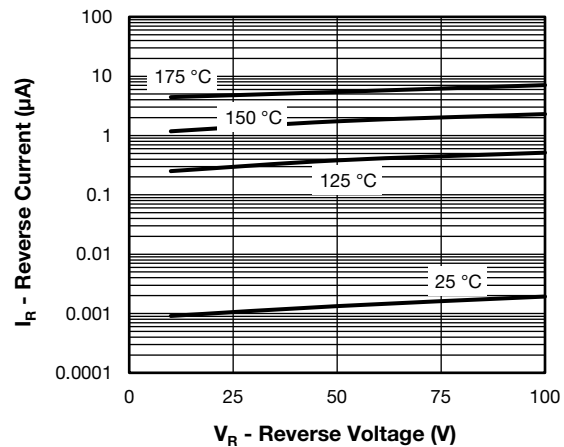


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

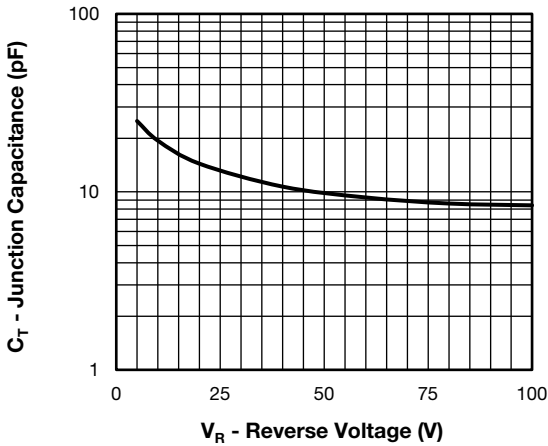


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

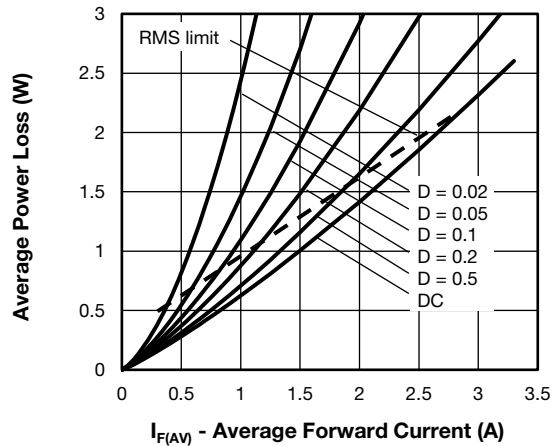


Fig. 5 - Forward Power Loss Characteristics

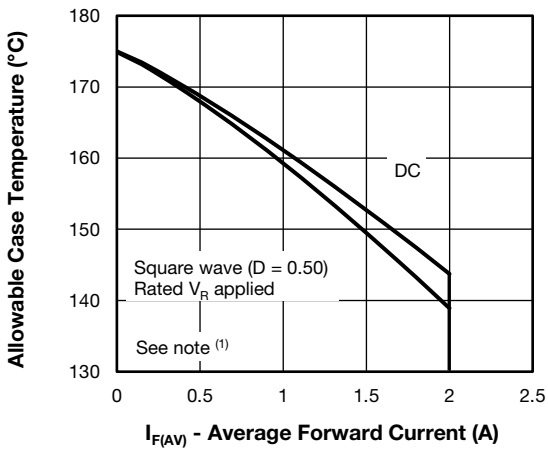


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

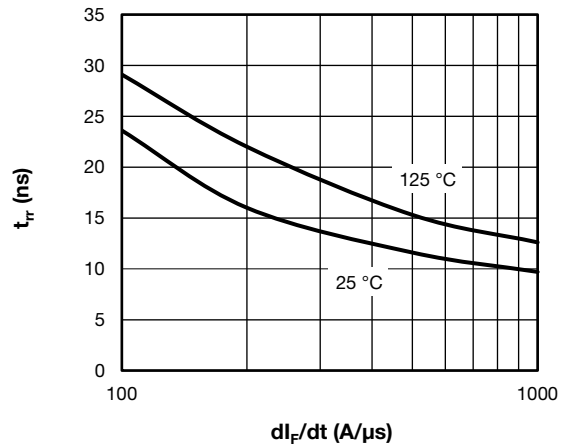


Fig. 6 - Typical Reverse Recovery Time vs.  $dI_F/dt$

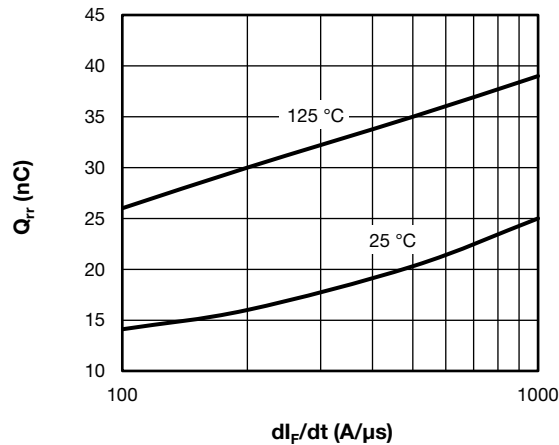


Fig. 7 - Typical Stored Charge vs.  $dI_F/dt$

**Note**

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;
- $P_d$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 5);
- $P_{dREV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$

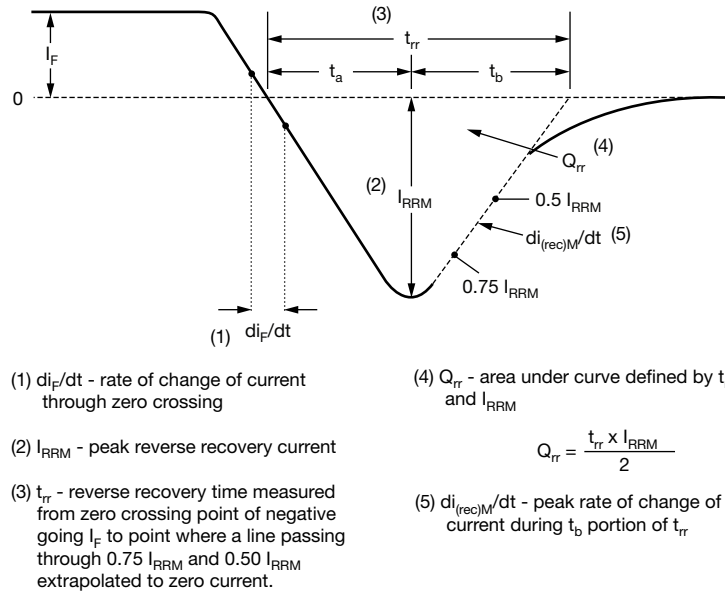


Fig. 8 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	<b>VS-</b>	<b>2</b>	<b>E</b>	<b>M</b>	<b>H</b>	<b>01</b>	<b>-M3</b>
	①	②	③	④	⑤	⑥	⑦
	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>	<b>7</b>
	-	-	-	-	-	-	-
	Vishay Semiconductors product	Current rating (2 = 2 A)	Circuit configuration: E = single diode	M = SMA package	Process type, H = hyperfast recovery	Voltage code (01 = 100 V)	-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-2EMH01-M3/5AT	7500	7500	13" diameter plastic tape and reel

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95400">www.vishay.com/doc?95400</a>
Part marking information	<a href="http://www.vishay.com/doc?95472">www.vishay.com/doc?95472</a>
Packaging information	<a href="http://www.vishay.com/doc?95404">www.vishay.com/doc?95404</a>
SPIICE model	<a href="http://www.vishay.com/doc?96376">www.vishay.com/doc?96376</a>

## SMA

**DIMENSIONS** in inches (millimeters)





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