

T494A226M010AH

T494

SMD Chip

Low ESR

Solder Coated

No

No

1

58.6 mg

156 Weeks

22 uF

20%

MnO2 Tantalum

SMD, MnO2, Molded, Low ESR

http://www.p65warnings.ca.gov.

MARNING: Cancer and reproductive harm -

1dd2e1b8-26dd-4d52-927c-6f9d519011aa

10 VDC (85C), 6.7 VDC (125C)

General Information

Series

Style

Dielectric

Description

Features

RoHS

Prop 65

SCIP Number

Termination

AEC-Q200

Component

Specifications

Capacitance Capacitance

Tolerance Voltage DC

Weight Shelf Life

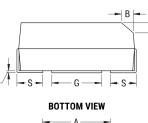
MSL

T494, Tantalum, MnO2 Tantalum, 22 uF, 20%, 10 VDC, SMD, MnO2, Molded, Low ESR, 4.5 Ohms, 3216, Height Max = 1.8mm

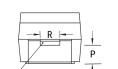
CATHODE (-) END VIEW

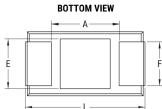


ANODE (+) END VIEW



SIDE VIEW





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	oit	hor	en	h	

Click here for the 3D model.

Dimensions					
Footprint	3216				
L	3.2mm +/-0.2mm				
W	1.6mm +/-0.2mm				
Н	1.6mm +/-0.2mm				
Т	0.13mm REF				
S	0.8mm +0.2/-0.3mm				
F	1.2mm +/-0.1mm				
А	1.2mm MIN				
В	0.4mm +/-0.15mm				
E	1.3mm REF				
G	1.1mm REF				
Р	0.4mm REF				
R	0.4mm REF				
Х	0.1mm +/-0.1mm				

Т

F 2/-0.3mm	Temperature Range	-55/+125°C
.1mm	Rated Temperature	85°C
	Dissipation Factor	10% 120Hz 25C
0.15mm	Failure Rate	N/A
	Resistance	4.5 Ohms (100kHz 25C)
-	Ripple Current	129 mA (rms, 100kHz 25C), 116.1 mA (rms, 85C), 51.6 mA (rms, 125C)
-	Leakage Current	2.2 uA (5min 25°C)

Packaging Specifications			
Packaging	T&R, 178mm		
Packaging Quantity	2000		

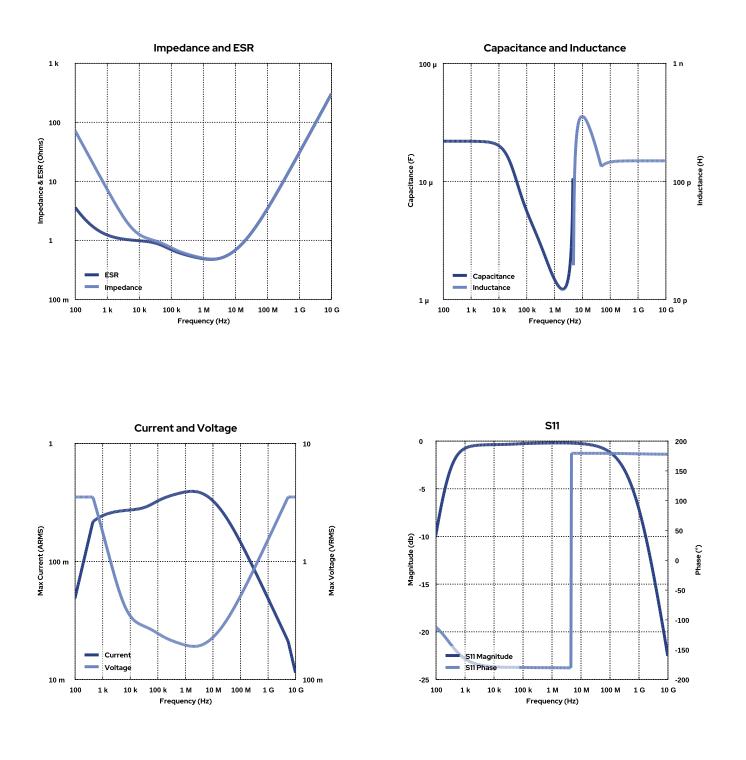
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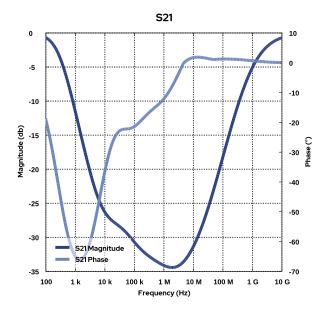
Simulations

For the complete simulation environment please visit K-SIM.





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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other
- harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.