PAC1921 Evaluation Board

Design Details

Revision History				
Rev	ECO	Description	Date	Approved
1.0		Initial Release	01-12-12	
2.0		Conversion to Altium, add MCHP micro	02-03-14	

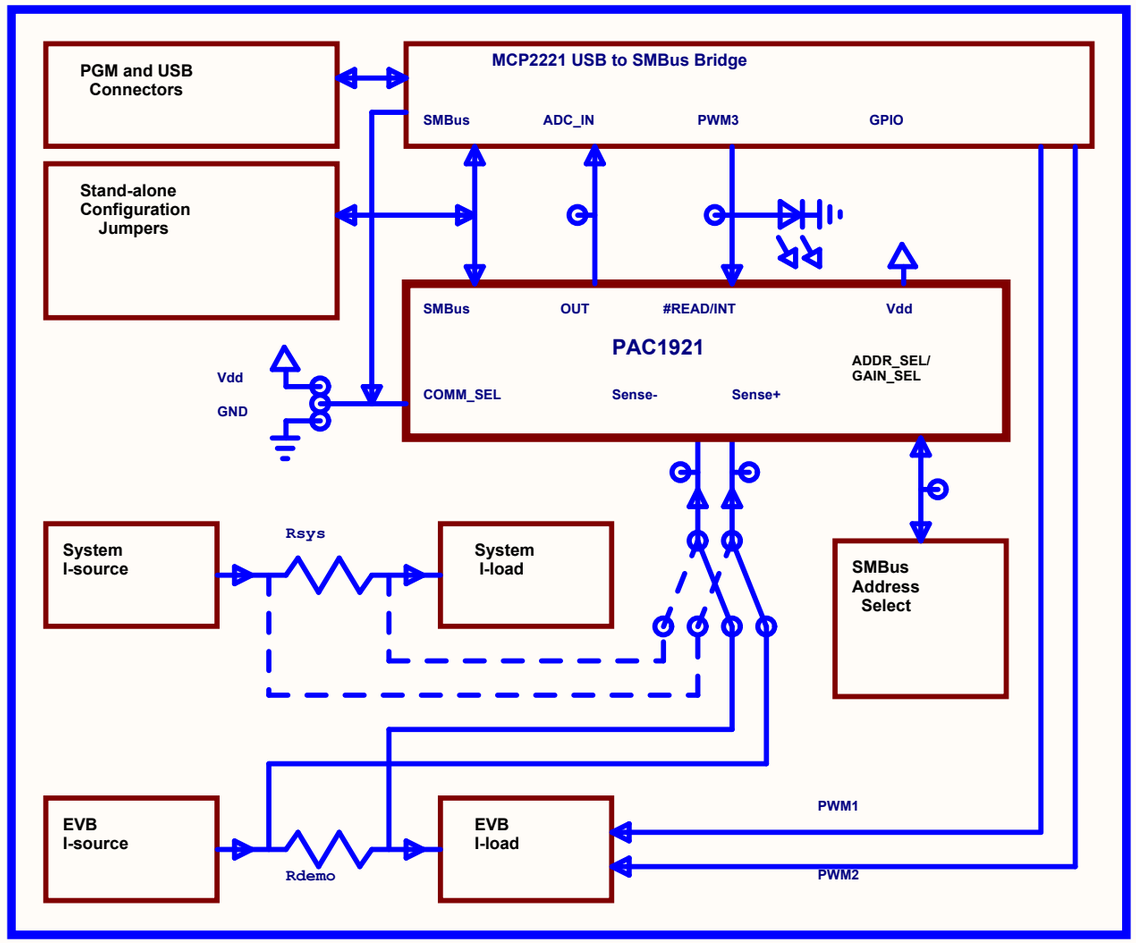
Assemblies:
PAC1921 Eval Board Rev B

Board:
8166_B0_PAC1921EVB
Customer EVB 2.0" x 2.5" x 0.063"
2 Layer Board

Chip:
PAC1921

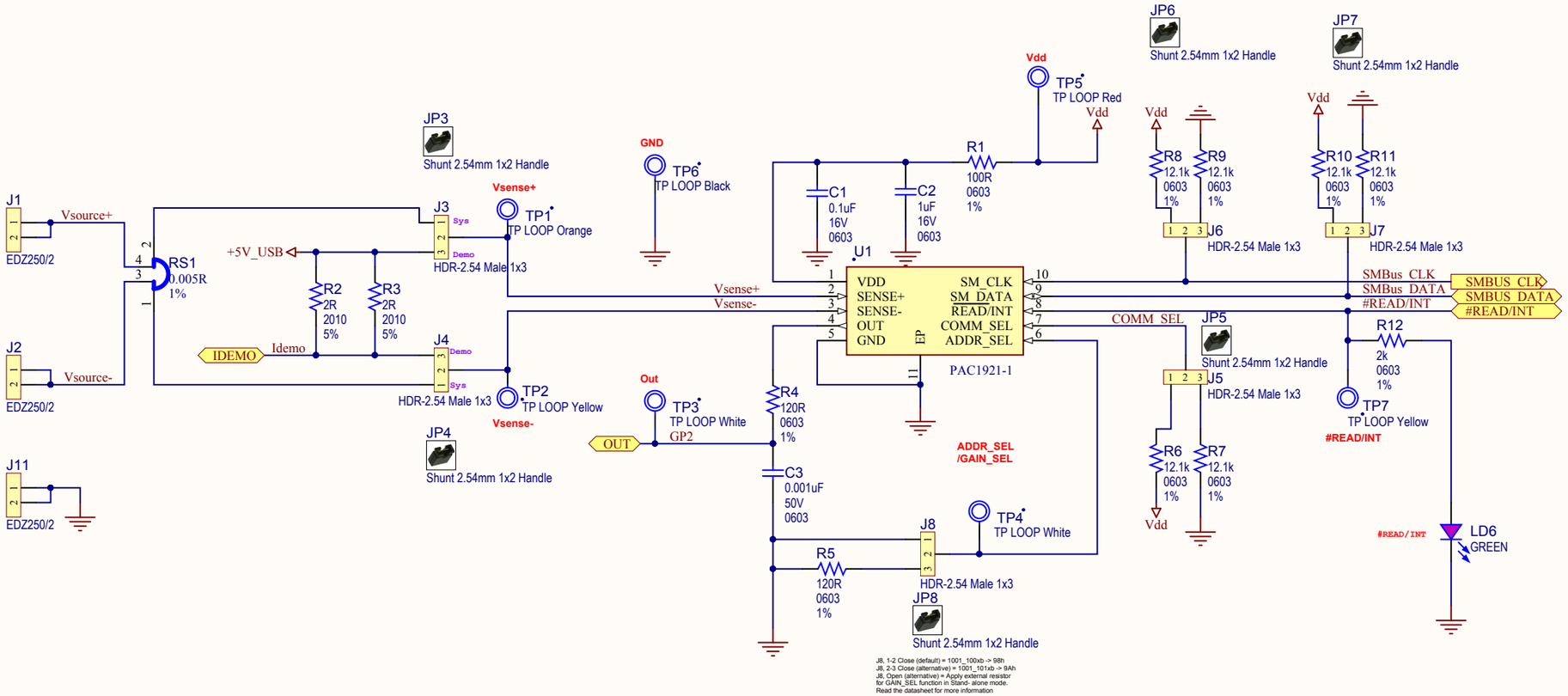
Package:
DFN-10

System Block Diagram

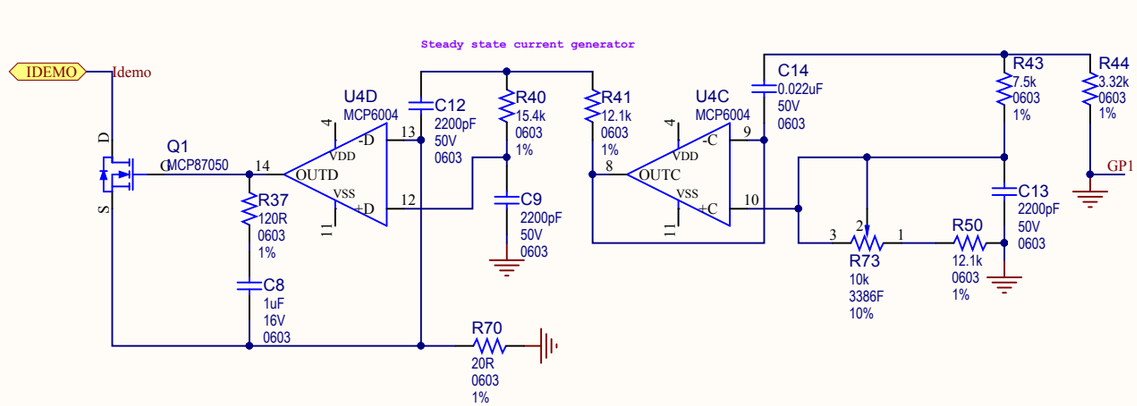


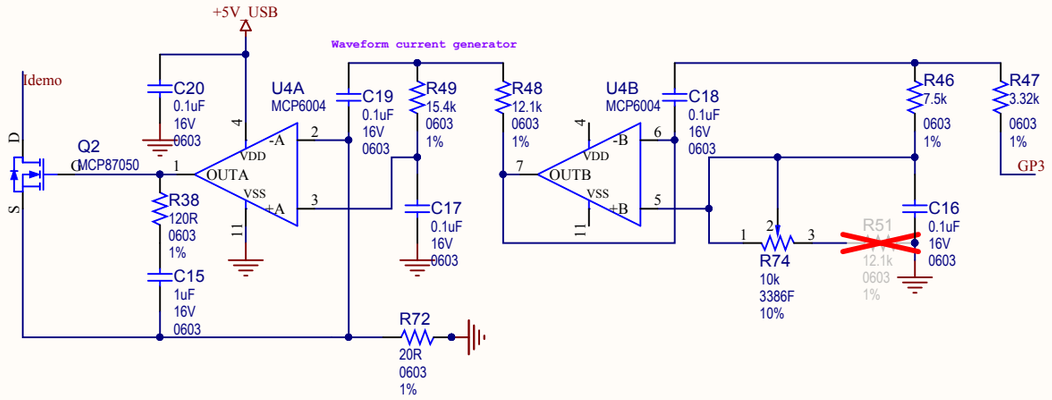
Schematic Page Description	Page #
Title Page	1
PAC1921 Device	2
Test Block, USB Interface	3

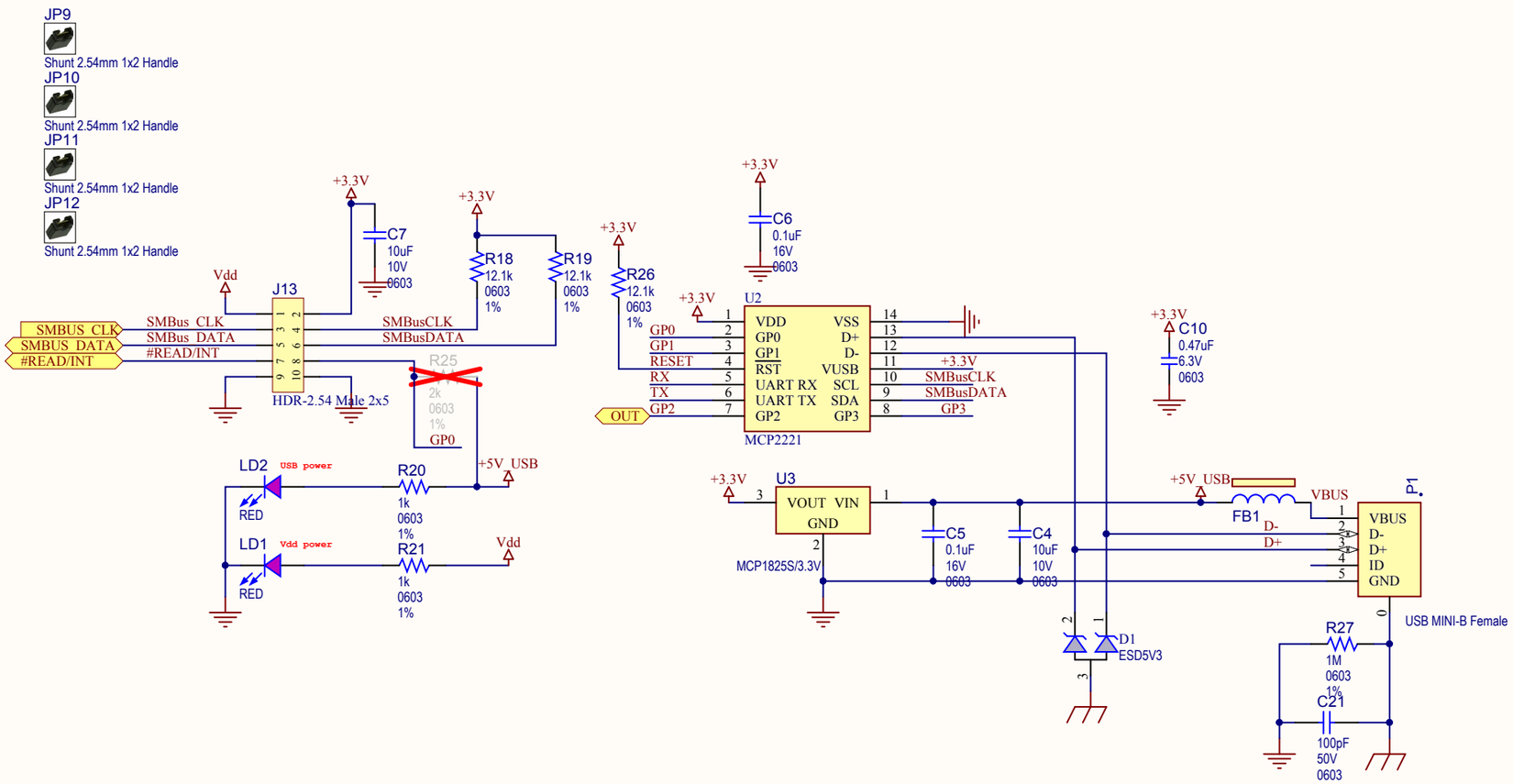
Drawn By: Lenard Milholland	Date: 3/30/2015	MICROCHIP	
Checked: *	Date: 2/3/2014		
Approved: *	Date: 2/3/2014	Title: *PAC1921 Eval Board*	
Sheet Name: 03-10305	Size: B	Number: ADM00592	Revision: 3
Engineer: Lynn Kern	Date: 3/30/2015 Time: 1:01:24 PM	Sheet 1 of 3	Designed with Altium Altium.com
	File: System Diagram.SchDoc		



Drawn By: Wayne Little	MICROCHIP
Engineer: Wayne Little	
PartNumber: ADM00592	Project Title: *PAC1921 Power Monitor EVB*
Sheet Title: PAC1921 Current Shunt and Power Monitor	Designed with Altium — Altium.com
Size: B / Sch #:03-10305 / Revision: 3	Date: 3/30/2015 1:01:24 PM / Sheet 1 of 2
File: PAC1921 Power Monitor.SchDoc	







Drawn By: Wayne Little	MICROCHIP
Engineer: Wayne Little	
PartNumber: ADM00592	Project Title: *PAC1921 Power Monitor EVB*
Sheet Title: USB Bridge and Sig Gen	Designed with Altium — Altium.com
Size: B	Sch #:03-10305 · Date: 3/30/2015 1:01:24 PM · Revision:3 · Sheet 2 of 2
File: Signal Generator and USB Bridge.SchDoc	