

T491C156K025ATAUTO

T491 Auto, Tantalum, MnO2 Tantalum, 15 uF, 10%, 25 VDC, SMD, MnO2, Molded, Auto, AEC-Q200, 1.5 Ohms, 6032, Height Max = 2.8mm

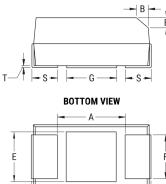
CATHODE (-) END VIEW



ANODE (+) END VIEW

R

Ρ



SIDE VIEW

General Information			
Series	T491 Auto		
Dielectric	MnO2 Tantalum		
Style	SMD Chip		
Description	SMD, MnO2, Molded, Auto, AEC-Q200		
Features	Automotive		
RoHS	Yes		
Termination	Tin		
Qualifications	AEC-Q200		
AEC-Q200	Yes		
Component Weight	224.48 mg		

15 uF
10%
25 VDC (85C), 16.75 VDC (125C)
-55/+125°C
85°C
6% 120Hz 25C
N/A
1.5 Ohms (100kHz 25C)
271 mA (rms, 100kHz 25C)
3.8 uA (5min 25°C)

Click	here	for	the	3D	model.

Termination cutout at KEMET's option, either end

Dimensions	
Footprint	6032
L	6mm +/-0.3mm
W	3.2mm +/-0.3mm
Н	2.5mm +/-0.3mm
Т	0.13mm REF
S	1.3mm +/-0.3mm
F	2.2mm +/-0.1mm
А	2.9mm MIN
В	0.5mm +/-0.15mm
E	2.4mm REF
G	2.8mm REF
Ρ	0.9mm REF
R	1mm REF
Х	0.1mm +/-0.1mm

Packaging Specifications				
Packaging	T&R, 178mm			
Packaging Quantity	500			

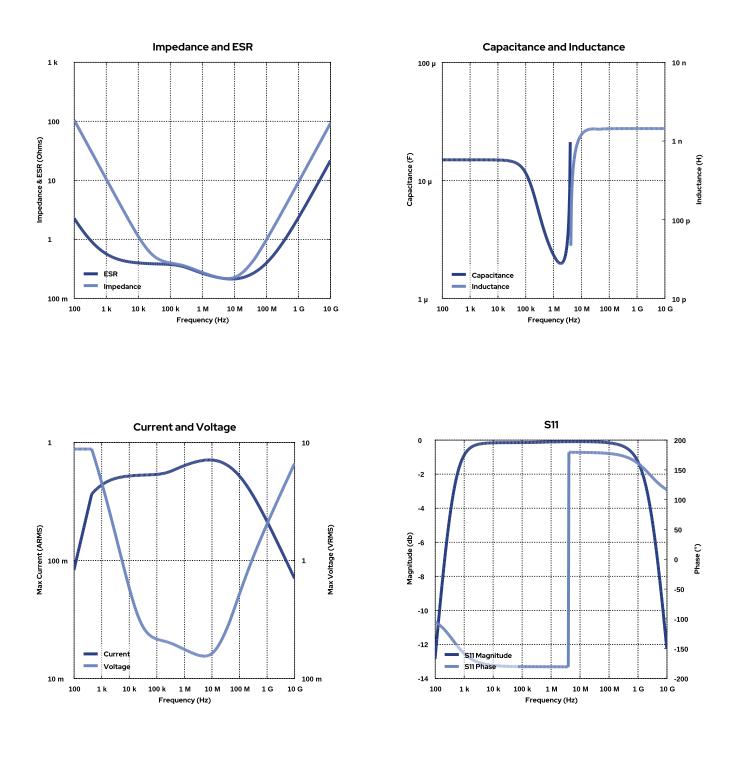
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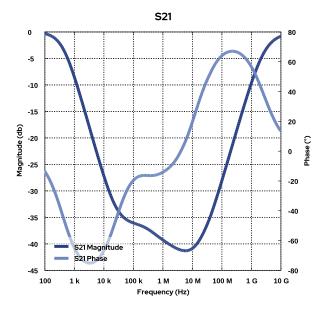
Simulations

For the complete simulation environment please visit K-SIM.





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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other
- harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.