

# Product/process change notification

**PCN224302**

Please find attached our Infineon Technologies AG PCN:

## 2Gb Octal SPI Semper Flash Rev \*C silicon qualification and datasheet changes

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before **07 Dec 2022**
- Infineon aligns with the widely-recognized JEDEC STANDARD “**JESD46**“, which stipulates:  
“**Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change.**”

Your prompt reply will help Infineon Technologies to assure a smooth and well executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.



On 16 April 2020, Infineon acquired Cypress.  
We are now in the process of merging and consolidating our tools and processes for PCN, Information Notes, Errata and Product Discontinuance.  
For further details, please visit our website:  
<https://www.infineon.com/cms/en/about-infineon/company/cypress-acquisition/>

► **Products affected:** Please refer to attached affected product list [4]

► **Detailed change information:**

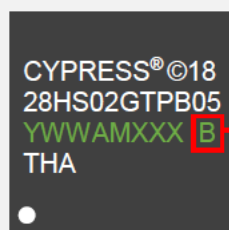
**Subject:** Qualification of Rev \*C Silicon for 2Gb Octal SPI Semper Flash family and related datasheet changes.

**Reason:** Rev \*C silicon fixes the errata impacting 2Gb Octal SPI Semper Flash products on Rev \*A silicon.

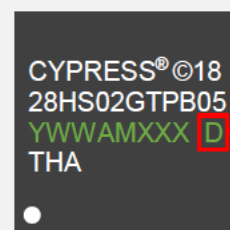
Description:	<u>Old</u>	<u>New</u>
	<ul style="list-style-type: none"> <li>■ Errata impacts Rev *A silicon               <ul style="list-style-type: none"> <li>○ Continuous Read across lower addressed Die to upper address Die boundary fails during SDR reads (1S-1S-1S) and (1S-1S-4S) without Latency Cycles and Mode Bits using commands (03h, 13h, 0Bh, 6Bh, 6Ch)</li> </ul> </li> <li>■ Rev *A silicon parameters: No change</li> </ul>	<ul style="list-style-type: none"> <li>■ Qualified a metal fix (Rev *C) to address the Continuous Read across die boundary read failure</li> <li>■ Rev *C silicon parameters: Changes in following specifications               <ul style="list-style-type: none"> <li>○ Standby Current (I<sub>SB</sub>)</li> <li>○ DPD Current (I<sub>DPD</sub>)</li> <li>○ Refer attachment 2GbSemper_SpecChanges.xlsx</li> </ul> </li> </ul>

► **Product identification:** Marking Die Code will be used to differentiate the silicon revision:

Device	OLD (REV *A)	NEW (REV *C)
2Gb Octal SPI Auto (PBO) Temp: B, M	Die Code "B"	Die Code "D"



— Mark Die Code



— Mark Die Code

► **Impact of change:** Based on the qualification performed, Infineon does not see any negative impact on quality, function and reliability.

► **Attachments:**

Affected product list [4]  
Supporting documents

► **Time schedule:**

- Final qualification report: available
- First samples available: on request
- Intended start of delivery: 2022-12-08

If you have any questions, please do not hesitate to contact your local sales office.

**[Product Change Notification] N° [PCN224302]**

2Gb Octal SPI Semper Flash Rev \*C silicon qualification and datasheet changes



Item	Marketing Part Number	Family	Sample Order Part Number	Sample Availability
1	S28HS02GTFPBHB050	NOR	S28HS02GTFPBHB050-011	on request
2	S28HS02GTFPBHB053	NOR	S28HS02GTFPBHB050-011	on request
3	S28HS02GTFPBHM050	NOR	S28HS02GTFPBHM050-011	on request
4	S28HS02GTFPBHM053	NOR	S28HS02GTFPBHM050-011	on request