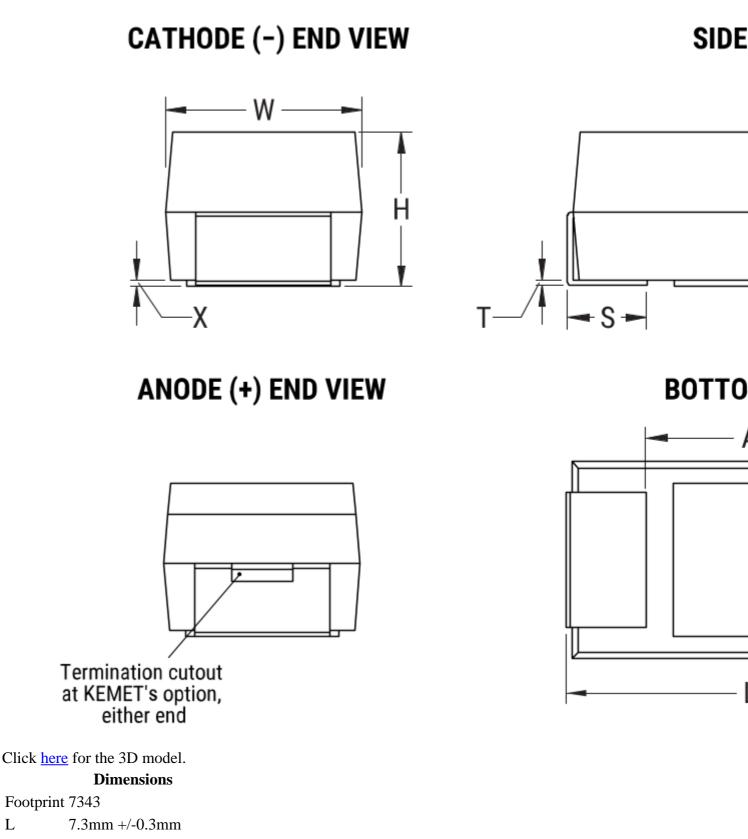
T521V476M016AHE055

T521, Tantalum, Polymer Tantalum, 47 uF, 20%, 16 VDC, SMD, Polymer, Molded, Low Profile/ESR, NonCombustible, 55 mOhms, 7343, Height Max = 2mm



W 4.3mm +/-0.3mm

H 1.8mm +/-0.2mm

Dimensions

- 0.13mm REF
- S 1.3mm +/-0.3mm
- F 2.4mm +/-0.1mm
- A 3.6mm MIN

Т

X 0.05mm REF

Packaging Specifications

Packaging T&R, 178mm Packaging Quantity 1000

General Information

Series	T521
Dielectric	Polymer Tantalum
Style	SMD Chip
Description	SMD, Polymer, Molded, Low Profile/ESR, NonCombustible
Features	Low ESR, High Voltage
RoHS	No
Prop 65	WARNING: Cancer and reproductive harm - <u>http://www.p65warnings.ca.gov</u> .
SCIP Number	b064b03e-bd75-42af-b342-1fe94dec2340
Termination	Solder Coated
AEC-Q200	No
Component Weight	274.3 mg
Shelf Life	52 Weeks
MSL	3
	Specifications
	-
Capacitance	47 uF
Capacitance Capacitance Tolerance	47 uF 20%
Capacitance	
Capacitance Tolerance	20% 16 VDC (105C), 10.72 VDC (125C)
Capacitance Tolerance Voltage DC	20% 16 VDC (105C), 10.72 VDC (125C) ge -55/+125°C
Capacitance Tolerance Voltage DC Temperature Ran	20% 16 VDC (105C), 10.72 VDC (125C) ge -55/+125°C
Capacitance Tolerance Voltage DC Temperature Ran Rated Temperature	20% 16 VDC (105C), 10.72 VDC (125C) ge -55/+125°C re 105°C
Capacitance Tolerance Voltage DC Temperature Ran Rated Temperatur Life	20% 16 VDC (105C), 10.72 VDC (125C) ge -55/+125°C re 105°C 2000 Hrs (125C) 60C, 90% RH, 500 Hours, No Load
Capacitance Tolerance Voltage DC Temperature Ran Rated Temperatur Life Humidity	20% 16 VDC (105C), 10.72 VDC (125C) ge -55/+125°C re 105°C 2000 Hrs (125C) 60C, 90% RH, 500 Hours, No Load
Capacitance Tolerance Voltage DC Temperature Ran Rated Temperatur Life Humidity Dissipation Facto	20% 16 VDC (105C), 10.72 VDC (125C) ge -55/+125°C re 105°C 2000 Hrs (125C) 60C, 90% RH, 500 Hours, No Load r 10% 120Hz 25C N/A 55 mOhms (100kHz 25C)
Capacitance Tolerance Voltage DC Temperature Ran, Rated Temperatur Life Humidity Dissipation Facto Failure Rate	20% 16 VDC (105C), 10.72 VDC (125C) ge -55/+125°C re 105°C 2000 Hrs (125C) 60C, 90% RH, 500 Hours, No Load r 10% 120Hz 25C N/A

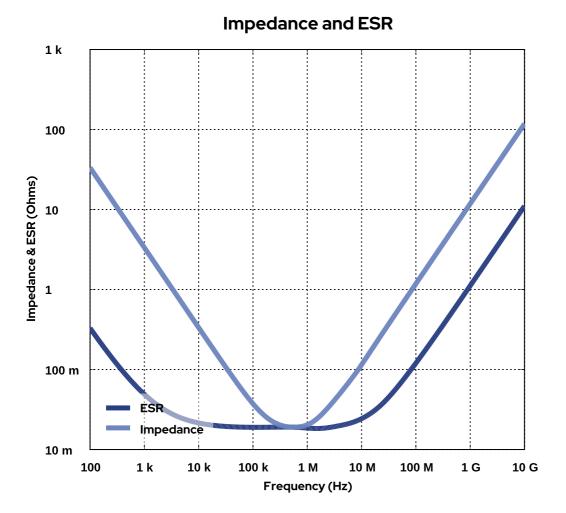
Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference

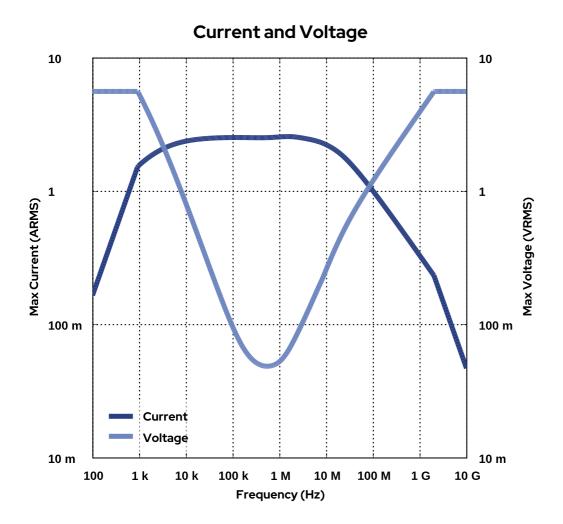
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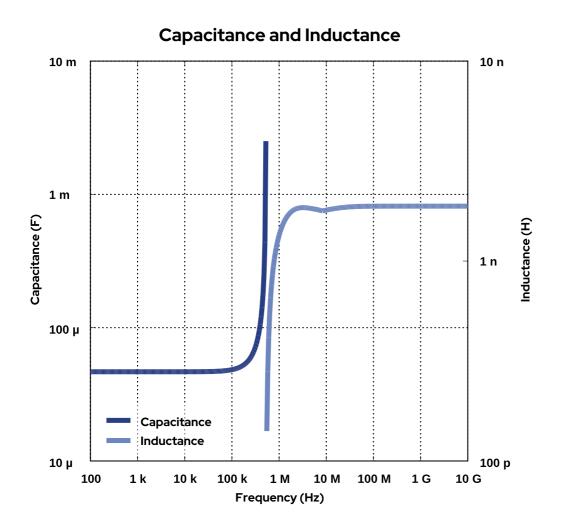
Generated 5/18/2023 - f16f6353-7e24-40ac-ae15-8ad5767c8858 © 2006 - 2023 KEMET Generated 5/18/2023 - f16f6353-7e24-40ac-ae15-8ad5767c8858 © 2006 - 2023 KEMET

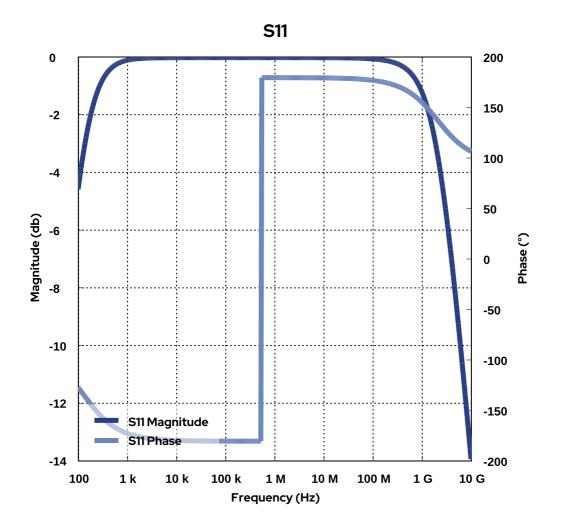
Simulations

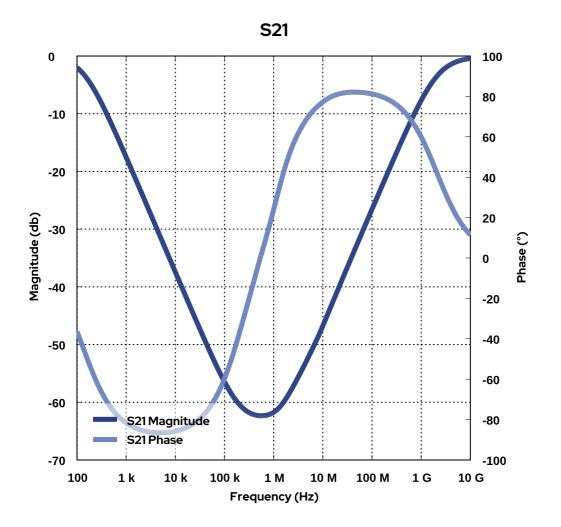
For the complete simulation environment please visit K-SIM.











These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
- The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are

calculated for each frequency and are not combined with voltages generated at any other harmonics.

• Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

All Information given herein is believed to be accurate and reliable, but is presented without guarantee, warranty, or responsibility of any kind, expressed or implied. Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute – and we specifically disclaim – any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

If you have any questions please contact K-SIM.