

Click here for the 3D model.

| Dimensions |  |
| :--- | :--- |
| Chip Size | 2220 |
| L | $5.7 \mathrm{~mm}+/-0.4 \mathrm{~mm}$ |
| W | $5 \mathrm{~mm}+/-0.4 \mathrm{~mm}$ |
| T | $2.4 \mathrm{~mm}+/-0.15 \mathrm{~mm}$ |
| B | $0.6 \mathrm{~mm}+/-0.35 \mathrm{~mm}$ |
|  |  |
| Packaging Specifications | T\&R, 180 mm, Plastic Tape |
| Packaging | 500 |
| Packaging Quantity |  |

C2220T106K5RCCTU
Aliases (C2220T106K5RCC7800)
SMD COTS X7R, Ceramic, 10 uF, 10\%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 2220

| General Information |  |
| :--- | :--- |
| Series | SMD COTS X7R |
| Style | SMD Chip |
| Description | SMD, MLCC, COTS, Temperature Stable, Class II |
| Features | Temperature Stable, Class II |
| RoHS | Yes |
| Termination | Tin |
| Marking | No |
| Failure Rate | Testing per MIL-PRF-55681 PDA 8\%, DPA per <br> EIA-469, Humidity per MIL-STD-202, Method 103, <br> Condition A |
| AEC-Q200 | No |
| Component <br> Weight | 390 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |


| Specifications |  |
| :--- | :--- |
| Capacitance | 10 uF |
| Measurement Condition | $10 \%$ |
| Capacitance Tolerance | 50 VDC |
| Voltage DC | 125 VDC |
| Dielectric Withstanding Voltage | $-55 /+125^{\circ} \mathrm{C}$ |
| Temperature Range | $\mathrm{X7R}$ |
| Temperature Coefficient | $15 \%, 1 \mathrm{kHz} 1.0 \mathrm{Vrms}$ |
| Capacitance Change with Reference <br> to $+25^{\circ} \mathrm{C}$ and O VDC Applied (TCC) | $2.5 \% 1 \mathrm{kHz} 1.0 \mathrm{Vrms}$ |
| Dissipation Factor | $3 \%$ Loss/Decade Hour: |
| Aging Rate | 50 MOhms |
| Insulation Resistance |  |

