3.3V
C1
0.1uF
GND

U1
VDDM VCC1
      I/O[0]
      I/O[1]
      I/O[2]
      I/O[3]
      I/O[4]
RESET I/O[5]
      I/O[6]
SDA   I/O[7]
SCL

INT   VCC2
      I/O[8]
      I/O[9]
OSCIO I/O[10]
      I/O[11]
ADDR0 I/O[12]
ADDR1 I/O[13]
      I/O[14]
GND   I/O[15]
SX1509

3.3V
3.3V
R3 10k   SJ5   R2 10k   R4 10k   R1 10k

NRESET 23
SDA 24
SCL 25
NINT 9
OSCIO 11
ADDR0 26
ADDR1 10
3*3 GND

VCC1 SJ3 3.3V
C3 0.1uF GND
VCC2 SJ4 3.3V
C2 0.1uF GND

JP7
3.3V
7
6 NINT
5 OSCIO
4 SCL
3 SDA
2 NRESET
1
GND

3.3V   3.3V
SJ2   SJ1
GND

JP5
11
10
9
8
7
6
5
4
3 VCC1
2 3.3V
1
GND

JP1 IDC-5X2
JP2 IDC-5×2

JP6
GND

I2C Addresses [ADDR1][ADDR0]:
00 - 0x3E (default)
01 - 0x3F
10 - 0x70
11 - 0x71

v10 Changes
  Moved VCC1/2 decoupling caps to better fit IDC connector
  Added jumpers to SDA and SCL pull-ups

Jumpers!
  All jumpers come configured as shown in the schematic
  The address jumpers (ADDR0/1) both come set to ground
  The VCC1/2 jumpers set those pins to 3.3V
  The I2C jumpers enable the pull-ups on SDA and SCL

open hardware

TITLE: sx1509-breakout   SFE   SFE
Design by:
Jim Lindblom   REV:
Date: 12/20/2012 11:12:37 AM   Sheet: 1/1