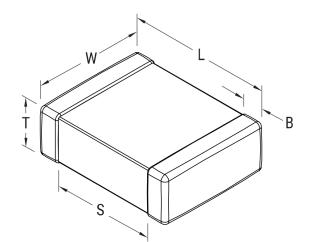


C0402T101J1GACTU

Aliases (C0402T101J1GAC7867)

SMD COTS COG, Ceramic, 100 pF, 5%, 100 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0402



Click here for the 3D model.

Chip Size 0402 L 1mm +/-0.05mm W 0.5mm +/-0.05mm T 0.5mm +/-0.05mm
W 0.5mm +/-0.05mm
T 0.5mm +/-0.05mm
S 0.3mm MIN
B 0.3mm +/-0.1mm

Packaging Specifications	
Packaging	T&R, 180r
Packaging Quantity	10000

T&R, 180mm, Paper Tape 10000

General Information	
Series	SMD COTS COG
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%
AEC-Q200	No
Component Weight	1.06 mg
Shelf Life	78 Weeks
MSL	1

Specifications	/
Capacitance	100 pF
Measurement Condition	1 MHz 1.0Vrms
Capacitance Tolerance	5%
Voltage DC	100 VDC
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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