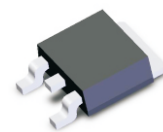


CMS35P06D-HF

P-Channel
RoHS Device
Halogen Free



Features

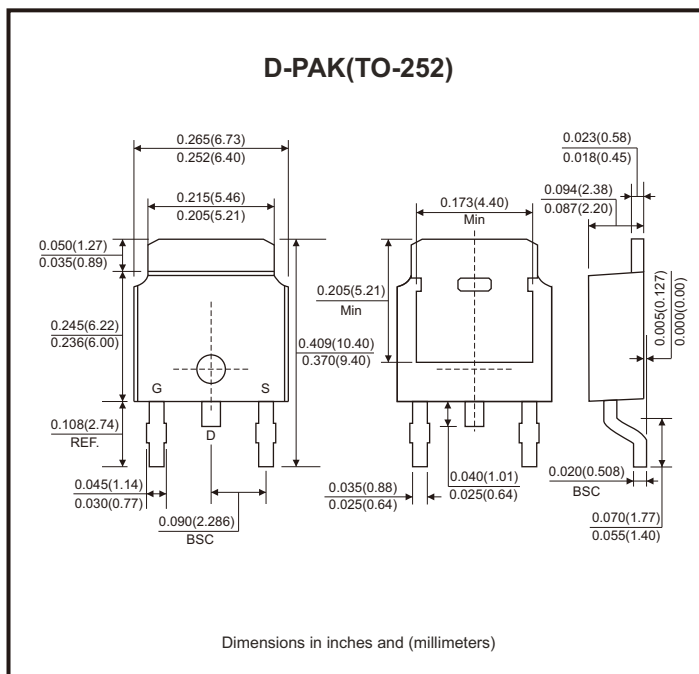
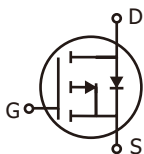
- Advanced DMOS trench technology.
- Fast switching.
- Green device available.
- 100% EAS guaranteed.

Mechanical data

- Case: D-PAK(TO-252) standard package, molded plastic.

Circuit Diagram

- G : Gate
- S : Source
- D : Drain



Maximum Ratings

Parameter	Conditions	Symbol	Value	Unit
Drain-source voltage		V_{DS}	-60	V
Gate-source voltage		V_{GS}	±20	V
Continuous drain current (Note 1)	$I_D @ T_C = 25^\circ C$		-35	A
	$I_D @ T_C = 100^\circ C$		-22.1	
Pulsed drain current (Note 1, 2)		I_{DM}	-140	A
Total power dissipation (Note 4)	$P_D @ T_C = 25^\circ C$		72.6	W
	$P_D @ T_A = 25^\circ C$		2	
Single pulse avalanche energy, L=0.1mH (Note 3)		E_{AS}	80	mJ
Single pulse avalanche current, L=0.1mH (Note 3)		I_{AS}	-40	A
Operating junction and storage temperature range		T_J, T_{STG}	-55 to +150	°C
Thermal resistance junction-ambient (Note 1)	Steady state	$R_{\theta JA}$	62.5	°C/W
Thermal resistance junction-case (Note 1)	Steady state	$R_{\theta JC}$	1.72	°C/W

Electrical Characteristics (at $T_j=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-source breakdown voltage	BV_{DSS}	$V_{GS} = 0V, I_D = -250\mu A$	-60			V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.0	-1.4	-2.5	
Gate-source leakage current	I_{GSS}	$V_{GS} = \pm 20V$			± 100	nA
Drain-source leakage current ($T_j=25^\circ\text{C}$)	I_{DSS}	$V_{DS} = -60V, V_{GS} = 0V$			-1	μA
Drain-source leakage current ($T_j=125^\circ\text{C}$)		$V_{DS} = -48V, V_{GS} = 0V$			-10	
Static drain-source on-resistance (Note 2)	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -20A$		23	28	m Ω
		$V_{GS} = -4.5V, I_D = -10A$		28	35	
Total gate charge (Note 2)	Q_g	$I_D = -5A, V_{DS} = -30V, V_{GS} = -10V$		43.8		nC
Gate-source charge	Q_{gs}			4.6		
Gate-drain ("miller") charge	Q_{gd}			8.3		
Turn-on delay time (Note 2)	$t_{d(on)}$	$V_{DD} = -30V, V_{GS} = -10V$ $I_D = -1A, R_G = 6\Omega$		25		nS
Rise time	t_r			13.8		
Turn-off delay time	$t_{d(off)}$			148		
Fall time	t_f			51		
Input capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = -25V, f = 1MHz$		2595		μF
Output capacitance	C_{oss}			162		
Reverse transfer capacitance	C_{rss}			115		
Source-drain diode						
Diode forward voltage (Note 2)	V_{SD}	$I_S = -20A, V_{GS} = 0V, T_j=25^\circ\text{C}$			-1.2	V
Continuous source current (Note 1, 6)	I_S	$V_G = V_D = 0V, \text{Force current}$			-35	A
Pulsed source current (Note 2, 6)	I_{SM}				-70	A
Guaranteed avalanche characteristics						
Single pulse avalanche energy (Note 5)	EAS	$V_{DD} = -25V, L=0.1mH, I_{AS} = -20A$	20			mJ

- Notes: 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2 oz copper.
 2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
 3. The EAS data shows max. rating. The test condition is $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-40A$.
 4. The power dissipation is limited by 150°C junction temperature.
 5. The min. value is 100% EAS tested guarantee.
 6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

Rating and Characteristic Curves (CMS35P06D-HF)

Fig.1 - Drain Current vs. T_c

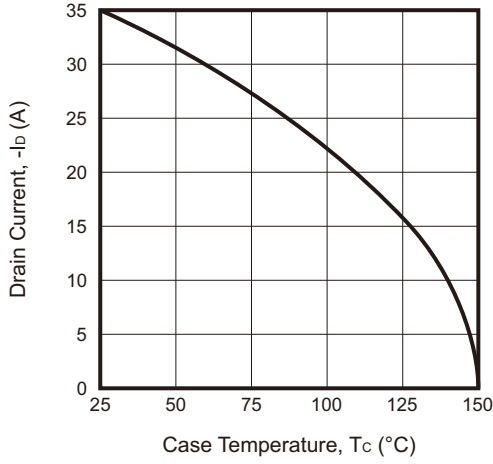


Fig.2 - Gate Charge Characteristics

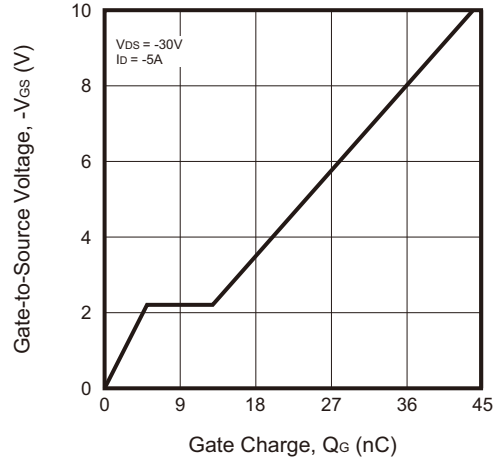


Fig.3 - Normalized $V_{GS(th)}$ vs. T_J

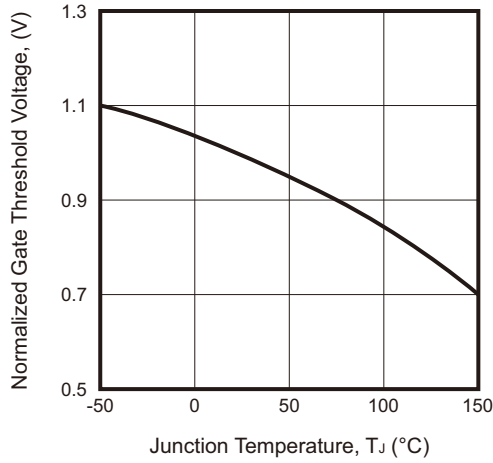


Fig.4 - Normalized $R_{DS(on)}$ vs. T_J

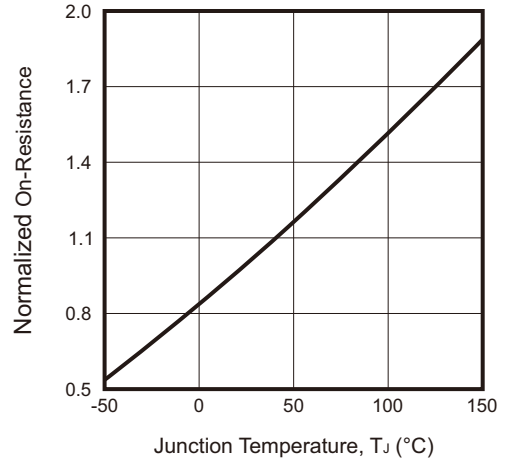
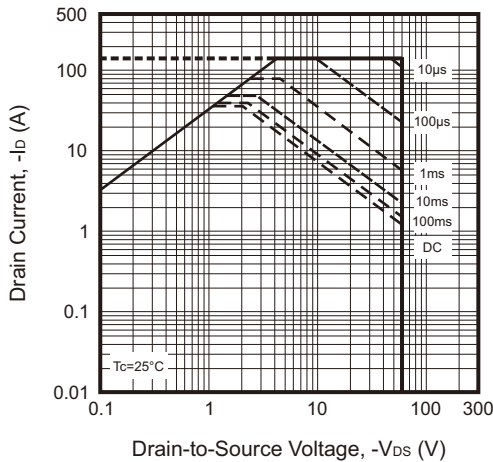
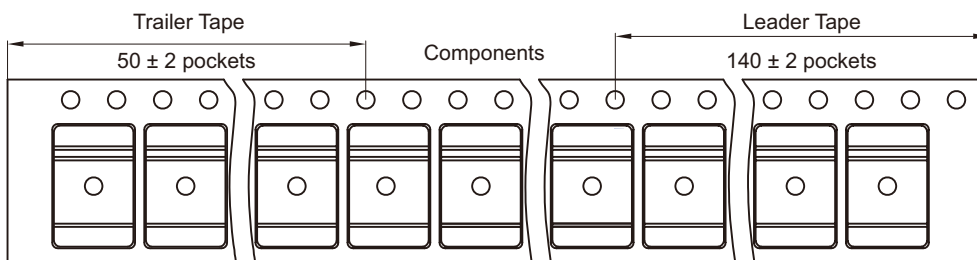
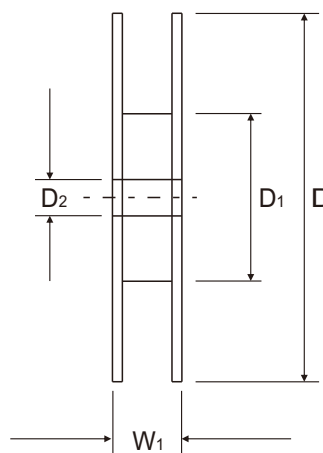
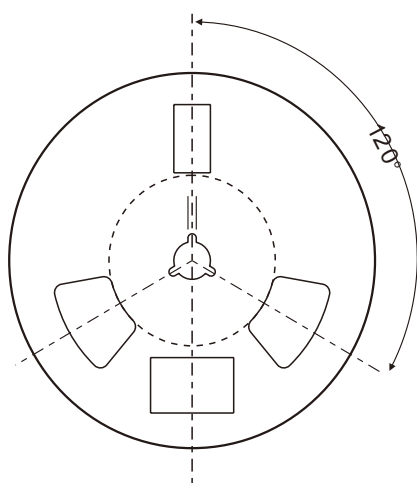
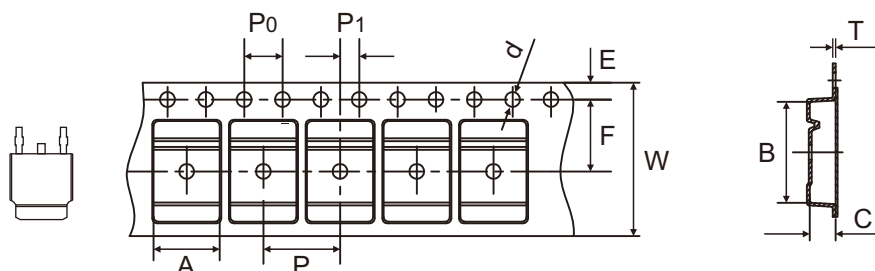


Fig.5 - Safe Operating Area



Reel Taping Specification



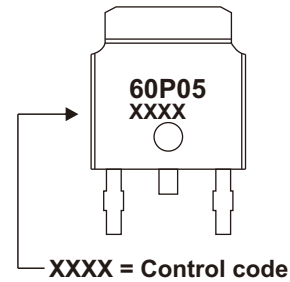
TO-252 (D-PAK)	SYMBOL	A	B	C	d	D	D1	D2
	(mm)	6.90 ± 0.10	10.50 ± 0.10	2.70 ± 0.10	1.55 ± 0.05	332 Max	100.00 ± 2.00	13.00 Min
	(inch)	0.272 ± 0.004	0.413 ± 0.004	0.106 ± 0.004	0.061 ± 0.002	13.071 Max	3.937 ± 0.079	0.512 Min

TO-252 (D-PAK)	SYMBOL	E	F	P	P0	P1	T	W	W1
	(mm)	1.75 ± 0.10	7.50 ± 0.10	8.00 ± 0.10	4.00 ± 0.10	2.00 ± 0.10	0.30 ± 0.05	16.00 ± 0.10	22.4 Max
	(inch)	0.069 ± 0.004	0.295 ± 0.004	0.315 ± 0.004	0.157 ± 0.004	0.079 ± 0.004	0.012 ± 0.002	0.630 ± 0.004	0.882 Max

Company reserves the right to improve product design, functions and reliability without notice. REV:A

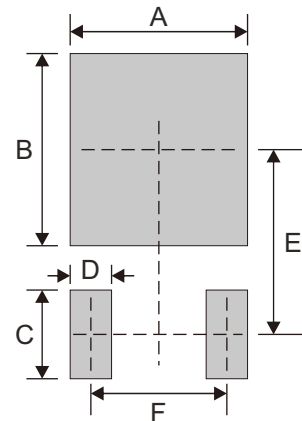
Marking Code

Part Number	Marking Code
CMS35P06D-HF	60P05



Suggested PAD Layout

SIZE	TO-252/D-PAK	
	(mm)	(inch)
A	6.00	0.236
B	6.50	0.256
C	3.00	0.118
D	1.40	0.055
E	6.25	0.246
F	4.60	0.181



Note: 1. The pad layout is for reference purposes only.

Standard Packaging

Case Type	REEL PACK	
	REEL (pcs)	Reel Size (inch)
TO-252/D-PAK	3,000	13