

# FRED Pt® Ultrafast Soft Recovery Diode, 150 A



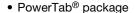
### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	150 A			
V <sub>R</sub>	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.08 V			
I <sub>FSM</sub>	1200 A			
t <sub>rr</sub> (typ.)	50 ns			
T <sub>J</sub> max.	175 °C			
Snap factor	0.5			
Package	PowerTab <sup>®</sup>			
Circuit configuration	Single			

#### **FEATURES**

- Ultrafast recovery time
- 175 °C max. operating junction temperature
- Screw mounting only
- Designed and qualified according to JEDEC®-JESD 47









#### **BENEFITS**

- · Reduced RFI and EMI
- Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

#### **DESCRIPTION/APPLICATIONS**

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

#### **MECHANICAL DATA**

Case: PowerTab®

Molding compound meets UL 94 V-0 flammability rating

Terminal: nickel plated, screwable

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	V <sub>R</sub>		600	V
Continuous forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 89 °C	150	۸
Single pulse forward current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	1200	A
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 200 μΑ	600	-	-	
		I <sub>F</sub> = 150 A	-	1.27	1.63	V
Forward voltage	$V_{F}$	I <sub>F</sub> = 150 A, T <sub>J</sub> = 125 °C	-	1.15	1.43	
		I <sub>F</sub> = 150 A, T <sub>J</sub> = 175 °C	-	1.08	1.32	
Develope legisare comment		$V_R = V_R$ rated	-	-	8	μA
Reverse leakage current I <sub>R</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	-	0.5	mA	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	70	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body - 3.5 -		nΗ		



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t <sub>rr</sub>		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	50	1	ns ns
	+	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	40	-	
	T <sub>J</sub> = 25 °C		-	100	-		
	T <sub>J</sub> = 125 °C		-	210	-		
Peak recovery current I <sub>RRM</sub>	ı	T <sub>J</sub> = 25 °C	$I_F = 50 \text{ A}$ $V_B = 200 \text{ V}$	-	10.5	-	Α
	T <sub>J</sub> = 125 °C	v <sub>R</sub> = 200 v dl <sub>F</sub> /dt = 200 A/μs	-	22	-	] ^	
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C	. '	-	550	-	nC
	T <sub>J</sub> = 125 °C		-	2350	-		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.35	K/W
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.2	-	IV/VV
Weight			-	-	5.02	g
Mounting torque			1.2 (10)	-	2.4 (20)	kgf · cm (lbf · in)
Marking device		Case style PowerTab®		EBU <sup>-</sup>	15006	

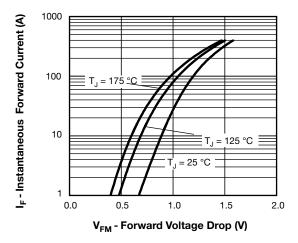


Fig. 1 - Maximum Forward Voltage Drop Characteristics

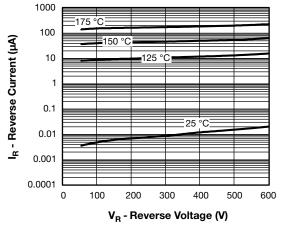


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



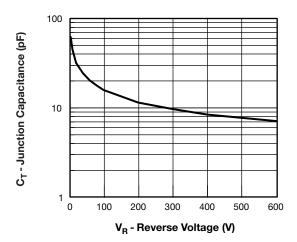


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

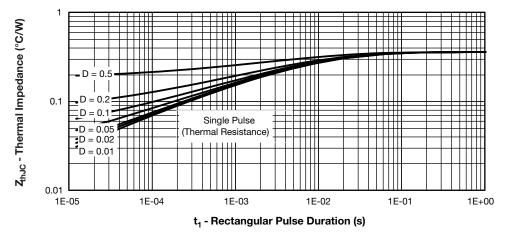


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

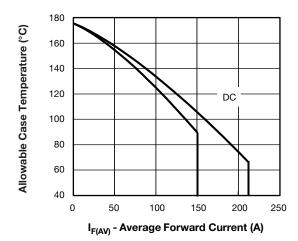


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

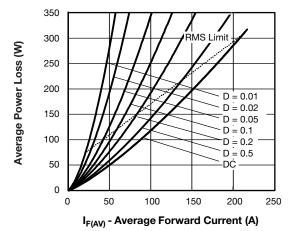


Fig. 6 - Forward Power Loss Characteristics

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## Vishay Semiconductors

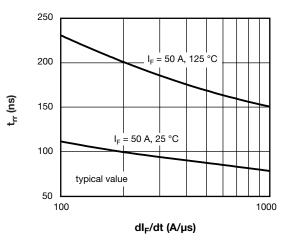


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

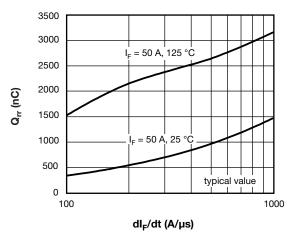
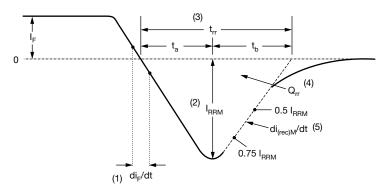


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  ${\rm Q_{rr}}$  area under curve defined by  ${\rm t_{rr}}$  and  ${\rm I_{RRM}}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

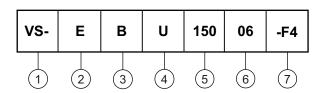
(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 9 - Reverse Recovery Waveform and Definitions



### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Single diode

**3** - PowerTab<sup>®</sup>

Ultrafast recovery

Current rating (150 = 150 A)

Voltage rating (06 = 600 V)

7 - Environmental digit:

-F4 = RoHS-compliant and totally lead (Pb)-free

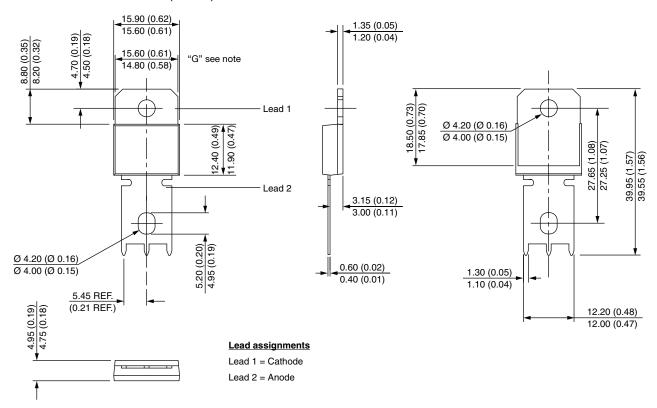
ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	BASE QUANTITY	PACKAGING DESCRIPTION	
VS-EBU15006-F4	25	375	Antistatic plastic tube	

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95240</u>				
Part marking information	www.vishay.com/doc?95467			
Application note	www.vishay.com/doc?95179			
SPICE model	www.vishay.com/doc?97099			



### PowerTab®

### **DIMENSIONS** in millimeters (inches)



#### Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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Vishay

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