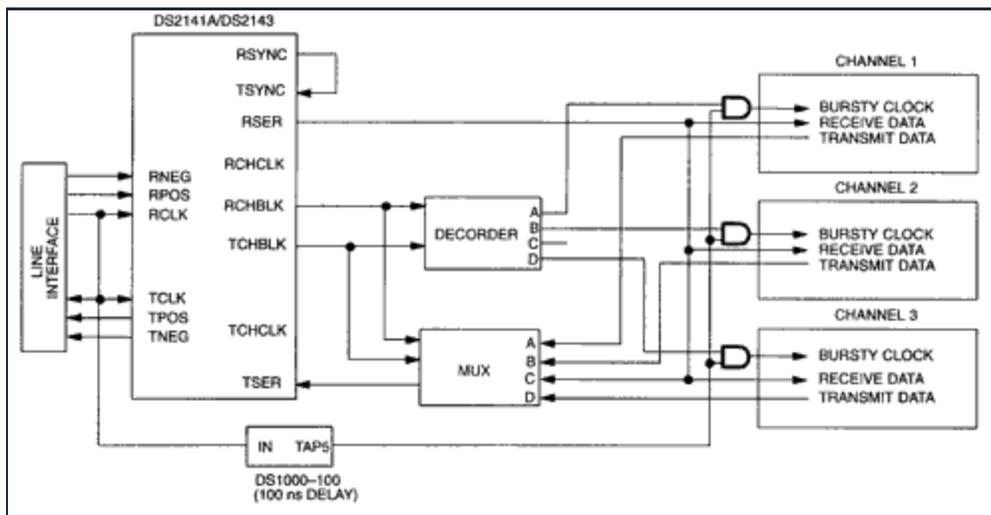Keywords: channel, drop and insert, drop, insert, looped-timed, timeslot, idle registers, T1, E1, framer, controller

APPLICATION NOTE 306

DS2141A, DS2143 Three Channel Drop and Insert

May 04, 2001

Abstract: Application Note 306 provides the design requirements for implementing a three channel drop and insert application using the Dallas Semiconductor/Maxim DS2141A T1 framer or DS2143 E1 framer.

Notes:

1. A "looped-timed" application is shown.
2. This application assumes the dropped channels occupy the same timeslots as the inserted channels.
3. The idle registers in the DS1241A/43/51/53 can be used to fill unused (if any) channels.
4. The used channels do not need to be contiguous nor do they need to be only one channel wide.
5. The line interface function is included onboard the DS2151 and DS2153 devices.
6. The delay is used to adjust the clock to account for the delay in generation of the signals that create the bursty clock.
7. Additional channels can be added by allowing the RCHCLK signal to be driven into a counter and decoded; the decoded signal would then be used to provide additional selects for the decoder and mux.

Related Parts	
DS2141A	T1 Controller
DS2143	E1 Controller

More Information

For Technical Support: http://www.maximintegrated.com/support
For Samples: http://www.maximintegrated.com/samples
Other Questions and Comments: http://www.maximintegrated.com/contact

Application Note 306: http://www.maximintegrated.com/an306
APPLICATION NOTE 306, AN306, AN 306, APP306, Appnote306, Appnote 306
Copyright © by Maxim Integrated Products
Additional Legal Notices: http://www.maximintegrated.com/legal