

# Engineering/Process Change Notice

#### ECN/PCN No.: 4152

For Manufacturer						
Product Description: PLASTIC SMD MEMS OSCILLATOR	Abracon Part Numb EM	er / Part Series: S11	<ul> <li>□ Documentation only</li> <li>□ ECN</li> <li>⊠ EOL</li> </ul>	⊠ Series □ Part Number		
Affected Revision: E	New Revision:	OL	Application:	□ Safety ⊠ Non-Safety		
Prior to Change: Active						
After Change: EOL						
Cause/Reason for Change: Discontinuation of manufacturing capabili	ty.					
	Chan	ge Plan				
Effective Date: 2/7/2022	Additional Remarks: N/A					
Change Declaration: N/A						
<b>Issued Date:</b> 2/7/2022	Issued By: Brooke Cushman Product Engineer		Issued Department: Engineering			
Approval: Thomas Culhane Engineering Director	Approval: Reuben Quintanilla Quality Director		Approval: Ying Huang Purchasing Director			
	For Abrac	on EOL only				
Last Time Buy (if applicable): 5/7/2022	Alternate Part Numb		per / Part Series: none			
Additional Approval:	Additional Approval	:	Additional Approval:			
	Customer Appr	oval (If Applicable)				
Qualification Status:		□ Not accepted	r ECN/RCN is released			
Note: It is considered approved if there is no feedback from the customer 1 month after ECN/PCN is released.						
Customer Part Number:		Customer Project:				
Company Name:	Company Representative:		Representative Signature:			
Customer Remarks:						

Form #7020 | Rev. G | Effective: 02/22/2021 |

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## EMS11 Series



### **REGULATORY COMPLIANCE**

Lead Free	EU RoHS	China RoHS	REACH
$\bigotimes$	2011/65 + 2015/863	<b>O</b>	SVHC
COMPLIANT	COMPLIANT	COMPLIANT	COMPLIANT



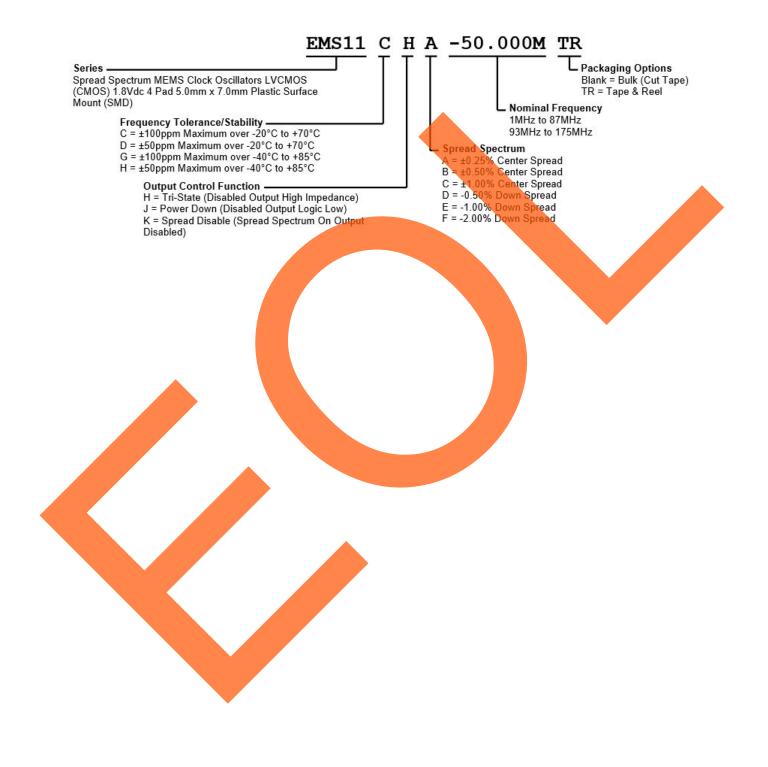
#### **ITEM DESCRIPTION**

Spread Spectrum MEMS Clock Oscillators LVCMOS (CMOS) 1.8Vdc 4 Pad 5.0mm x 7.0mm Plastic Surface Mount (SMD)

ELECTRICAL SPECIFI	CATIONS
Nominal Frequency	1MHz to 175MHz
Frequency Tolerance/Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, 260°C Reflow, Shock, and Vibration ±100ppm Maximum over -20°C to +70°C ±50ppm Maximum over -40°C to +85°C ±50ppm Maximum over -40°C to +85°C
Aging at 25°C	±1ppm Maximum First Year
Supply Voltage	1.8Vdc ±5%
Maximum Supply Voltage	-0.5Vdc to +1.98Vdc
Input Current	Unloaded; Nominal Vdd 25mA Maximum over Nominal Frequency of 1MHz to 25MHz 35mA Maximum over Nominal Frequency of 25.000001MHz to 175MHz
	IOH=-8mA 90% of Vdd Minimum
Output Voltage Logic Low (V <sub>ol</sub> )	IOL=+8mA 10% of Vdd Maximum
Rise/Fall Time	Measured from 20% to 80% of waveform 2nSec Maximum
Duty Cycle	Measured at 50% of waveform 50 ±5(%) over Nominal Frequency of 1MHz to 75MHz 50 ±10(%) over Nominal Frequency of 75.000001MHz to 175MHz
Load Drive Capability	15pF Maximum
Output Logic Type	CMOS
Output Control Function	Tri-State (Disabled Output High Impedance) Power Down (Disabled Output Logic Low) Spread Di <mark>sable (Spr</mark> ead Spectrum On Output Disabled)
Power Down Input Voltage (Vih and Vil)	70% of Vdd Minimum or No Connection to Enable Output, 30% of Vdd Maximum to Disable Output (Disabled Output Logic Low)
Tri-State Input Voltage (Vih and Vil)	70% of Vdd Minimum or No Connection to Enable Output, 30% of Vdd Maximum to Disable Output (Disabled Output High Impedance)
Standby Current	Pad 1=Ground 50μΑ Maximum (Disabled Output: Logic Low)
Disable Current	Pad 1=Ground 20mA Maximum (Disabled Output: High Impedance)
Spread Spectrum Input Voltage (Vih and Vil)	70% of Vdd Minimum or No Connection to Enable Spread Spectrum-On Output, 30% of Vdd Maximum to Disable Spread Spectrum-On Output (Spread Spectrum On Output Disabled)
Spread Spectrum	±0.25% Center Spread (Not available with Output Control Function of Spread Disable) ±0.50% Center Spread (Not available with Output Control Function of Spread Disable) ±1.00% Center Spread (Not available with Output Control Function of Spread Disable) -0.50% Down Spread -1.00% Down Spread -2.00% Down Spread
Modulation Frequency	30kHz Minimum, 32kHz Typical, 35kHz Maximum
Period Jitter	Cycle to Cycle; Spread Spectrum-On; Fo=133.333M, Vdd=1.8Vdc, 90pSec Maximum
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

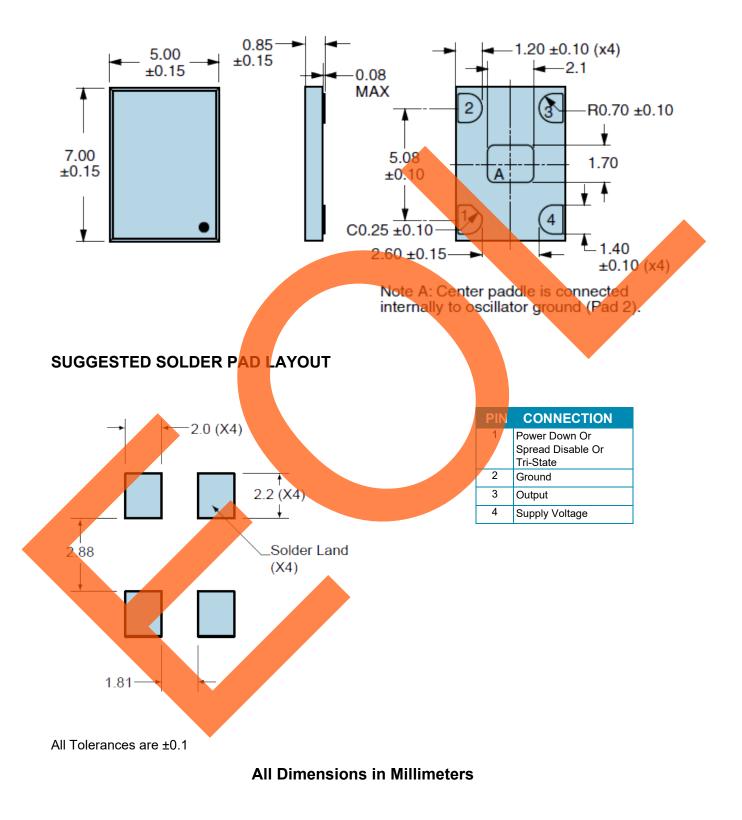


#### PART NUMBERING GUIDE





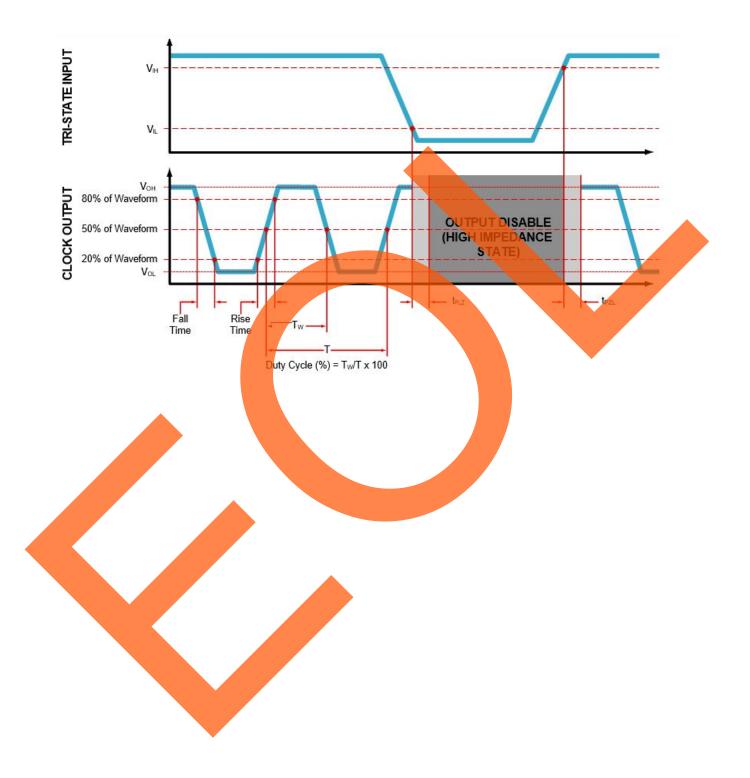
### **MECHANICAL DIMENSIONS**



## **EMS11** Series

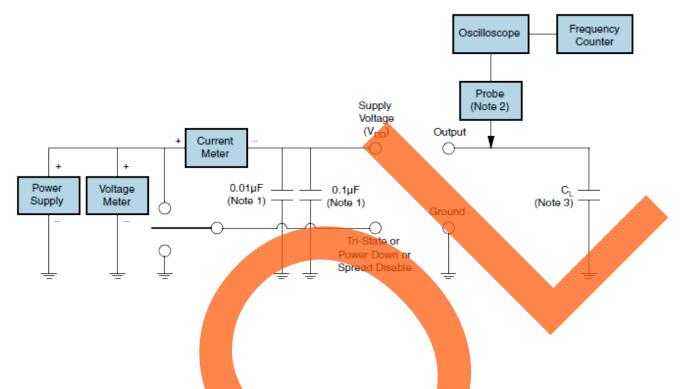


### OUTPUT WAVEFORM & TIMING DIAGRAM





## **TEST CIRCUIT FOR CMOS OUTPUT**



- Note 1: An external 0.01μF ceramic bypass capacitor in parallel with a 0.1μF high frequency ceramic bypass capacitor close (less Than 2mm) to the package ground and supply voltage pin is required.
   Note 2: A low capacitance (<12pF), 10X Attentuation Factor, High Impedance (>10Mohrms), and High bandwidth (>300MHz)
- Passive probe is recommended.
- Note 3: Capacitance value ( $C_L$ ) includes sum of all probe and fixture capacitance.

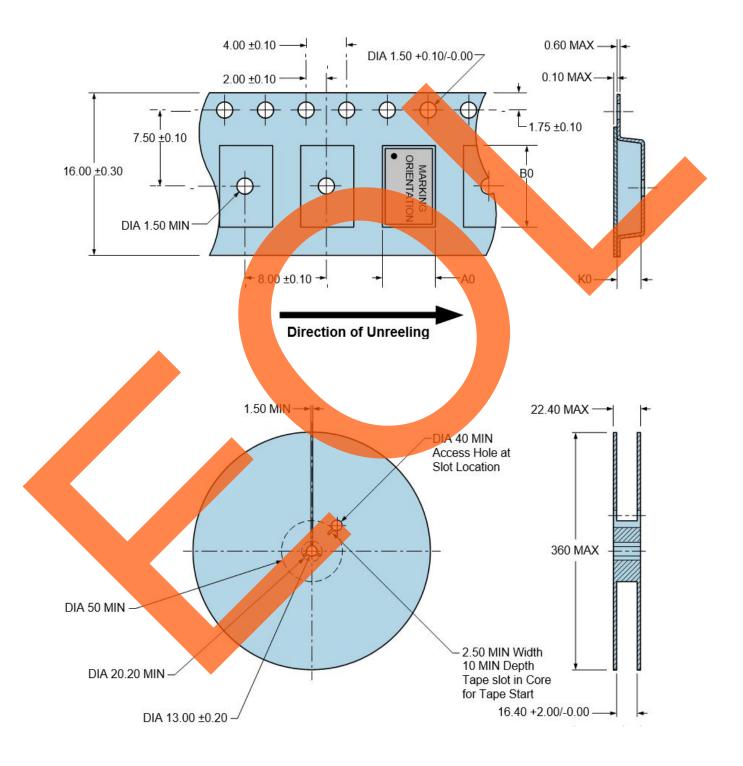
## **EMS11** Series



### **TAPE & REEL DIMENSIONS**

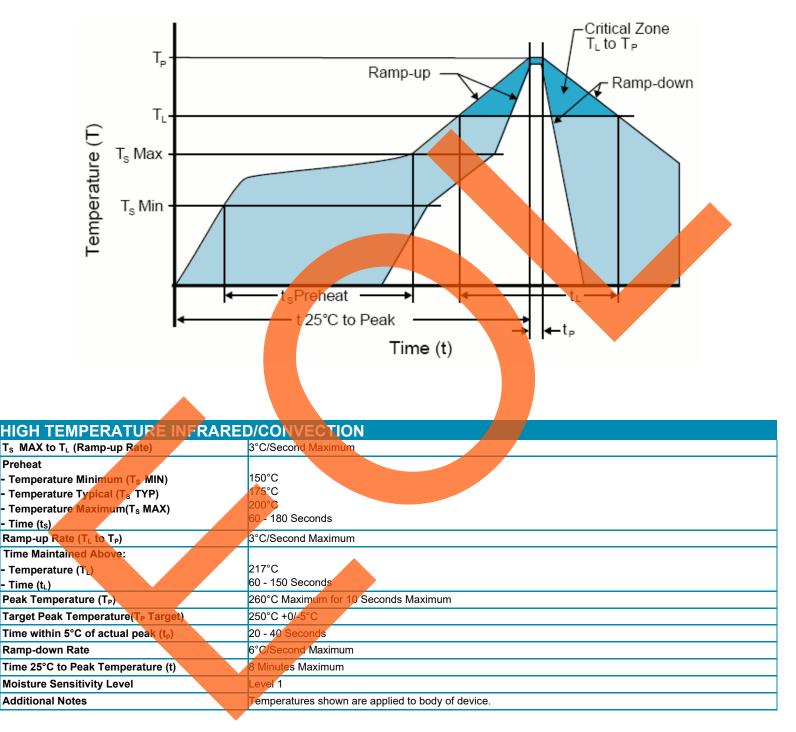
Quantity per Reel: 1000 Units All Dimensions in Millimeters

Compliant to EIA-481





#### RECOMMENDED SOLDER REFLOW METHOD

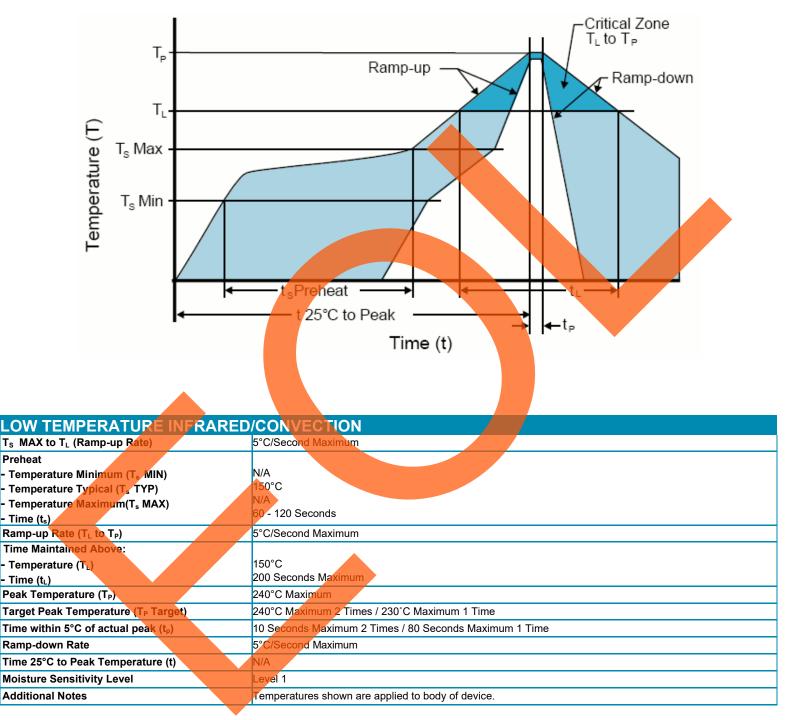


#### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)



#### RECOMMENDED SOLDER REFLOW METHOD



#### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)