190826577 Si47961-62 Data Sheet Rev 1.3

PCN Issue Date: 8/26/2019   **Effective Date:** 12/2/2019

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce the release of the Si47961-62 Data Sheet from document revision 1.0 to documents revision 1.3. A detailed description of the changes to the data sheet are summarized in the change impact section of this document. The new features reflected in the data sheet are applicable with a FW version that is 4.0 or later.

Reason for Change

The Data Sheet was updated to reflect changes in features.

Impact on Form, Fit, Function, Quality, Reliability

There is no impact on form, fit, quality and reliability.

Si47961-62 Data Sheet Revision 1.3 changes:

 Updated " Features" on page 1.
--DRM30/DRM+
--- This feature will be supported only on Si47962 (but not on Si47961).
--- Added DRM Detect feature.

Updated Table 1, "Recommended Operating Conditions," on page 5.
--Added Note 2 specifying that VIO1 and VIO2 must be configured to the same voltage level.

Updated Table 9, "Band III Receiver Characteristics (Si47962 Only)," on page 13.
--Changed CW offset for Selectivity spec from 5.163 to 5.136 MHz.

Updated Table 17, "I2S Digital Audio Interface Characteristics in Master Mode (Pins DICLK0, DCLK, DIFS0, DFS0, DIN0, DOUT0/1)," on page 25.
--Added Crystal Frequency Tolerance specification for DRM applications (±50 ppm Max).
--Changed Crystal Frequency Tolerance specification for DAB applications to be ±50 ppm Max (instead of ±100 ppm Max). The change enables improved reception performance of the DAB coprocessor.

Updated Table 22, "Absolute Maximum Ratings," on page 29.
--Band III Max RF input level increased from 13 dBm to 21 dBm.

Updated "6.4.1. ZIF I/Q Data Format" on page 43.
--Added explanation about possible frame sizes in DAB mode.

 Updated "7. Digital Radio and Antenna Diversity" on page 48.
--Added new "7.8. DRM30 (Si47962 Only)" on page 52, which describes DRM operation.
--Added new "7.9. Digital Radio Detect" on page 53, which explains HD Detect, DAB Detect, and DRM Detect features.
--Added new "7.10. Digital AGC (Si47962 Only—DAB and DRM)" on page 53.

 Deleted "14. Additional Supporting Documents".
--Previously listed support documentation and collateral is now provided to customers during the development cycle.

Updated "8.3. OPERATIONAL State" on page 54.
-- Updated setup and hold timing requirements for RSTB signal

Product Identification

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Existing Part #
SI47961A0GE1AM
SI47961A0GE1AMR
SI47961A0GE1GM
SI47961A0GE1GMR
SI47962A0GE1AM
SI47962A0GE1AMR
SI47962A0GE1GM
SI47962A0GE1GMR
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Last Date of Unchanged Product: 12/2/2019

Qualification Samples

Available upon request.

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at http://www.silabs.com.
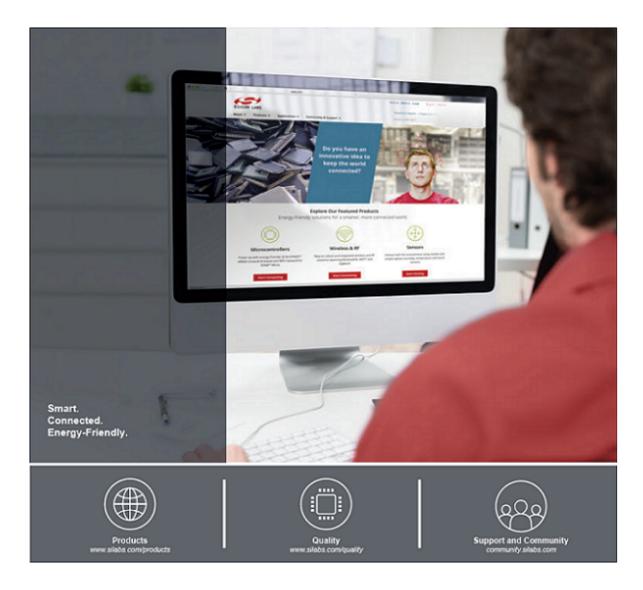
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