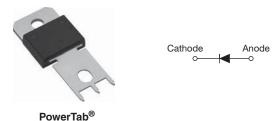
VS-80EBU02HF4

Vishay Semiconductors

Ultrafast Soft Recovery Diode, 80 A FRED Pt®



PowerTab

| PRODUCT SUMMARY | | | | |
|----------------------------------|-----------------------|--|--|--|
| Package | PowerTab [®] | | | |
| I _{F(AV)} | 80 A | | | |
| V _R | 200 V | | | |
| V _F at I _F | 0.77 V | | | |
| t _{rr} (typ.) | See recovery table | | | |
| T _J max. 175 °C | | | | |
| Diode variation | Single die | | | |

FEATURES

- Ultrafast recovery time
- 175 °C max. operating junction temperature
- Screw mounting only
- AEC-Q101 qualified
- PowerTab[®] package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

DESCRIPTION/APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

| ABSOLUTE MAXIMUM RATINGS | | | | |
|--|-----------------------------------|-------------------------|-------------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MAX. | UNITS |
| Cathode to anode voltage | V _R | | 200 | V |
| Continuous forward current | I _{F(AV)} | T _C = 131 °C | 80 | |
| Single pulse forward current | I _{FSM} | T _C = 25 °C | 800 | А |
| Maximum repetitive forward current | I _{FRM} | Square wave, 20 kHz | 160 | |
| Operating junction and storage temperatures | T _J , T _{Stg} | | -55 to +175 | °C |

| ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified) | | | | | | |
|--|--|--|------|------|------|-------|
| PARAMETER | SYMBOL | 30L TEST CONDITIONS | | TYP. | MAX. | UNITS |
| Breakdown voltage, blocking voltage | V _{BR} , V _r | I _R = 50 μA | 200 | - | - | |
| Forward voltage | VF | I _F = 80 A | - | 0.94 | 1.10 | V |
| r orward voltage VF | I _F = 80 A, T _J = 175 °C | - | 0.77 | 0.88 | | |
| Poweree lookage ourrept | | $V_{R} = V_{R}$ rated | - | - | 50 | μA |
| Reverse leakage current | I _R | $T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$ | - | - | 2 | mA |
| Junction capacitance | CT | V _R = 200 V - | | 89 | - | pF |
| Series inductance | Ls | Measured lead to lead 5 mm from package body - 3.5 - | | nH | | |

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1

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RoHS

COMPLIANT





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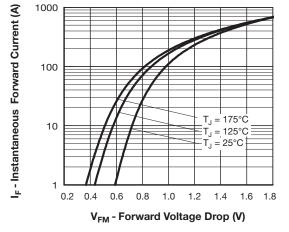
| DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified) | | | | | | | |
|---|-------------------------|--|---|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | MIN. | TYP. | MAX. | UNITS |
| Reverse recovery time t _{rr} | T _J = 25 °C | | - | 40 | - | | |
| | ۲rr | T _J = 125 °C | I _F = 80 A V _B = 160 V | - | 75 | - | ns |
| Peak recovery current I _{RRM} | | T _J = 25 °C | | - | 4.0 | - | А |
| | T _J = 125 °C | v _R = 160 v dI _F /dt = 200 A/µs | - | 8.8 | - | ~ | |
| Reverse recovery charge Q _{rr} | 0 | T _J = 25 °C | | - | 75 | - | |
| | Q _{rr} | T _J = 125 °C | | - | 310 | - | nC |

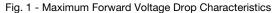
| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | |
|---|-------------------|--|-------------|------|-------------|---------------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Thermal resistance, junction to case | R _{thJC} | | - | - | 0.5 | °C/W |
| Thermal resistance, junction to heatsink | R _{thCS} | Mounting surface, flat, smooth and greased | - | 0.2 | - | 0/10 |
| Weight | | | - | - | 5.02 | g |
| weight | | | - | 0.18 | - | oz. |
| Mounting torque | | | 1.2 (10) | - | 2.4 (20) | N · m (lbf · in) |
| Marking device | | Case style PowerTab® | | 80EB | U02H | |



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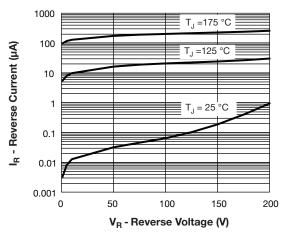


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

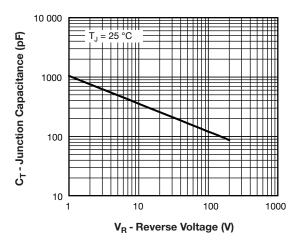
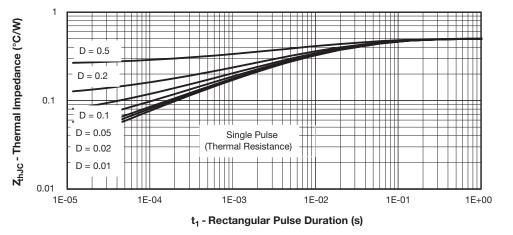
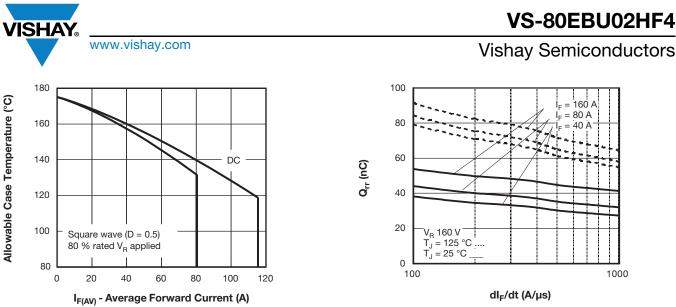
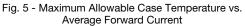


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage









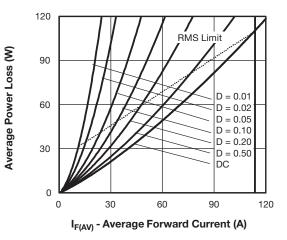
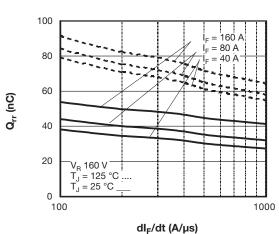


Fig. 6 - Forward Power Loss Characteristics



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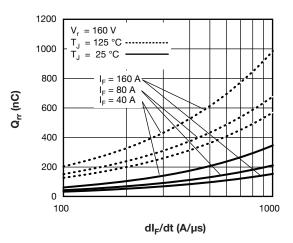


Fig. 8 - Typical Stored Charge vs. dl_F/dt

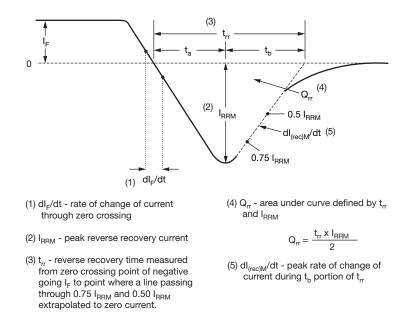


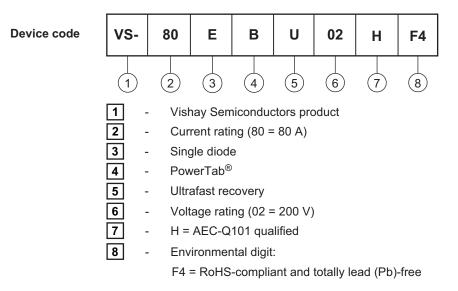
Fig. 9 - Reverse Recovery Waveform and Definitions

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|--|--|--------------------------------|
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Vishay Semiconductors



ORDERING INFORMATION TABLE



| ORDERING INFORMATION (Example) | | | | | | | |
|--------------------------------|--|-----|-------------------------|--|--|--|--|
| PREFERRED P/N | ED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION | | | | | | |
| VS-80EBU02HF4 | 25 | 375 | Antistatic plastic tube | | | | |

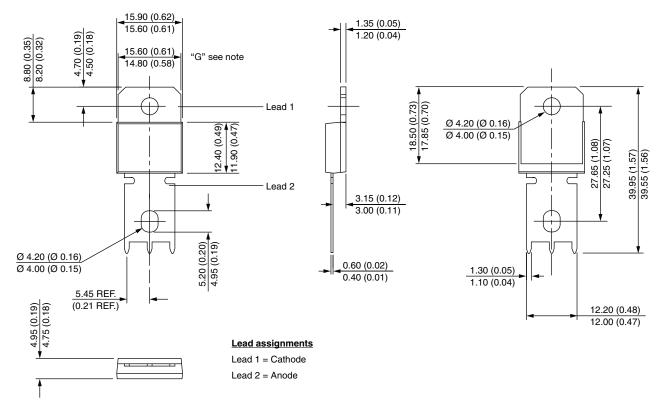
| LINKS TO RELATED DOCUMENTS | | | | |
|---|--------------------------|--|--|--|
| Dimensions www.vishay.com/doc?95240 | | | | |
| Part marking information www.vishay.com/doc?95467 | | | | |
| Application note | www.vishay.com/doc?95179 | | | |



Vishay Semiconductors

PowerTab[®]

DIMENSIONS in millimeters (inches)



Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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