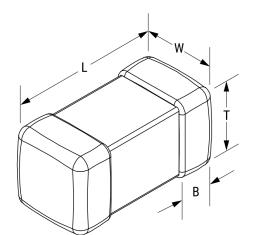


L0806B560JSWST

Aliases (LO806B560JSWST) Not for New Design KEMET, L-SWS, Commercial Grade, 56 uH, 5%



General Information		
Series	L-SWS	
Style	SMD Chip	
RoHS	Yes	
Notes	Last Order Date June 30, 2023.	

Specifications	
Inductance	56 uH
Inductance Tolerance	5%
Temperature Range	-40/+105°C
Current	95 mAmps (MAX)
DC Resistance	5.9 Ohms
Self-Resonant Frequency	12 MHz MIN

Click here for the 3D model.

Dimensions	
Chip Size	0806
L	2mm +/-0.2mm
W	1.6mm +/-0.2mm
Т	1.6mm +/-0.2mm
В	0.5mm +/-0.2mm

Packaging Specifications	
Packaging	T&R
Packaging Quantity	2000

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