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PCN Number: Title: Datasheet for		DS80PCI102							05/00/2015		
	_	er Contact:	PCN Manager			D)eı	pt:	Опа	lity Services	
		Type:	- Crimanager	_				7	Quu	ney Services	
		embly Site			Design		1	Wafei	r Bum	p Site	
Ħ	Assembly Process			Data Sheet			Ť	Wafer Bump Material			
Assembly Materials			S						er Bump Process		
Mechanical Specification							Wafer Fab Site				
Packing/Shipping/Labeling				Test Process Wafer			Fab l	Materials			
☐ Wafer Fab								Process			
				N	otification Details						
		tion of Chang			nouncing an information						
The	follo		. ,		ated as summarized belo	ow.				DS80PCI102	
	INS	RUMENTS				SNLS344	1G -	JULY 2011	-REVISE	D AUGUST 2015	
'											
Cha	anges	from Revision F (C	october 2014) to	Rev	ision G					Page	
•	Chang	ed pin mapping for	VIN and VDD to	corre	ct typo					5	
Added full SMBus-to-EEPROM table mapping									20		
Changed description of EEPROM bits to match corresponding SMBus register map description											
	Changed location of CHB VOD in Table 7 to match correct location in EEPROM map										
	2. a.										
Changed EEPROM bit description to match the description in the corresponding SMBus register map											
	Changed location of CHA VOD in Table 9 to match correct location in EEPROM map										
_	Audeu	inomation in regi	ster Map about 16	gist	er bits saved to EEFICON						
The	data	sheet number	will be chan	ain	n						
		Family	Will be chari	9111	Change From:				Chan	ge To:	
DS80PCI102				SNLS344F			SNLS344G				
		anges may be			e datasheet links provide <u>02</u>	ed.					
		for Change:									
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		•	-	-	unction, Quality or Re						
		pated impact. tual device.	This is a spe	ecifi	cation change announce	ment	OI	nly. Th	ere ar	e no changes	
Cha	nges	s to product i	dentification	n	resulting from this PC	N:					
Non	e.										
Pro	duct	Affected:									
DS	80PC	I102SQ/NOPE	DS80P	CI1	02SQE/NOPB						
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
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