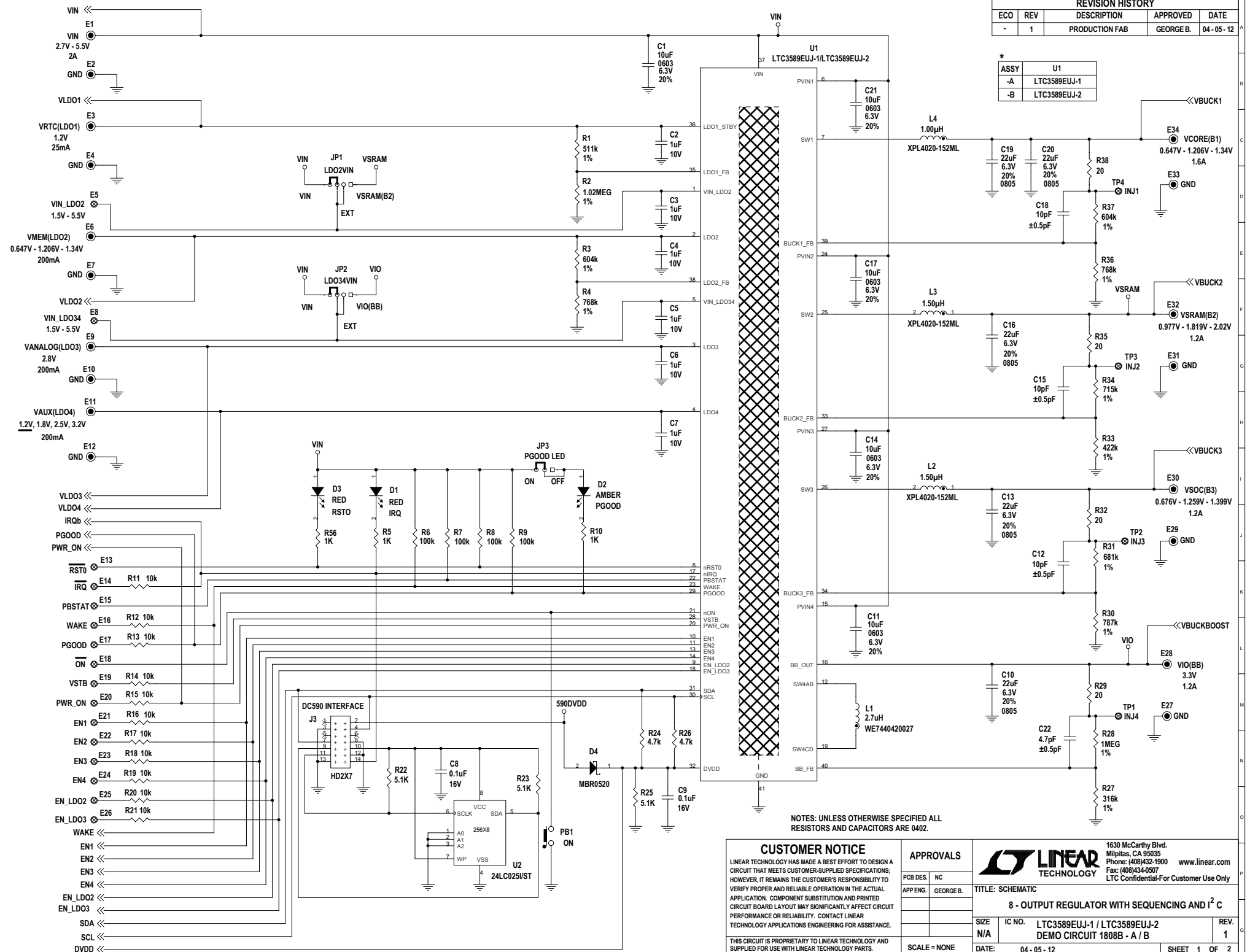



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PRODUCTION FAB	GEORGE B.	04-05-12

* ASSY		U1
-A	LTC3589EUJ-1	
-B	LTC3589EUJ-2	



NOTES: UNLESS OTHERWISE SPECIFIED ALL RESISTORS AND CAPACITORS ARE 0402.

CUSTOMER NOTICE		APPROVALS		 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE. THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		PCB DES: NC	APP ENG: GEORGE B.	
TITLE: SCHEMATIC		8 - OUTPUT REGULATOR WITH SEQUENCING AND I²C		
SIZE: N/A	IC NO: LTC3589EUJ-1 / LTC3589EUJ-2	REV: 1		
DATE: 04-05-12	DEMO CIRCUIT 1808B - A / B			
SCALE: NONE		DATE: 04-05-12	SHEET 1 OF 2	