

Product/Process Change Notification



N° 2021-012-A

Dear customer,

please find attached our Infineon Technologies AG PCN:

Several changes affecting product TLS820F0EL V33

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before 2021-07-07.
- Infineon aligns with the widely-recognized JEDEC STANDARD "JESD46", which stipulates:
"Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change."
Notwithstanding the aforesaid individual agreements shall prevail.

Your prompt reply will help Infineon to assure a smooth and well-executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Infineon Technologies AG

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Chairman of the Supervisory Board: Dr. Wolfgang Eder

Management Board: Dr. Reinhard Ploss (CEO), Dr. Helmut Gassel, Jochen Hanebeck, Constanze Hufenbecher, Dr. Sven Schneider

Registered Office: Neubiberg

Commercial Register: München HRB 126492

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Products affected

Please refer to attached affected product list 1_cip21012_A

Detailed change information

Subject: Several changes affecting product TLS820FOEL V33

Reason/Motivation: Expansion of wafer production and wafer test to assure continuity of supply and enable flexible manufacturing. Update of design to further centralize the FE process.

Description	Old	New
DATA SHEET: Change of datasheet parameters /electrical specification (min./max./typ. values) and/or AC/DC specification	Rev. 1.1	Rev. 1.2
DESIGN: Design changes in active elements.		Full mask redesign for Kulim only
PROCESS - WAFER PRODUCTION: Move all or parts of production to a different wafer fab site.	Infineon Technologies Austria AG, Villach, Austria	Infineon Technologies Austria AG, Villach, Austria AND Infineon Technologies (Kulim) Sdn. Bhd., Kulim, Malaysia
TEST FLOW: Move of all or part of electrical wafer test and/or final test to a different test site.	Infineon Technologies Austria AG, Villach, Austria	Infineon Technologies Austria AG, Villach, Austria AND Infineon Technologies (Kulim) Sdn. Bhd., Kulim, Malaysia

Product identification

Wafer lot numbers from Villach start with VExxxxxx.
 Wafer lot numbers from Kulim with 1Exxxxxx
 Traceability assured via date code.
 No change in SP ordering number

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Anticipated impact of change

Based on the qualification performed, Infineon does not expect any negative impact on quality, function and reliability. No change in function expected.

DeQuMa-ID(s): SEM-DS-01 / SEM-DE-01 / SEM-PW-13 / SEM-TF-01

Attachments

1_cip21012_A	affected product list
4_cip21012_A	data sheet

Time schedule

Final qualification report	available
First samples available	on request
Intended start of delivery [1]	2021-11-24
Last order date (LOD) [2]	2021-11-24
Last delivery date (LDD) [3]	2022-11-24

[1] Provided date or earlier after customer approval

[2] Last time buy volume to be placed latest until LOD

[3] Delivery of new product can be earlier (see Intended start of delivery) and depends on approval

If you have any questions, please do not hesitate to contact your local sales office.

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Sales name	SP number	OPN	Package
TLS820F0EL V33	SP000898044	TLS820F0ELV33XUMA1	PG-SSOP-14-5