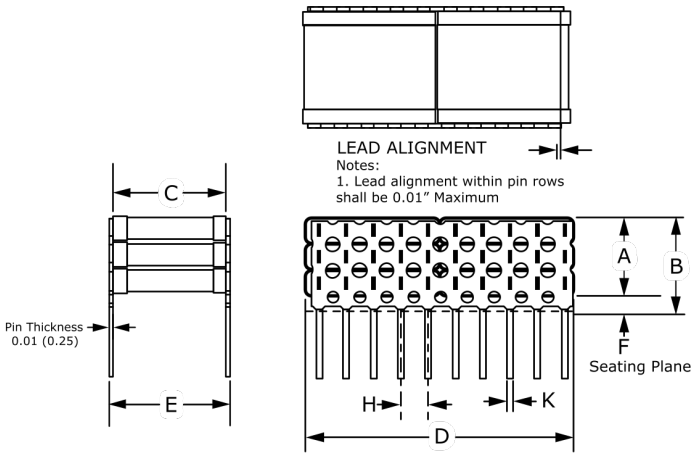
L1GN30D474KA06

KPS-MCC Indust C0G HT200C, Ceramic, 0.47 uF, 10%, 1000 VDC, C0G



Click here for the 3D model.

General Information	
Series	KPS-MCC Indust C0G HT200C
Style	Leaded Stacked Chip
Description	Low ESR, Stacked Ceramic Chips
Features	200C, Low ESR, High Thermal Stability, Bulk Capacitance
RoHS	With Exemptions
REACH	SVHC (Pb – CAS 7439-92-1)
SCIP Number	297427bb-2a48-4853-b594-641304a2cc24
Termination	Silver
Lead	Straight Leads
AEC-Q200	No
Notes	Number of chips in this stack: 6.

Dimensions	
D	25.64mm +/-0.635mm
L	6.35mm MIN
H	2.54mm NOM
F	1.397mm +/-0.25mm
A	8.13mm MAX
B	9.908mm MAX
C	11.43mm +/-0.635mm
E	12.7mm MAX
K	0.5mm NOM

Specifications	
Capacitance	0.47 uF
Capacitance Tolerance	10%
Voltage DC	1000 VDC
Dielectric Withstanding Voltage	1200 VDC
Temperature Range	-55/+200°C
Temperature Coefficient	C0G
Dissipation Factor	0.1% 1 kHz 25C
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	2.13 GOhms

Packaging Specifications	
Packaging	Waffle, Box
Packaging Quantity	16