

T495B157M004AHE900

T495

MnO2 Tantalum

Solder Coated

107.45 mg

156 Weeks

SMD, MnO2, Molded, Low ESR

http://www.p65warnings.ca.gov.

MARNING: Cancer and reproductive harm -

1dd2e1b8-26dd-4d52-927c-6f9d519011aa

SMD Chip

Low ESR

No

No

1

General Information

Series

Style

RoHS

Prop 65

SCIP Number

Termination

AEC-Q200

Component

Specifications

Weight Shelf Life

MSL

Dielectric

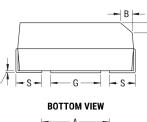
Description Features

T495, Tantalum, MnO2 Tantalum, 150 uF, 20%, 4 VDC, SMD, MnO2, Molded, Low ESR, 900 mOhms, 3528, Height Max = 2.1mm

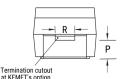
CATHODE (-) END VIEW

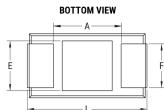


ANODE (+) END VIEW



SIDE VIEW





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	eith	er e	nd	

Click here for the 3D model.

Dimensions				
Footprint	3528			
L	3.5mm +/-0.2mm			
W	2.8mm +/-0.2mm			
Н	1.9mm +/-0.2mm			
т	0.13mm REF			
S	0.8mm +0.1/-0.3mm			
F	2.2mm +/-0.1mm			
А	1.9mm MIN			
В	0.4mm +/-0.15mm			
E	2.2mm REF			
G	1.8mm REF			
Р	0.5mm REF			
R	1mm REF			
Х	0.1mm +/-0.1mm			

Т

	Dissipation actor
0.4mm +/-0.15mm	Failure Rate
2.2mm REF	Resistance
1.8mm REF	Dinale Current
0.5mm REF	Ripple Current
1mm REF	Leakage Current
0.1mm +/-0.1mm	

	Capacitance	150 uF
	Capacitance Tolerance	20%
	Voltage DC	4 VDC (85C), 2.68 VDC (125C)
	Temperature Range	-55/+125°C
	Rated Temperature	85°C
	Dissipation Factor	12% 120Hz 25C
	Failure Rate	N/A
	Resistance	900 mOhms (100kHz 25C)
	Ripple Current	307 mA (rms, 100kHz 25C), 276.3 mA (rms, 85C), 122.8 mA (rms, 125C)

6 uA (5min 25°C)

Packaging Specifications				
Packaging	T&R, 178mm			
Packaging Quantity	2000			

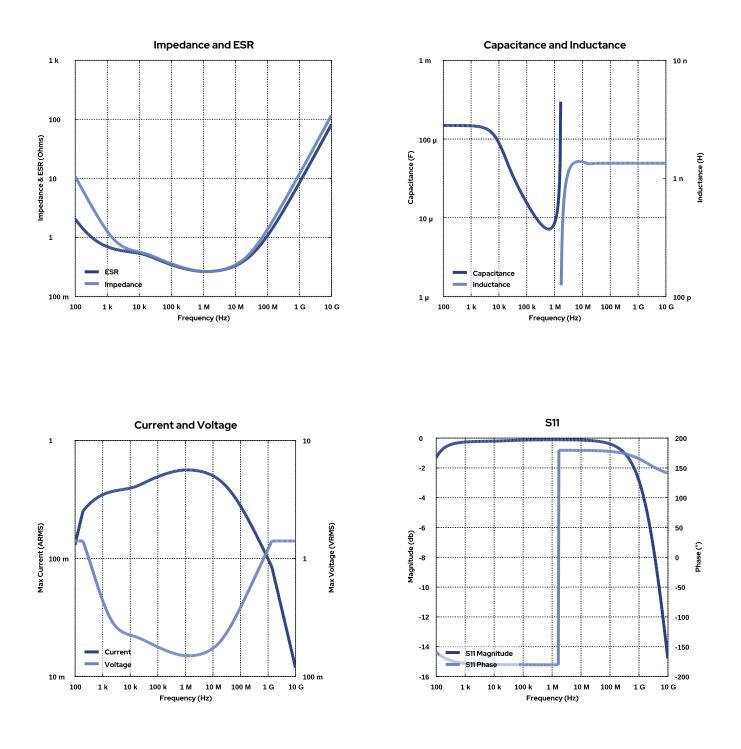
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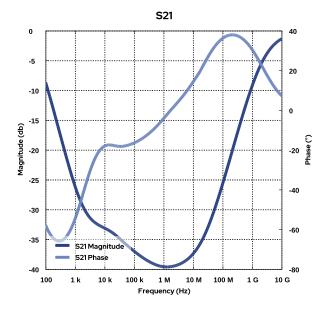
Simulations

For the complete simulation environment please visit K-SIM.





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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other
- harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.