



PMH600UNE

20 V, N-channel Trench MOSFET

8 March 2019

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN0606-3 (SOT8001) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 1 kV HBM
- Leadless ultra small and ultra thin SMD plastic package: 0.62 x 0.62 x 0.37 mm

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

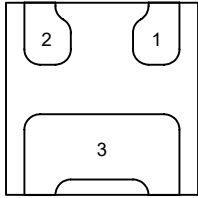
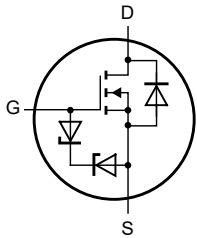
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ °C}$	-	-	20	V
V_{GS}	gate-source voltage		-8	-	8	V
I_D	drain current	$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ °C}$	[1]	-	800	mA
Static characteristics						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 4.5\text{ V}; I_D = 600\text{ mA}; T_j = 25\text{ °C}$	-	470	620	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm².

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 <p>Transparent top view DFN0606-3 (SOT8001)</p>	 <p>017aaa255</p>
2	S	source		
3	D	drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMH600UNE	DFN0606-3	plastic, leadless ultra small package; 3 terminals; body 0.62 x 0.62 x 0.37 mm	SOT8001

7. Marking

Table 4. Marking codes

Type number	Marking code
PMH600UNE	0001 0001

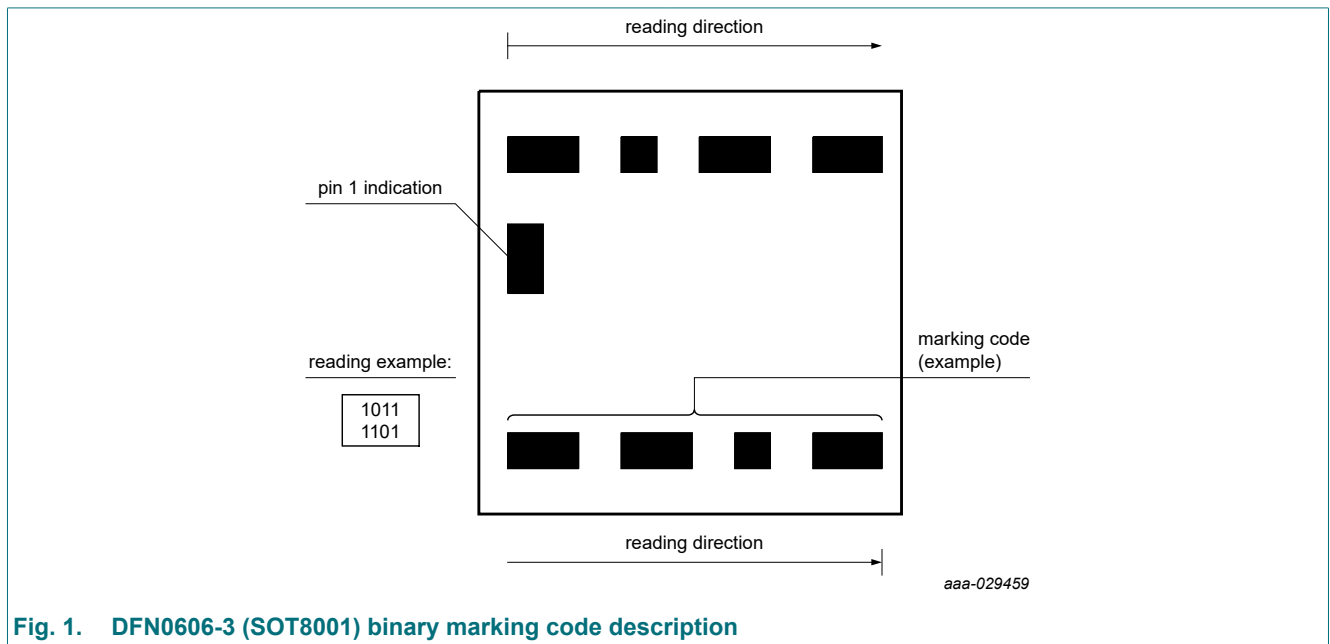


Fig. 1. DFN0606-3 (SOT8001) binary marking code description

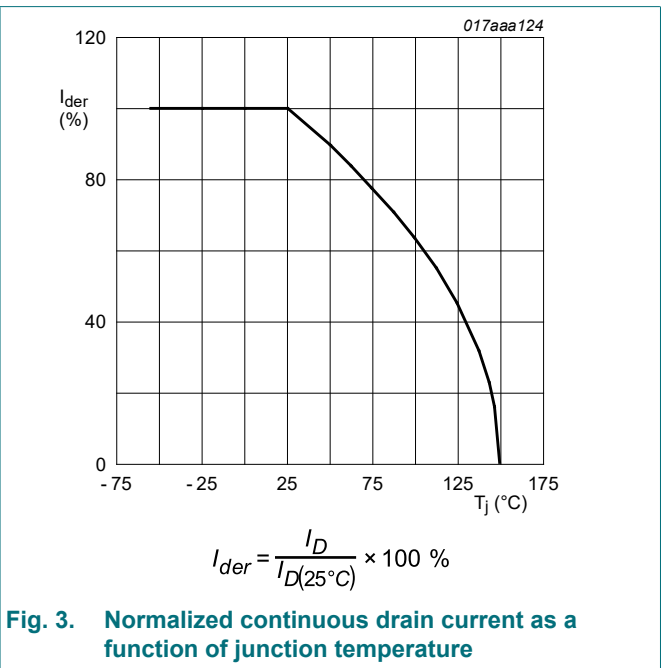
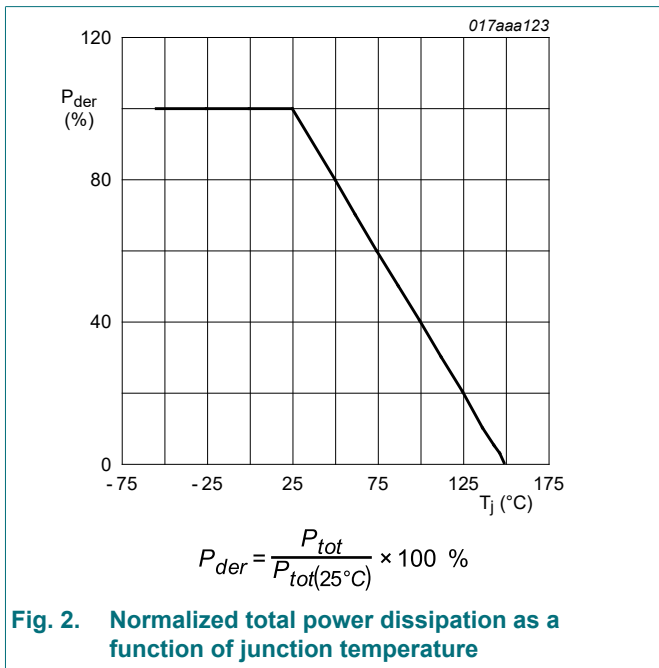
8. Limiting values

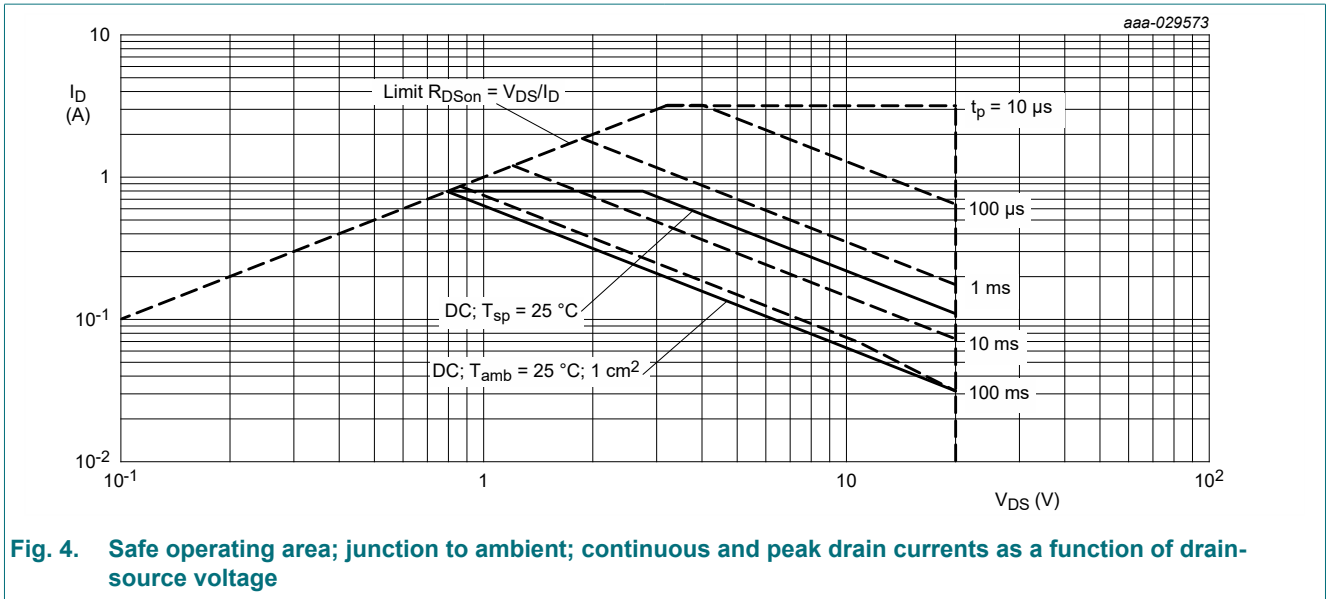
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	20	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	800	mA
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	500	mA
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	3.2	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	370	mW
			[1]	-	625	mW
		T _{sp} = 25 °C		-	2.2	W
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	650	mA

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm².
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.





9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	295	339	K/W
			[2]	-	174	200	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	50	58	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm².

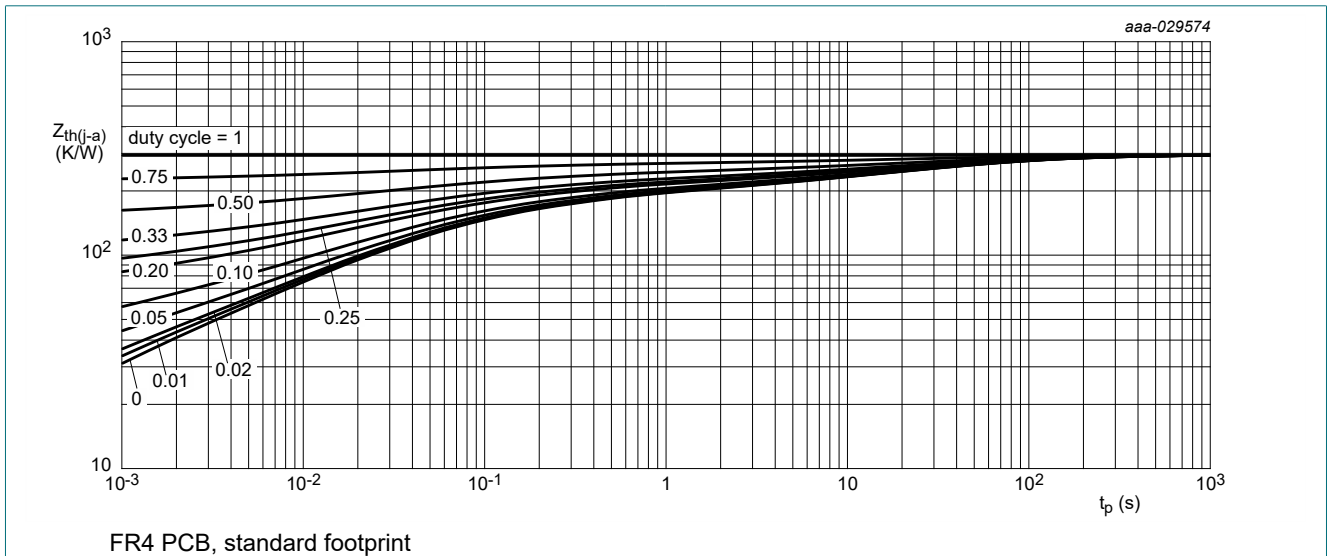


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

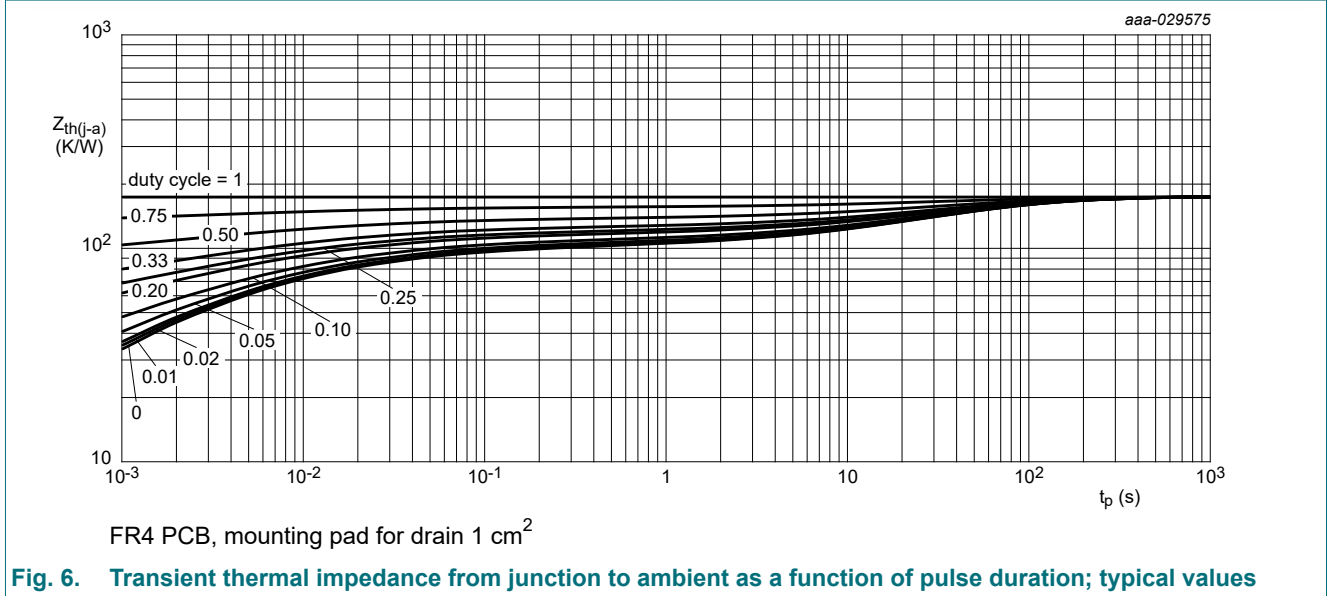


Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	20	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu A$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ }^\circ C$	0.45	0.7	0.95	V
I_{DSS}	drain leakage current	$V_{DS} = 20 V$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	1	μA
I_{GSS}	gate leakage current	$V_{GS} = 8 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	10	μA
		$V_{GS} = -8 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	-10	μA
		$V_{GS} = 4.5 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	1	μA
		$V_{GS} = -4.5 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	-1	μA
		$V_{GS} = 2.5 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	100	nA
		$V_{GS} = -2.5 V$; $V_{DS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	-	-100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 V$; $I_D = 600 mA$; $T_j = 25 \text{ }^\circ C$	-	470	620	m Ω
		$V_{GS} = 4.5 V$; $I_D = 600 mA$; $T_j = 150 \text{ }^\circ C$	-	750	1000	m Ω
		$V_{GS} = 2.5 V$; $I_D = 500 mA$; $T_j = 25 \text{ }^\circ C$	-	535	710	m Ω
		$V_{GS} = 1.8 V$; $I_D = 100 mA$; $T_j = 25 \text{ }^\circ C$	-	685	1050	m Ω
		$V_{GS} = 1.5 V$; $I_D = 10 mA$; $T_j = 25 \text{ }^\circ C$	-	860	1350	m Ω
		$V_{GS} = 1.2 V$; $I_D = 1 mA$; $T_j = 25 \text{ }^\circ C$	-	1500	-	m Ω
g_{fs}	forward transconductance	$V_{DS} = 10 V$; $I_D = 800 mA$; $T_j = 25 \text{ }^\circ C$	-	1.1	-	S
R_G	gate resistance	$f = 1 MHz$	-	15	-	Ω
Dynamic characteristics						
$Q_{G(tot)}$	total gate charge	$V_{DS} = 10 V$; $I_D = 600 mA$; $V_{GS} = 4.5 V$; $T_j = 25 \text{ }^\circ C$	-	0.29	0.31	nC
Q_{GS}	gate-source charge		-	0.04	-	nC
Q_{GD}	gate-drain charge		-	0.1	-	nC
C_{iss}	input capacitance	$V_{DS} = 10 V$; $f = 1 MHz$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	21.3	-	pF
C_{oss}	output capacitance		-	6	-	pF
C_{rss}	reverse transfer capacitance		-	4.6	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 10 V$; $I_D = 600 mA$; $V_{GS} = 4.5 V$; $R_{G(ext)} = 6 \Omega$; $T_j = 25 \text{ }^\circ C$	-	1	-	ns
t_r	rise time		-	3	-	ns
$t_{d(off)}$	turn-off delay time		-	9	-	ns
t_f	fall time		-	36	-	ns
Source-drain diode						
V_{SD}	source-drain voltage	$I_S = 600 mA$; $V_{GS} = 0 V$; $T_j = 25 \text{ }^\circ C$	-	0.8	1.2	V

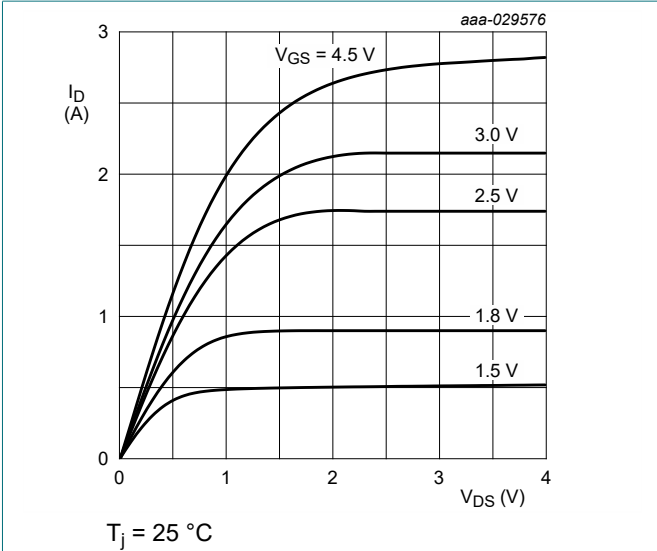


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

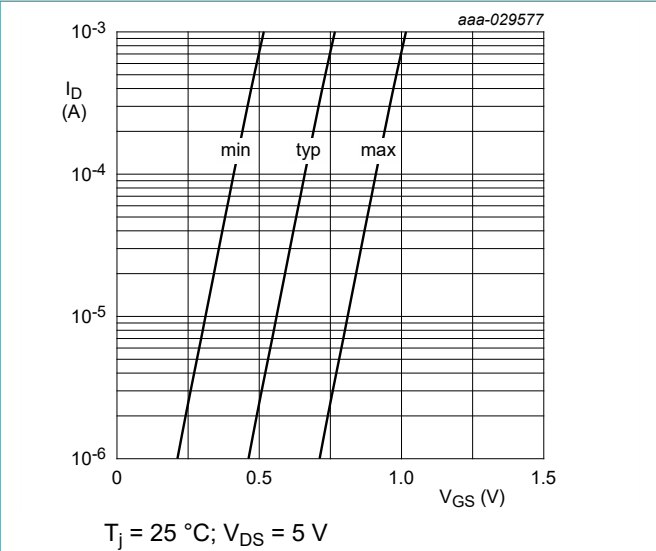


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

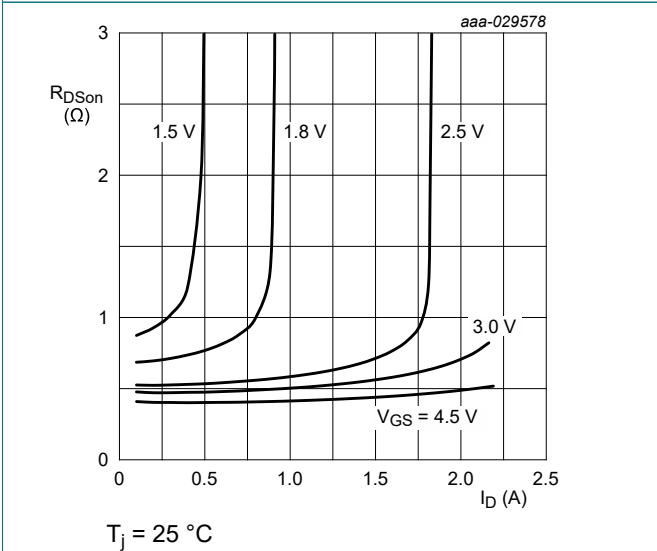


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

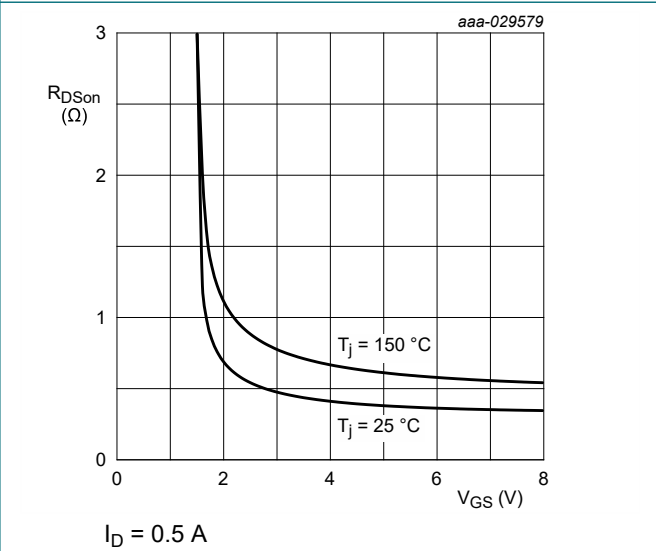
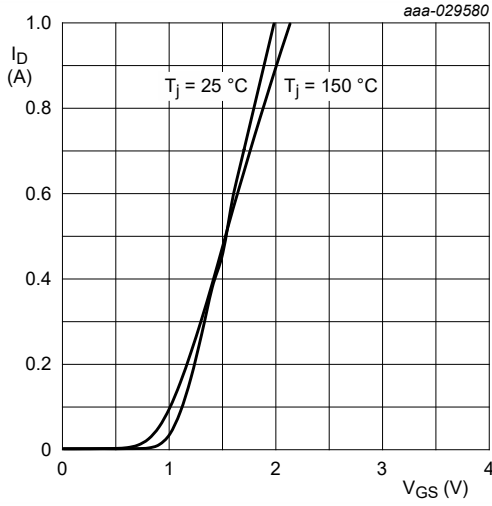
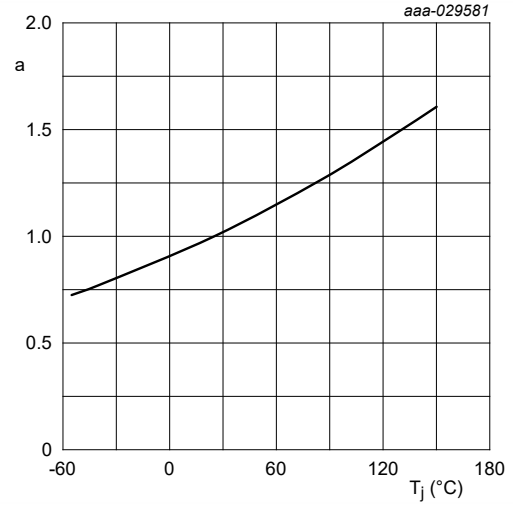


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values



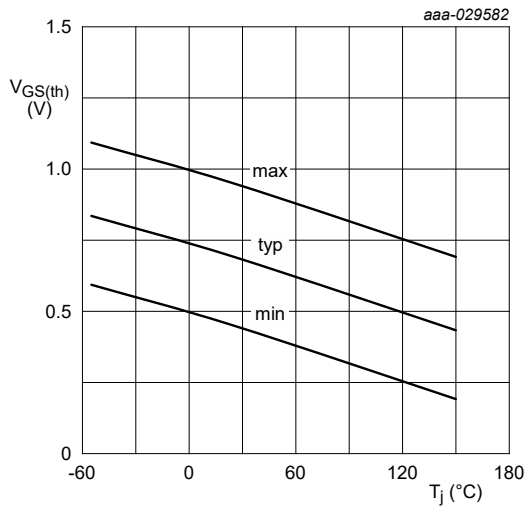
$V_{DS} > I_D \times R_{DSon}$

Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values



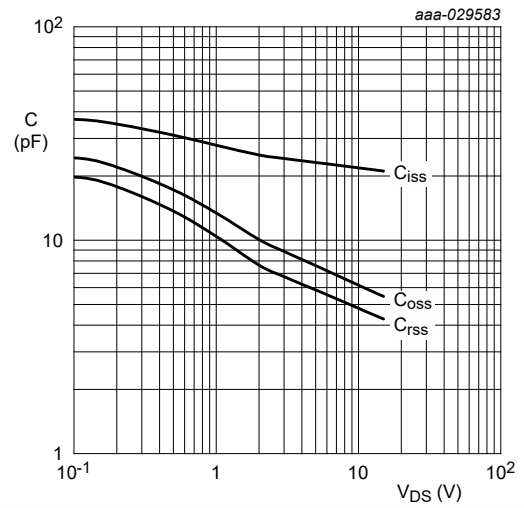
$a = \frac{R_{DSon}}{R_{DSon(25^\circ\text{C})}}$

Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values



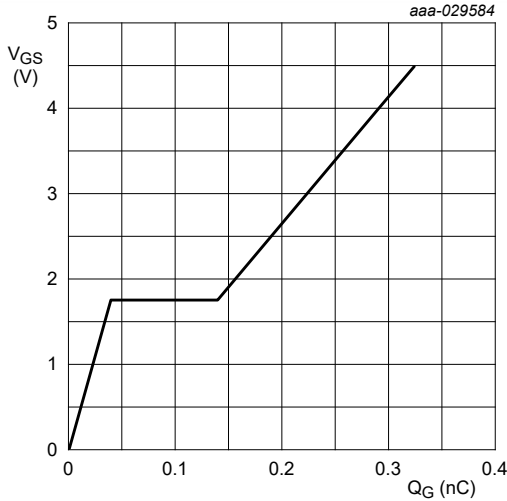
$I_D = 250\text{ }\mu\text{A}; V_{DS} = V_{GS}$

Fig. 13. Gate-source threshold voltage as a function of junction temperature



$f = 1\text{ MHz}; V_{GS} = 0\text{ V}$

Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = 0.8 \text{ A}$; $V_{DS} = 10 \text{ V}$; $T_{amb} = 25 \text{ }^\circ\text{C}$

Fig. 15. Gate-source voltage as a function of gate charge; typical values

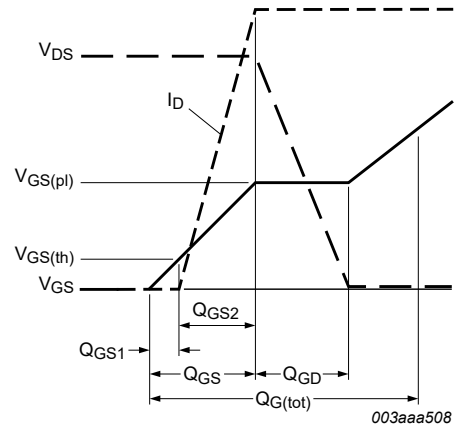
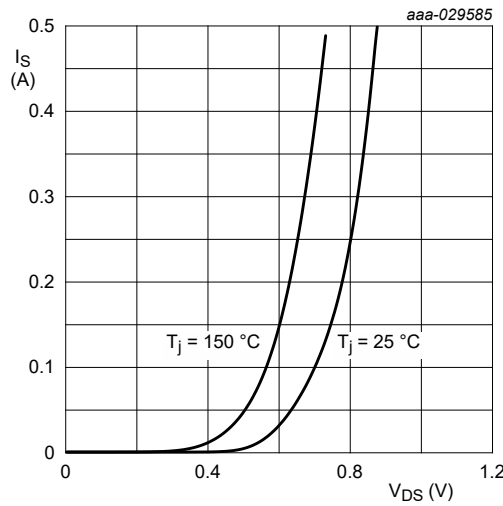


Fig. 16. Gate charge waveform definitions



$V_{GS} = 0 \text{ V}$

Fig. 17. Source current as a function of source-drain voltage; typical values

11. Test information

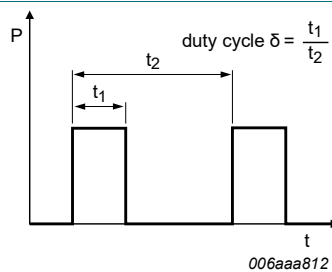


Fig. 18. Duty cycle definition

12. Package outline

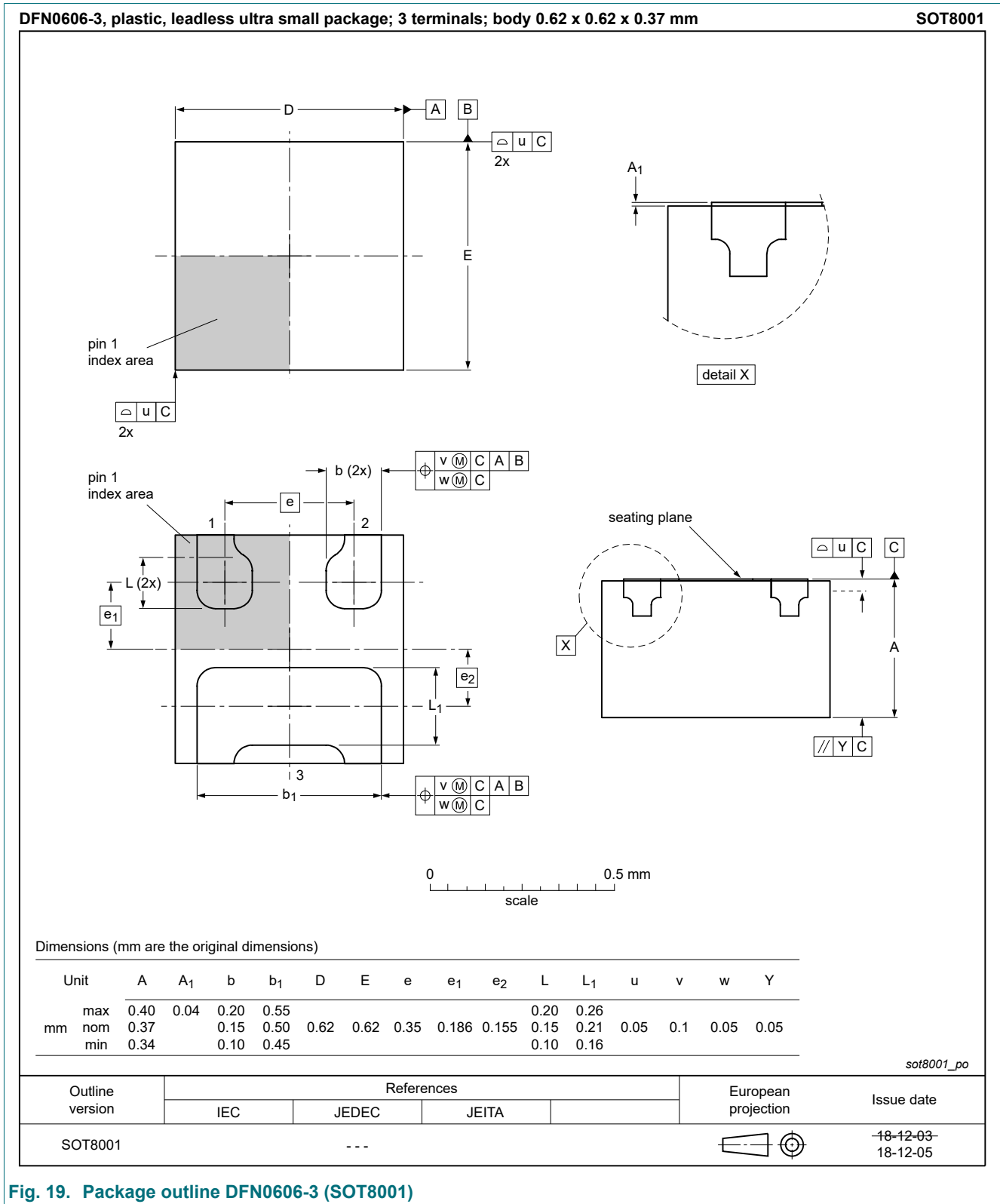


Fig. 19. Package outline DFN0606-3 (SOT8001)

13. Soldering

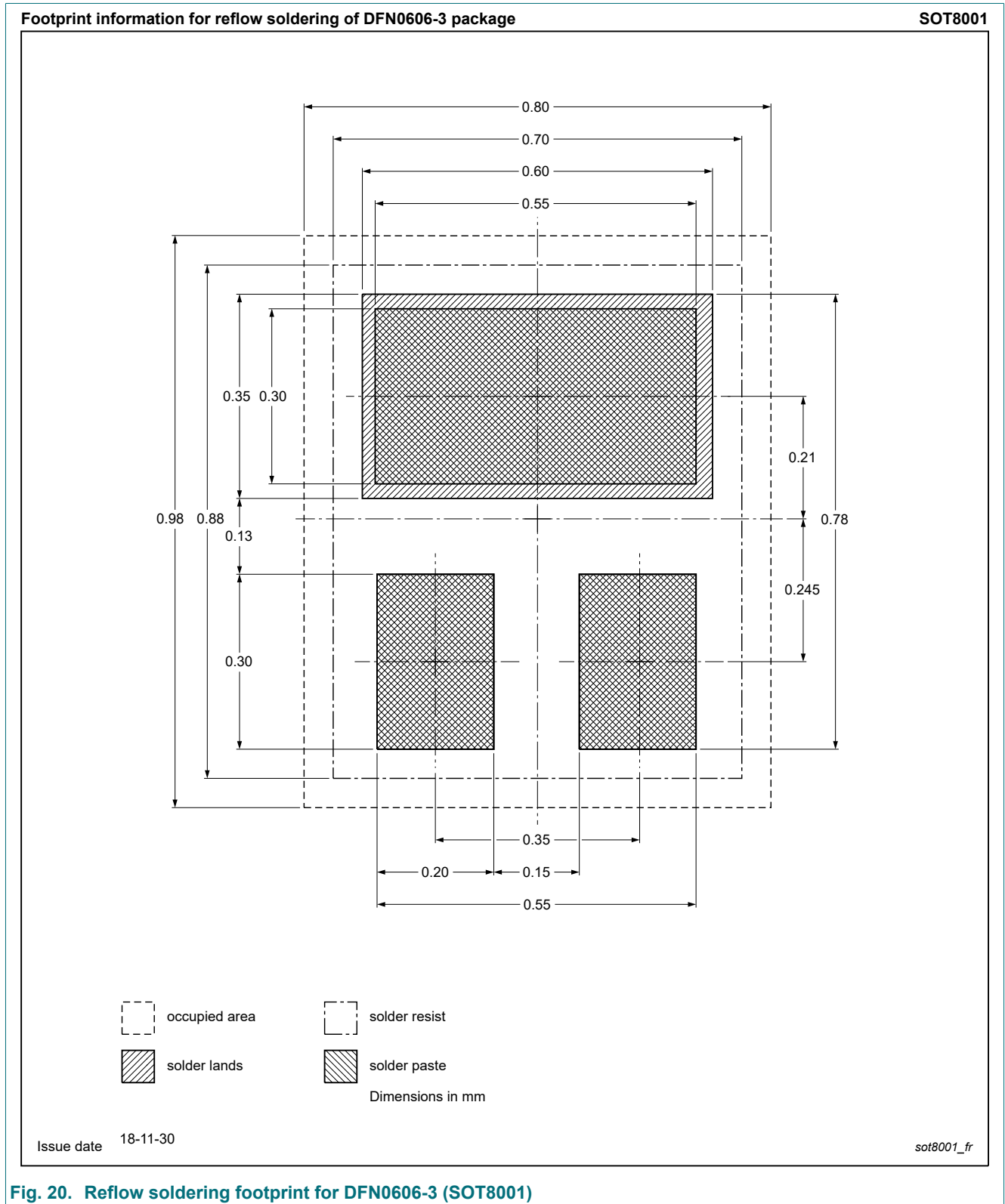


Fig. 20. Reflow soldering footprint for DFN0606-3 (SOT8001)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMH600UNE v.1	20190308	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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