

<b>Notification Number:</b>	20211209001	<b>Notification Date:</b>	December 17, 2021
<b>Title:</b>	Datasheet for DACx750 and DACx760		
<b>Customer Contact:</b>	<a href="#">Notification Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b>	Electrical Specification		

### Notification Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



**DAC7750, DAC8750**

SBAS538D – DECEMBER 2013 – REVISED DECEMBER 2021

#### Changes from Revision C (January 2018) to Revision D (December 2021)

**Page**

• Updated the numbering format for tables, figures, and cross-references throughout the document.....	1
• Changed <i>Loop compliance voltage</i> to <i>Reference input voltage</i> , and <i>Reference input voltage</i> to <i>External reference current</i> in <i>Recommended Operating Conditions</i> .....	5
• Changed <i>Digital input low voltage</i> test condition upper limit from 2.6 V to 3.6 V in <i>Recommended Operating Conditions</i> .....	5
• Deleted <i>Timing Requirements: Daisy-Chain Mode</i> section and <i>Daisy-Chain Mode Timing</i> figure.....	10
• Deleted <i>Power-Supply Sequence</i> section; content moved to <i>Power Supply Recommendations</i> section.....	21
• Deleted daisy-chain operation content from <i>Watchdog Timer</i> section.....	23
• Deleted <i>The DACx750 Shares the SPI Bus With Other Devices</i> subsection from <i>Watchdog Timer</i> section...	23
• Deleted daisy-chain operation from <i>Frame Error Checking</i> section.....	23
• Added CRC fault reset command of 0x96 to <i>Frame Error Checking</i> section.....	23
• Deleted <i>The DACx750 Shares the SPI Bus With Other Devices</i> subsection.....	23
• Changed duplicated 010 step-size from 0.125 to 0.25 in Table 8-3, <i>Slew Rate Step-Size Options</i> .....	25
• Added CRC fault reset command to Table 8-8, <i>Write Address Functions</i> .....	30
• Deleted <i>Daisy-Chain Operation</i> section.....	31
• Added <i>Multiple Devices on the Bus</i> section.....	31
• Changed Table 8-11 to delete daisy-chain operation and add CRC fault reset.....	32
• Changed <i>DCEN</i> to <i>Reserved</i> for DB3 in <i>Control Register</i> table.....	32
• Deleted text stating CAP2 pin is only available for the 40-pin VQFN package.....	35
• Added series resistance for supply and corrected HART-IN capacitance for Figure 9-3.....	37
• Added content from deleted <i>Power-Supply Sequence</i> section to <i>Power Supply Recommendations</i> section..	40
• Added fast supply ramp and series resistance content to <i>Power-Supply Recommendations</i> .....	40
• Added power supply series resistance to Figure 11-1, <i>Layout Example</i> .....	41



**DAC7760, DAC8760**

SBAS528D – JUNE 2013 – REVISED DECEMBER 2021

**Changes from Revision C (January 2018) to Revision D (December 2021)**

**Page**

- Updated the numbering format for tables, figures, and cross-references throughout the document..... 1
- Changed CMP pin description in *Pin Functions* table to include 100-pF capacitance to ground..... 3
- Deleted *Timing Requirements: Daisy-Chain Mode* section and *Daisy-Chain Mode Timing* figure..... 13
- Added 100-pF capacitance to ground for larger external compensation capacitors in *Voltage Output Stage* section..... 28
- Deleted *Power-Supply Sequence* section..... 31
- Deleted daisy-chain operation content from *Watchdog Timer* section..... 33
- Deleted *The DACx760 Shares the SPI Bus With Other Devices* subsection from *Watchdog Timer* section... 33
- Deleted daisy-chain operation content from *Frame Error Checking* section..... 33
- Added CRC fault software reset command of 0x96 to *Frame Error Checking* section..... 33
- Deleted *The DACx760 Shares the SPI Bus With Other Devices* subsection from *Frame Error Checking* section..... 33
- Changed duplicated 010 step-size from 0.125 to 0.25 in Table 8-3, *Slew Rate Step-Size Options* ..... 35
- Added CRC fault reset command to Table 8-8, *Write Address Functions* ..... 40
- Deleted *Daisy-Chain Operation* section..... 41
- Added *Multiple Devices on the Bus* section..... 41
- Changed *Command and Register Map* table to delete daisy-chain operation content and add CRC fault reset content..... 42
- Changed DCEN to Reserved for DB3 in Table 8-17, *Control Register* ..... 43
- Added series resistance for supply and 100-pF capacitance from CMP to GND for Figure 9-3..... 49
- Added text on fast supply ramp, series resistance for power supply, and content from deleted *Power-Supply Sequence* to the *Power Supply Recommendations* section..... 52
- Added power supply series resistance and CMP capacitor to GND to Figure 11-1, *Layout Example* ..... 54

The datasheet number will be changing.

Device Family	Change From:	Change To:
DACx750	SBAS538C	SBAS538D
DACx760	SBAS528C	SBAS528D

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/DAC8750IRHAT>

<http://www.ti.com/product/DAC7760IPWP>

**Reason for Change:**

To accurately reflect device characteristics.

**Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):**

No anticipated impact. This is a documentation change announcement only. There are no changes to the actual device.

**Changes to product identification resulting from this notification:**

None.

**Product Affected:**

DAC7750IPWP	DAC7750IPWPR	DAC7750IRHAR	DAC7750IRHAT
DAC8750IPWP	DAC8750IPWPR	DAC8750IRHAR	DAC8750IRHAT
DAC7760IPWP	DAC7760IPWPR	DAC7760IRHAR	DAC7760IRHAT
DAC8760IPWP	DAC8760IPWPR	DAC8760IRHAR	DAC8760IRHAT

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

<b>Location</b>	<b>E-Mail</b>
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
Europe	<a href="mailto:PCNEuropeContact@list.ti.com">PCNEuropeContact@list.ti.com</a>
Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
WW Change Management Team	<a href="mailto:PCN_ww_admin_team@list.ti.com">PCN_ww_admin_team@list.ti.com</a>

### **IMPORTANT NOTICE AND DISCLAIMER**

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property

right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale ([www.ti.com/legal/termsofsale.html](http://www.ti.com/legal/termsofsale.html)) or other applicable terms available either on [ti.com](http://ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.