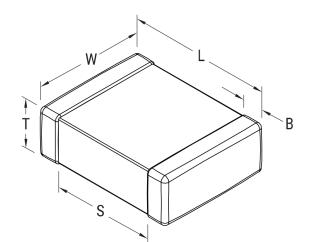


C0402C181F3GACAUTO

SMD Auto COG, Ceramic, 180 pF, 1%, 25 VDC, COG, SMD, MLCC, Ultra-Stable, Low Loss, Automotive Grade, 0402



Click here for the 3D model.

| Chip Size 0402 L 1mm +/-0.05mm W 0.5mm +/-0.05mm |
|--|
| , |
| W 0.5mm +/-0.05mm |
| |
| T 0.5mm +/-0.05mm |
| S 0.3mm MIN |
| B 0.3mm +/-0.1mm |

| Packaging Specifications | |
|--------------------------|------------------------|
| Packaging | T&R, 180mm, Paper Tape |
| Packaging Quantity | 10000 |

| General Information | |
|---------------------|--|
| Series | SMD Auto COG |
| Style | SMD Chip |
| Description | SMD, MLCC, Ultra-Stable, Low Loss, Automotive Grade |
| Features | Ultra-Stable, Low Loss, Automotive Grade |
| RoHS | Yes |
| Termination | Tin |
| Marking | No |
| Qualifications | AEC-Q200 |
| AEC-Q200 | Yes |
| Component Weight | 1.06 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Specifications | / |
|---|------------------------------|
| Capacitance | 180 pF |
| Measurement Condition | 1 MHz 1.0Vrms |
| Capacitance Tolerance | 1% |
| Voltage DC | 25 VDC |
| Dielectric Withstanding Voltage | 62.5 VDC |
| Temperature Range | -55/+125°C |
| Temperature Coefficient | COG |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1MegaHz 1.0Vrms |
| Dissipation Factor | 0.1% 1 MHz 1.0Vrms |
| Aging Rate | 0% Loss/Decade Hour |
| Insulation Resistance | 100 GOhms |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.