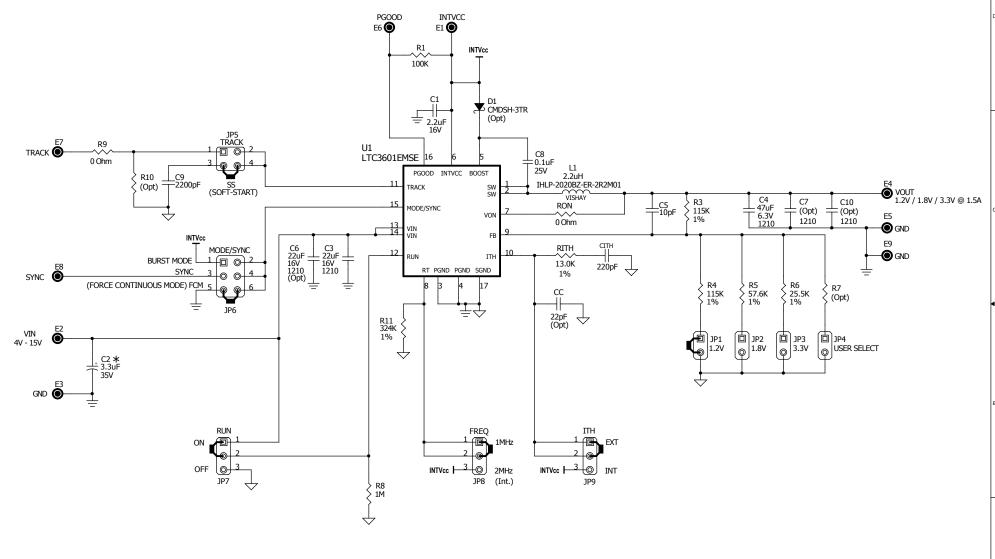
REVISION HISTORY										
ECO	REV	DESCRIPTION	APPROVED	DATE						
1	1	PRODUCTION	TOM GROSS	08-14-09						





C2 IS AN OPTIONAL CAPACITOR. IT IS INSERTED ON THE DC1454A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.

	CUSTOMER NOTICE		CONTRACT NO.			INEAD	1630 McCarthy Blv Milpitas, CA 95035			
	LINEAR TECHNOLOGY HAS MADE A BEST		APPROVALS		_#		Phone: (408)432-19 Fax: (408)434-0507	00		
		IRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; OWEVER. IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO				ECHNOLOGY	LTC Confidential-F	or Custom	er Us	e Only
	ERIFY PROPER AND RELIABLE OPERATION		ENG. TOM GROSS	TITLE:	SCHEMATIC					
	APPLICATION. COMPONENT SUBSTITUTION CIRCUIT BOARD LAYOUT MAY SIGNIFICAL			1.5A	MONOLIT	THIC SYNCHR	ONOUS BUCK	(REGUL	AT(OR
	PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE. THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.			SIZE	IC NO.	LTC360	1EMSE			REV
				A DEMO CIRO			CUIT 1454A	١		1
				DATE:	Wednesda	y, October 21, 200	9	SHEET	1 (OF 1
	3		2				1			