

Product Advisor (PA)

Subject: Datasheet Specification Change for the listed ISL9237* Products

Publication Date: 5/13/2016

Effective Date: 5/13/2016

Revision Description:

Initial Release

Description of Change:

This notice is to inform you that Intersil has updated Table 18 Prog pin to GND resistance values associated with charger programming options.

Affected Products:

ISL9237HRZ	ISL9237HRZ-TS2568	ISL9237HRZ-TS2722	ISL9237HRZ-TS2780
ISL9237HRZ-T	ISL9237HRZ-T7A	ISL9237HRZ-TK	

Reason for Change:

The change provides improved guidelines for resistor selection for programming options. Details regarding the change are contained on the following page. For a copy of the updated datasheet, please contact your local sales representative.

Impact on fit, form, function, quality & reliability:

The change will have no impact on the form, fit, function, quality, reliability and environmental compliance of the devices.

Product Identification:

Product affected by this change is identifiable via Intersil's internal traceability system.

Qualification status: Not applicable

Sample availability: 5/13/2016

Device material declaration: Available upon request

Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: PCN-US@INTERSIL.COM	Europe: PCN-EU@INTERSIL.COM	Japan: PCN-JP@INTERSIL.COM	Asia Pac: PCN-APAC@INTERSIL.COM

From:

To:

TABLE 18. PROG PIN PROGRAMMING OPTIONS

PROG-GND RESISTANCE (kΩ)	CELL NUMBER	DEFAULT SWITCHING FREQUENCY	DEFAULT AdapterCurrentLimit1 Register (A)
TYP 1%			
0	1-cell	733kHz	0.1
16.9			0.476
31.6			1.5
44.2		1MHz	0.476
59			1.5
73.2			0.1
86.6			0.1
102	2-cell	733kHz	0.476
118			1.5
133			0.476
147		1MHz	1.5
162			0.1
178			0.476
191			1.5
207 (Note 8)	3-cell	1MHz	0.476
232			1.5

NOTE:
8. 207kΩ is not standard resistor; use two resistors in series or in parallel to get the closest value; or use 208kΩ.

TABLE 18. PROG PIN PROGRAMMING OPTIONS

PROG-PIN RESISTOR (kΩ)			BATTERY CELL NUMBER	DEFAULT SWITCHING FREQUENCY	DEFAULT AdapterCurrent Limit1 Register (A)
MIN.	VALUE 1%	MAX.			
	0		1-cell	733kHz	0.1
16.6	16.9	17.2			0.476
31.1	31.6	32.1			1.5
43.5	44.2	44.9		1MHz	0.476
58.1	59	59.9			1.5
72.1	73.2	74.3			0.1
85.3	86.6	87.9	2-cell	733kHz	0.1
101	102	103			0.476
113.9	115	116.2			1.5
128.7	130	131.3		1MHz	0.476
141.6	143	144.4			1.5
156.4	158	159.6			3-cell
172.3	174	175.7	0.476		
185.1	187	188.9	1.5		
201	203 (Note 8)	205	1MHz	0.476	
218.8	221	223.2		1.5	

NOTE:
8. 203kΩ is not standard resistor; use two resistors in series or in parallel to get the closest value.