

Customer Information Notification

Issue Date: 26-Jul-2019 Effective Date: 27-Jul-2019

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Management Summary

Update to LS1046A/26A DataSheet. Wrong information were shown for the Differential SerDes reference clock DC requirments in figure 80.

201907015I

Change C	ategory

Change Calegory				
[] Wafer Fab Process	[] Assembly Process	[] Product Marking	[] Test Location	[] Design
[] Wafer Fab Materials	[] Assembly Materials	[] Mechanical Specification	n[]Test Process	[] Errata
[] Wafer Fab Location	[] Assembly Location	[] Packing/Shipping/Labeling	[] Test Equipment	[X] Electrical spec./Test coverage
[] Firmware	[] Other			J

LS1046A/26A DataSheet Update

Description

Correction to the LS1046A/26A DataSheet. Updates to figure 80 in section 3.23.2.3 DC-level requirements for SerDes reference clock.

Reason

Wrong information shown for the Differential SerDes reference clock DC requirments in figure 80 for Rev 2 of the LS1046A/26A DataSheet.

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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NXP Quality Management Team.

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Affected Part Numbers	Affected 12NC
LS1026ASN8P1A	935313234557
LS1046ASN8MQA	935316391557
LS1046ASE8MQA	935318834557
LS1046ASN8Q1A	935318912557
LS1046AXN8MQA	935341004557
LS1046ASN8P1A	935325099557
LS1046ASE8T1A	935338066557
LS1046ASN8T1A	935338067557
LS1046AXN8T1A	935338069557
LS1046AXN8P1A	935323889557