

C1812A104J5XAH7189

GR900 SMD Space, Ceramic, 0.1 uF, 5%, 50 VDC, BX, N/A, 1812



Click here for the 3D model.

| Dimensions | |
|------------|---|
| Chip Size | 1812 |
| L | 4.57mm +/-0.38mm (0.18 in +/-0.015 in) |
| W | 3.18mm +/-0.38mm (0.125 in +/-0.015 in) |
| Т | 2.03mm MAX (0.08 in MAX) |
| В | 0.51mm +/-0.25mm (0.02 in +/-0.01 in) |

| Packaging Specifications | |
|--------------------------|-------------------|
| Packaging | T&R, Plastic Tape |

| General Information | |
|----------------------------|--|
| Series | GR900 SMD Space |
| Style | SMD Chip |
| RoHS | No |
| Prop 65 | ▲ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov. |
| Termination | Lead (SnPb) |
| Failure Rate | N/A |
| Testing and Reliability | GR900 CSAM A |
| AEC-Q200 | No |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Specifications | |
|-------------------------|------------|
| Capacitance | 0.1 uF |
| Capacitance Tolerance | 5% |
| Voltage DC | 50 VDC |
| Temperature Range | -55/+125°C |
| Temperature Coefficient | BX |

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