

# **Customer Information Notification**

Issue Date: 12-Aug-2020 Effective Date: 13-Aug-2020

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# 2020070361

[X] Other - Data Sheet Update for correction and clarification



| Change Category        |  |                                   |          |                |
|------------------------|--|-----------------------------------|----------|----------------|
| [] Wafer Fab Process   | [] Assembly                                  | [] Product Marking                | [] Test  | [] Design      |
|                        | Process                                      |                                   | Location |                |
| [] Wafer Fab Materials | [] Assembly                                  | [] Mechanical Specification[]Test |          | [] Errata      |
|                        | Materials                                    |                                   | Process  |                |
| [] Wafer Fab Location  | [] Assembly                                  | []                                | [] Test  | [X] Electrical |
|                        | Location Packing/Shipping/Labeling Equipment |                                   |          | spec./Test     |
|                        |  |                                   |          | coverage       |

i.MX RT1060 Data Sheet IMXRT1060CEC/IEC Update to

Rev 1

[] Firmware

## **Description**

NXP Semiconductors announces data sheet update to revision 1 for i.MX RT1060. The revision history included in the updated document provides a detailed description of the changes. Changes are summarized below.

- 1. Changed the MPCore to core in the Section 1.1
- 2. Added new part numbers in the Table 1, Ordering information
- 3. Updated the Figure 1, "Part number nomenclature--i.MX RT1060"
- 4. Corrected the frequency of RTC OSC in the Table 2 from 32.678kHz to 32.768kHz
- 5. Updated conditions and maximum current of DCDC\_IN in the Table 12, Maximum supply currents from 100mA to 110mA
- 6. Added a restrictions in the Section 4.2.1.1, Power-up sequence
- 7. Added typical values of IOH and IOL in the Table 22, Single voltage GPIO DC parameters
- 8. Corrected the minimum and maximum values in the Table 33, SEMC output timing in SYNC mode

The i.MXRT1060 data sheet revision 1 is attached to this notice, and can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i.mx-rt-series/i.mx-rt1060-crossover-processor-with-arm-cortex-m7-core:i.MX-RT1060?tab=Documentation\_Tab&linkline=Data-Sheet

## Reason

The data sheets have been updated to correct errors and / or provide additional technical clarification on some device features.

### **Identification of Affected Products**

Product identification does not change

## Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

#### **Data Sheet Revision**

A new datasheet will be issued

## **Contact and Support**

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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## NXP Quality Management Team.

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High Tech Campus, 5656 AG Eindhoven, The Netherlands

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### **Affected Part Numbers**

MIMXRT1062CVL5A

MIMXRT1062DVL6A

MIMXRT1061CVJ5A

MIMXRT106LDVL6A

MIMXRT1062DVJ6A

MIMXRT106ADVL6A

MIMXRT106FDVL6A

MIMXRT1062CVJ5A