**MACOM** 

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15 September 2020

Subject: MAAP-011232 Datasheet Change

**PCN #:** PCN-01360

**Dear Valued Customer:** 

The goal of MACOM Technology Solutions is to continually deliver high quality products and services that meet our customers' needs. We strive to offer products that are industry leading in terms of performance, delivery, safety and value.

In accordance with these goals, this communication is to inform you that MACOM is making a change to the following product's datasheet.

MAAP-011232 MAAP-011232-TR0500 MAAP-011232-TR1000

Min/Max specification limits for Gate Bias Voltage (VG) are being added to the data sheet. Details of the change are on the following page. There are no changes to design, form, fit, function, or reliability associated with this change.

Please contact your local sales representative if you have any questions or require additional information.

Sincerely,

**Alan Miller** 

Product Marketing Manager MACOM

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# MAAP-011232 Current Rev V2 Datasheet

# 1 W Driver Amplifier with VG Enable 0.1 - 3.0 GHz

Rev. V2

# **Electrical Specifications:**

Freq. = 1 GHz,  $T_A$  = +25°C,  $V_D$ 1 =  $V_D$ 2 = 9 V,  $I_{DQ}$ 2 = 200 mA,  $Z_0$  = 50  $\Omega$ ,  $V_G$  pulsed with 1 ms pulse width and 10% duty cycle

Parameter	Symbol	Test Conditions	Units	Min.	Тур.	Max.
Small-Signal Gain	SSG	-10 dBm input drive level	dB	23	24.5	_
Output Power at 1dB compression	P <sub>-1dB</sub>	_	dBm	_	29	-
Saturated Output Power	P <sub>SAT</sub>	3 dB Gain compression	dBm	28.5	30	_
Power Added Efficiency	PAE	3 dB Gain compression	%	35	40	_
Reverse Isolation	S12	-10 dBm input drive level	dB	_	50	_
Input Return Loss	IRL	-10 dBm input drive level	dB	_	8	_
Output Return Loss	ORL	-10 dBm input drive level	dB	_	12	_
Output Third Order Intercept	OIP3	-13 dBm/tone, F1-F2 = 6 MHz	dBm	_	40	_
Gate Bias Voltage	V <sub>G</sub>	_	V	_	-0.55	_
Quiescent Drain Current	I <sub>DQ</sub> 1	_	mA	_	65	_

# MAAP-011232 New Rev V3 Datasheet

MAAP-011232 Rev. V3

# **Electrical Specifications:**

Freq. = 1 GHz,  $T_A$  = +25°C,  $V_D$ 1 =  $V_D$ 2 = 9 V,  $I_{DQ}$ 2 = 200 mA,  $Z_0$  = 50  $\Omega$ ,  $V_G$  pulsed with 1 ms pulse width and 10% duty cycle

Parameter	Symbol	Test Conditions	Units	Min.	Тур.	Max.
Small-Signal Gain	SSG	-10 dBm input drive level	dB	23	24.5	
Output Power at 1dB compression	P <sub>-1dB</sub>	_	dBm	_	29	
Saturated Output Power	P <sub>SAT</sub>	3 dB Gain compression	dBm	28.5	30	==0
Power Added Efficiency	PAE	3 dB Gain compression	%	35	40	-
Reverse Isolation	S12	-10 dBm input drive level	dB	_	50	-
Input Return Loss	IRL	-10 dBm input drive level	dB	10	8	
Output Return Loss	ORL	-10 dBm input drive level	dB	-	12	_
Output Third Order Intercept	OIP3	-13 dBm/tone, F1-F2 = 6 MHz	dBm	_	40	_
Gate Bias Voltage	V <sub>G</sub>	3 dB Gain compression	٧	-0.75	-0.55	-0.30
Quiescent Drain Current	I <sub>DQ</sub> 1	_	mA	_	65	_