



ONE CHANNEL BIDIRECTIONAL TVS DIODE

Product Summary

VBR (Min)	IPP (Max)	Ст (Тур)
40V	4.5A	22pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Switches / buttons
- Portable electronics
- Computers and peripherals
- Automotive electronics

Features

- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- Bidirectional Configuration
- Ultra Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES[™] SD36CQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOD323
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.004 grams (Approximate)

SOD323





Top View

Device Schematic

Ordering Information (Note 4)

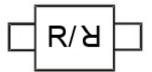
Part Number	nber Package Marking Reel Size (inches)		Tape Width (mm)	Packing		
Fait Number	Fackage	Warking	Reel Size (Iliches)	rape widin (ililii)	Qty.	Carrier
SD36CQ-7	SOD323	R/A	7	8	3,000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Option A: Made in Shanghai



Option B: Made in Chengdu



R/ \(\) = Product Type Marking Code



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power	Ppp	300	W	8/20µs, per Figure 3
Peak Pulse Current	IPP	4.5	Α	8/20µs, per Figure 3
ESD Protection – Contact Discharge	VESD_CONTACT	±30	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	Vesd_air	±30	kV	IEC 61000-4-2 Standard

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _θ JA	500	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions	
Reverse Standoff Voltage	VRWM	_	_	36.0	V	_	
Channel Leakage Current (Note 6)	I _{RM}	_	10	500	nA	V _R = 36V	
Breakdown Voltage	V _{BR}	40	_	_	V	I _R = 1mA	
Olaman'a a Malka a a		_	_	50	V	$I_{PP} = 1A$, $t_P = 8/20 \mu s$	
Clamping Voltage	\/	_	_	60		$I_{PP} = 4.5A$, $t_P = 8/20\mu s$	
Clamping Voltage (Note 7)	VcL	_	47.3	_	V	I _{PP} = 16A, TLP = 10/100ns	
		_	52.4	_		I _{PP} = 30A, TLP = 10/100ns	
Channel Input Capacitance	Ст		22	_	pF	$V_R = 0V$, $f = 1MHz$	

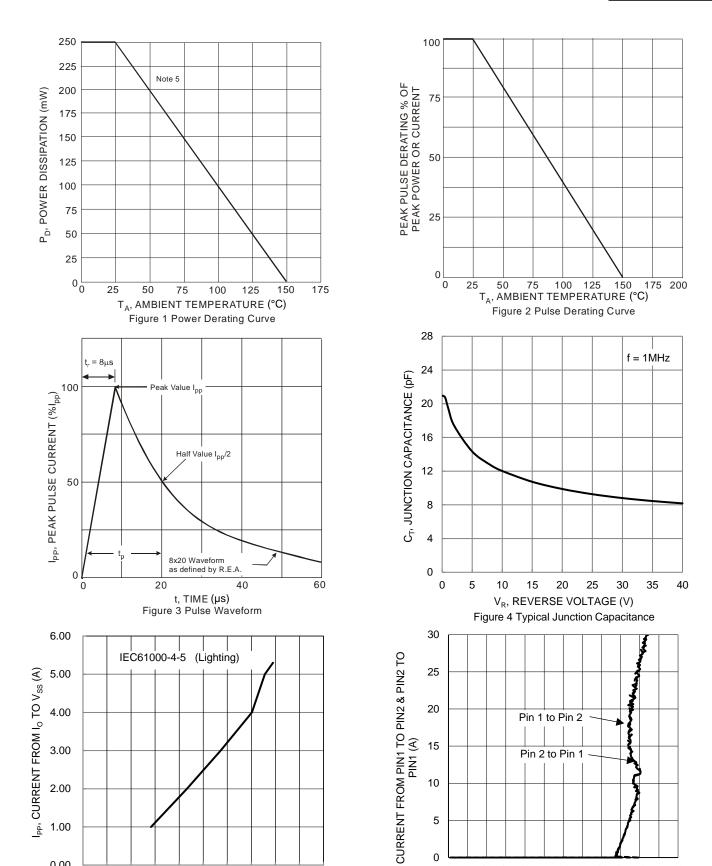
Notes:

^{5.} Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

6. Short duration pulse test used to minimize self-heating effect.

^{7.} Transmission Line Pulse Test (TLP) settings: $t_P = 100$ ns, $t_R = 10$ ns, t_{TLP} and V_{TLP} averaging window is from 70ns to 90ns.





48

50 52

 $\rm V_{\rm C}$ FROM $\rm I_{\rm O}$ to $\rm V_{\rm SS}$ (V) Figure 5 Clamping Voltage Characteristic

54 56 58

0.00

40 42 44 60

5 10 15 20 25 30 35 40 45 50 55 60

VOLTAGE FROM PIN1 TO PIN2 & PIN2 TO PIN1 (V)

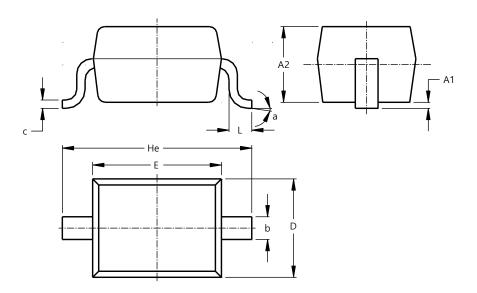
Figure 6 TLP Curve (t_P = 100ns)



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOD323

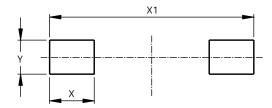


SOD323					
Dim	Min	Max	Тур		
A1		0.10	0.05		
A2	1.00	1.10	1.05		
b	0.25	0.35	0.30		
С	0.10	0.15	0.11		
D	1.20	1.40	1.30		
Е	1.60	1.80	1.70		
He	2.30	2.70	2.50		
L	0.20	0.40	0.30		
а	00	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOD323



Dimensions	Value (in mm)
X	0.590
X1	2.700
Υ	0.450



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