PCN Number: 20190410000.1 PCN Date: Apr 11, 2019 Qualification of RFAB and DMOS6 as an additional Wafer Fab Site options for select Title: devices in LBC8 Technology **Customer Contact: PCN Manager** Dept: Quality Services **Estimated Sample** Date provided at **Proposed 1st Ship Date:** Jul 11, 2019 **Availability:** sample request. **Change Type: Assembly Site Assembly Process Assembly Materials Electrical Specification** Mechanical Specification Design **Test Site** Packing/Shipping/Labeling Test Process Wafer Bump Site Wafer Bump Material Wafer Bump Process Wafer Fab Process Wafer Fab Site Wafer Fab Materials Part number change **PCN Details Description of Change:**

Texas Instruments is pleased to announce the qualification of its RFAB and DMOS6 fabrication facilities as an additional Wafer Fab sources for the selected devices listed in "Product Affected" section.

Current Sites			Additional Sites		
Current Fab Site	Process	Wafer Diameter	Additional Fab Site	Process	Wafer Diameter
DP1DM5	LPCO	200mm	RFAB	LBC8	300mm
DPIDMS	LBC8	200mm	DMOS6	LBC8	200mm

Qual details are provided in the Qual Data Section.

Reason for Change:

Continuity of Supply

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

Changes to product identification resulting from this PCN:

Current

LBL:

(Chip Site	Chip Site Origin (20L)	Chip Site Country Code (21L)	Chip Site City
	DP1DM5	DM5	USA	Dallas

New Fab Site

Chip Site	Chip Site Origin (20L)	Chip Site Country Code (21L)	Chip Site City
RFAB	RFB	USA	Richardson
DMOS6	DM6	USA	Dallas

Sample product shipping label (not actual product label)





(1P) SN74LS07NSR (a) 2000 (P) 0336 31T)LOT: 3959047MLA 4W) TKY(1T) 7523483S12 (P) (20L) CSO: SHE (21L) CCO:USA (22L) ASO: MLA (23L) ACO: MYS

Product Affected Group:

TMP461AIRUNR TMP461AIRUNT

Qualification Report

Approve Date 26-Mar-2019

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Name / Condition	Duration	Qual Device: TMP461AIRUN	QBS Product Reference TMP461AIRUN	QBS Process Reference: PCD3215B00BZQZR/ PCD3215B00DZQZR
ED	Electrical Characterization	Per Datasheet Parameters	1/30/0	Pass	-
HAST	Biased HAST, 130C/85%RH	96 Hours	1/77/0	-	-
HBM	ESD - HBM	2000 V	1/3/0	1/3/0	-
CDM	ESD - CDM	1000 V	1/3/0	1/3/0	-
HTOL	Life Test, 125C	1000 Hours	1/77/0	-	2/154/0
HTSL	High Temp. Storage Bake, 170C	420 Hours	1/77/0	-	-
HTSL	High Temp. Storage Bake, 150C	1000 Hours	-	-	2/154/0
LU	Latch-up	(per JESD78)	1/6/0	1/6/0	-
PD	Physical Dimensions	Per mechanical drawing	-	3/45/0	-
SD	Solderability	Pb Free	-	3/66/0	-
TC	Temperature Cycle, -65/150C	500 Cycles	1/77/0	3/231/0	-
UHAST	Unbiased HAST, 130C/85%RH	96 Hours	1/77/0	3/231/0	-

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

 The following are equivalent HTOL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free (SMT) and Green

For questions regarding this notice, e-mails can be sent to the regional contacts shown below, or you can contact your local Field Sales Representative.

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USA	PCNAmericasContact@list.ti.com
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