

**ABRACON, LLC
ENGINEERING/PROCESS CHANGE NOTIFICATION FORM**

ABRACON ENGINEERING ORIGINATOR:

IMPLEMENTATION DATE: 03/15/2018

Brooke Cushman – Associate Engineer

SCD/DRAWING AFFECTED:

AP2S Series

NOTIFICATION DATE:

CMOS Output Programmable

03/15/2018

SMD Crystal Clock Oscillator

REV: L

NEW REV: M

EFFECTIVITY DATE: 03/15/2018

ECO# 3284

REASON FOR CHANGE:

**Certain frequencies removed from offering due to low demand.
Additional instructions for part usage included.**

DETAILS OF SPECIFICATION CHANGE:

APPLICATION INFORMATION

1. Frequency range removed from offering:
 - 1.000MHz ~ 2.047MHz
2. Added *Note 1 to pin# & function table:
 - Do not leave Pin 1 (Tri-State) floating
 - If Pin 1 (Tri-State) is not utilized for toggling, it must be tied to Vdd (logic 1)

Safety

Non Safety

Application:

(Check one)

O

DISPOSITION OF CURRENT STOCK

Scrap

Transfer to: _____

Use as is

Return to vendor

ABRACON INTERNAL APPROVAL:

APPROVAL (PRES) Mike Calabria DATE 03/15/2018

GLOBAL QUOTING/PRICING MGR Rachael Gough DATE 03/15/2018

ENGINEERING VP Syed Raza DATE 03/15/2018

PURCHASING MGR Ying Huang DATE 03/15/2018

SALES VP Mike White DATE 03/15/2018

QUALITY & PROCEESS IMPROVEMENT MGR: Reuben Quintanilla DATE 03/15/2018

CUSTOMER APPROVAL (If Applicable)

ENGINEERING: NAME: _____ TITLE: _____ DATE: _____

BUYER/PURCHASING: NAME: _____ TITLE: _____ DATE: _____