

Dear Customer,

Please find attached our INFINEON Technologies PCN:

Decommission of Newport, Wales wafer fab, as announced on May 5th 2015

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before **16-April-2017**.
- Infineon aligns with the widely-recognized JEDEC STANDARD “**JESD46**“, which stipulates:
“**Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change.**”

Your prompt reply will help Infineon Technologies to assure a smooth and well executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Infineon Technologies AG
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Chairman of the Supervisory Board: Wolfgang Mayrhuber
Management Board: Dr. Reinhard Ploss (CEO), Dominik Asam, Dr. Helmut Gassel, Jochen Hanebeck
Registered Office: Neubiberg Commercial Register
Amtsgericht München HRB 126492

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► Products affected:

Sales Name	SP N°	OPN	Package
IRSM506-076PA	SP001549848	IRSM506-076PA	SOP23

► Detailed Change Information:

Subject: Transfer of HVIC wafer fabrication processing from Newport, Wales to NXP Oak Hill and transfer of IGBT 6.2 backend of line processing to IFX Kulim.

Reason: As part of a manufacturing consolidation plan announced in March 2015, Infineon indicated that it would be closing the Fab11 facility and transferring the production capability for certain parts to established SiFo partners.

Description: Decommission of Newport, Wales wafer fab, as announced on May 5th 2015

Gen 5 HVIC and IGBT 6.2

As the result of this change

	OLD	NEW
HVIC Wafer Fab Location	Infineon Technologies Newport Limited	NXP Oak Hill, Austin Texas, US
IGBT Wafer backend Location	Infineon Technologies Newport Limited	Infineon Technologies AG, Kulim, Malaysia

► Product Identification: Wafer fab origins will not be distinguished through the external part marking or shipping labels, however, based on the assembly lot code and marked on the device. IFX internal systems will be able to trace the site of the wafer lots used to produce the final product.

► Impact of Change: No changes to form, fit, or function. The package outline remains the same. Datasheet specifications will not be changed. The package bill of material and the location for assembly and final test will not change.

► Attachments:

► Time Schedule:

- Final qualification report: 4/07/2017
- First samples available: On request
- Intended start of delivery: 28-April-2017

If you have any questions, please do not hesitate to contact your local Sales office.