

C0603T183J5RCLTU

Aliases (C0603T183J5RCL7867)

SMD COTS X7R, Ceramic, 0.018 uF, 5%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 0603



Click here for the 3D model.

| Dimensions | |
|------------|------------------|
| Chip Size | 0603 |
| L | 1.6mm +/-0.15mm |
| W | 0.8mm +/-0.15mm |
| Т | 0.8mm +/-0.07mm |
| S | 0.7mm MIN |
| В | 0.35mm +/-0.15mm |

Packaging Specifications

Packaging Packaging Quantity

T&R, 180mm, Paper Tape 4000

| General Information | | |
|---------------------|---|--|
| Series | SMD COTS X7R | |
| Style | SMD Chip | |
| Description | SMD, MLCC, COTS, Temperature Stable, Class II | |
| Features | Temperature Stable, Class II | |
| RoHS | No | |
| Prop 65 | WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov. | |
| Termination | Lead (SnPb) | |
| Marking | No | |
| Failure Rate | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA- 469, Humidity per MIL-STD-202, Method 103, Condition A | |
| AEC-Q200 | No | |
| Component Weight | 4.8 mg | |
| Shelf Life | 78 Weeks | |
| MSL | 1 | |

| Specifications | |
|---|--|
| Capacitance | 0.018 uF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Capacitance Tolerance | 5% |
| Voltage DC | 50 VDC |
| Dielectric Withstanding Voltage | 125 VDC |
| Temperature Range | -55/+125°C |
| Temperature Coefficient | X7R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms |
| Dissipation Factor | 2.5% 1 kHz 1.0Vrms |
| Aging Rate | 3% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance | 55.5556 GOhms |

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