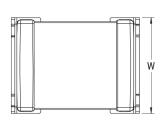


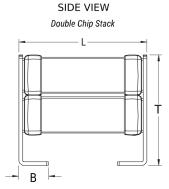
a YAGEO company

C1210C104M1R2CAUTO

KPS SMD Auto X7R, Ceramic, 0.1 uF, 20%, 100 VDC, X7R, 1210-2

TOP VIEW Single or Double Chip Stack





Click here for the 3D model.

Dimensions	
L	3.5mm +/-0.3mm
W	2.6mm +/-0.3mm
Т	6.15mm +/-0.15mm
В	0.8mm +/-0.15mm

Packaging Specifications			
Packaging	T&R, 180mm, Plastic Tape		
Packaging Quantity	300		

Double Chip, Class II,

Specifications	
Capacitance	0.1 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	20%
Voltage DC	100 VDC
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	10 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.