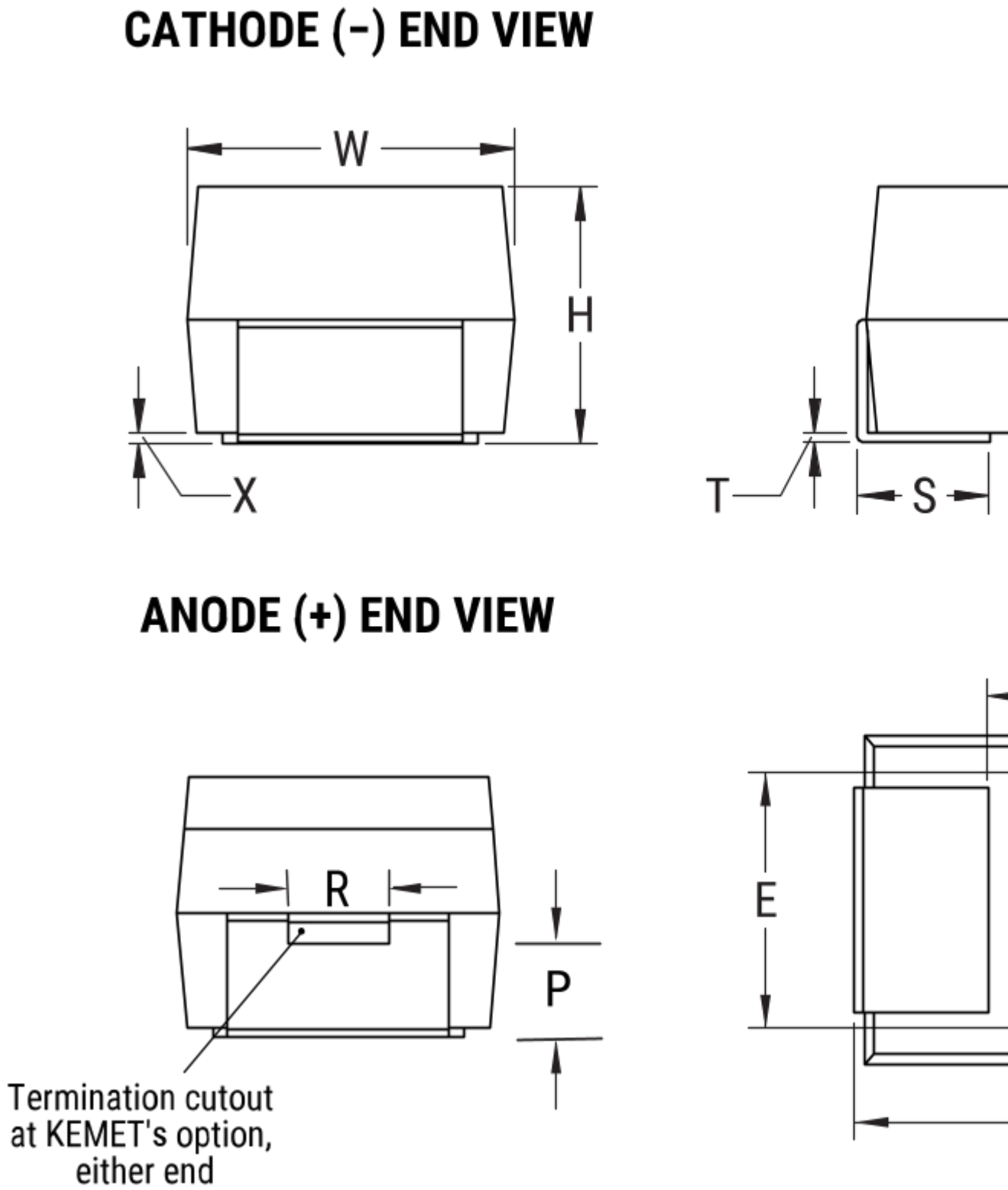
T541X686K025AT6510

T541 HRA, Tantalum, Polymer Tantalum, HRA Multi-Anode, 68 uF, 10%, 25 VDC, SMD, Polymer, Molded, High Reliability, Multi-Anode, Low ESR, N/A, 50 mOhms, 7343, Height Max = 4.3mm



Click here for the 3D model.

Dimensions

Footprint	7343
L	7.3mm +/-0.3mm
W	4.3mm +/-0.3mm
H	4mm +/-0.3mm
T	0.13mm REF
S	1.3mm +/-0.3mm
F	2.4mm +/-0.1mm
A	3.8mm MIN
B	0.5mm +/-0.15mm
E	3.5mm REF
G	3.5mm REF
P	1.7mm REF
R	1mm REF
X	0.1mm +/-0.1mm

Packaging Specifications

Packaging	T&R, 178mm
Packaging Quantity	500

General Information

Series	T541 HRA
Dielectric	Polymer Tantalum
Style	SMD Chip
Description	SMD, Polymer, Molded, High Reliability, Multi-Anode, Low ESR
Features	Non-Combustible, Multiple Anode, Low ESR, High Reliability
RoHS	Yes
Termination	Tin
AEC-Q200	No
Component Weight	410.89 mg
Shelf Life	52 Weeks
MSL	3

Specifications

Capacitance	68 uF
Capacitance Tolerance	10%
Voltage DC	25 VDC (105C), 16.75 VDC (125C)
Temperature Range	-55/+125°C
Rated Temperature	105°C
Humidity	60C, 90% RH, 500 Hours, rated voltage
Dissipation Factor	10% 120Hz 25C
Failure Rate	N/A
Resistance	50 mOhms (100kHz 25C)
Ripple Current	2324 mA (rms, 100kHz 45C)
Leakage Current	170 uA (5min 25°C)
Testing and Reliability	4 Cycles At +25C +/-5C Before Voltage Aging

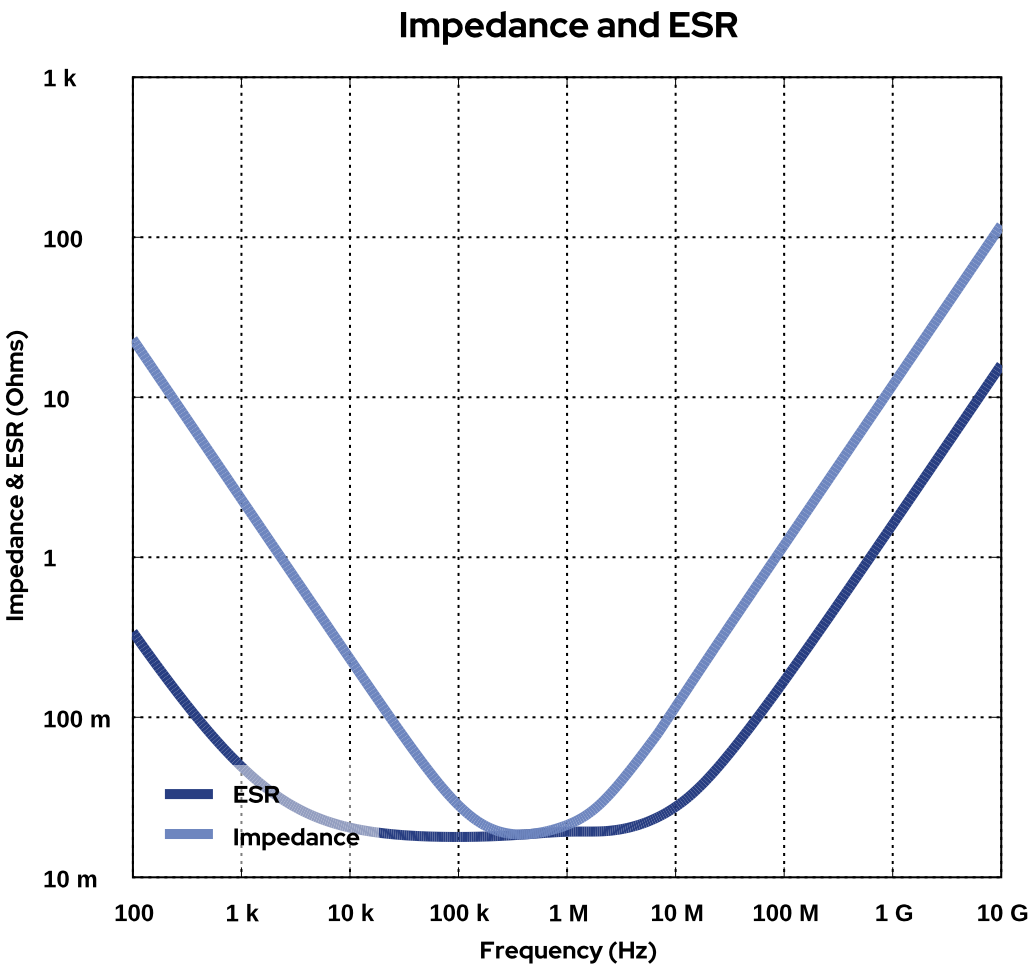
for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

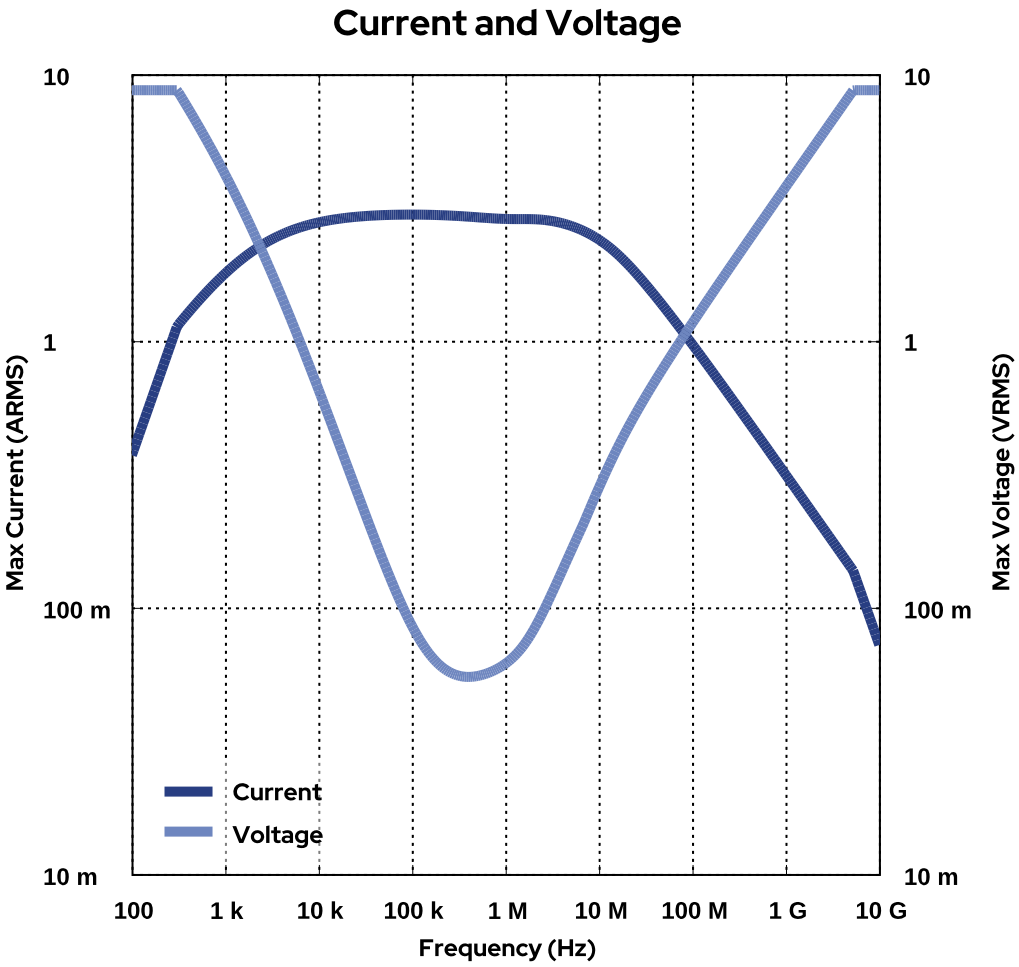
Generated 5/18/2023 - db428942-5de3-4bc3-94af-40d62686ea8f

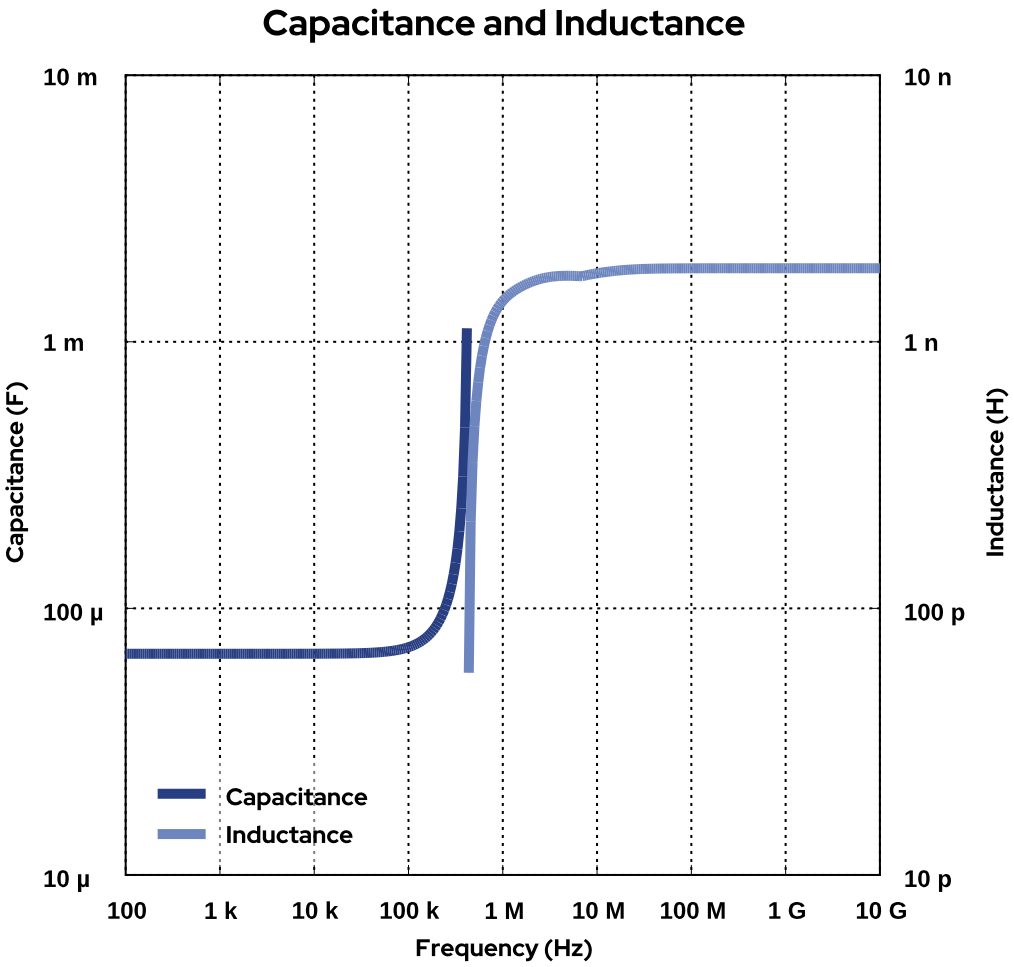
Generated 5/18/2023 - db428942-5de3-4bc3-94af-40d62686ea8f
© 2006 - 2023 KEMET

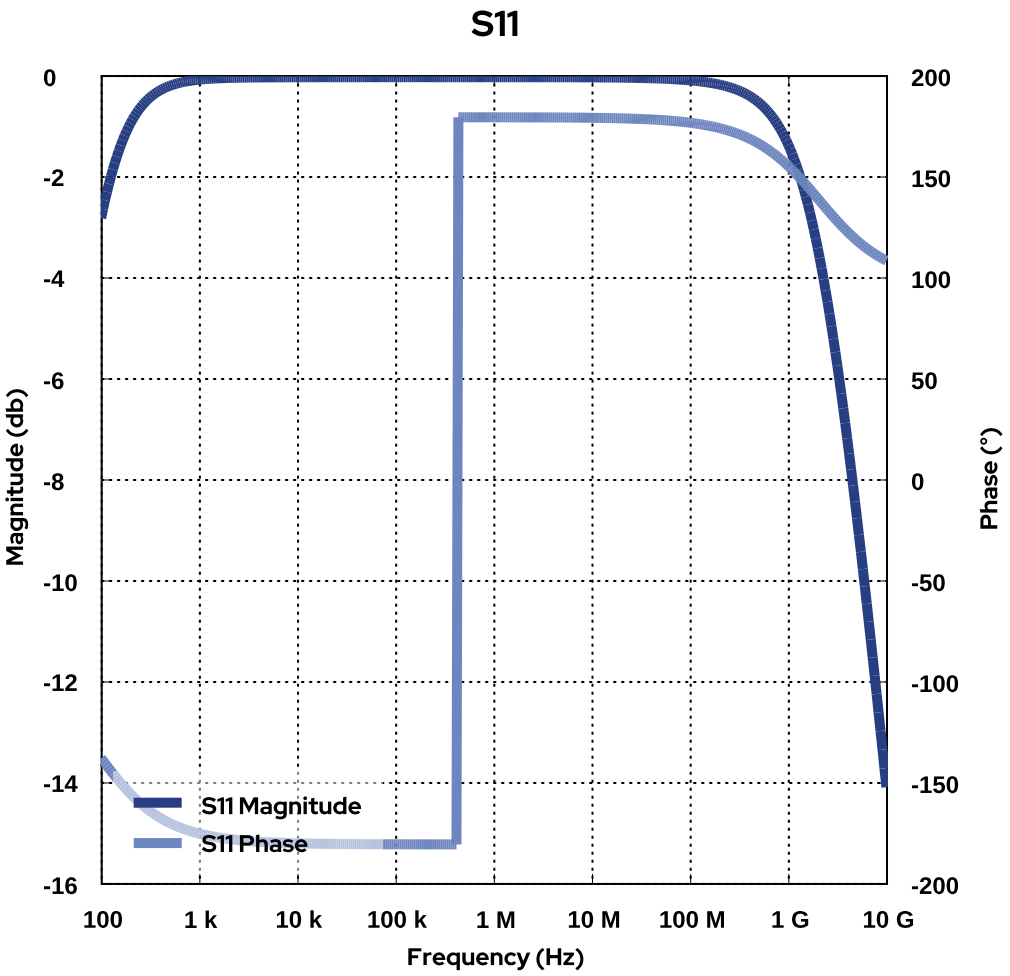
Simulations

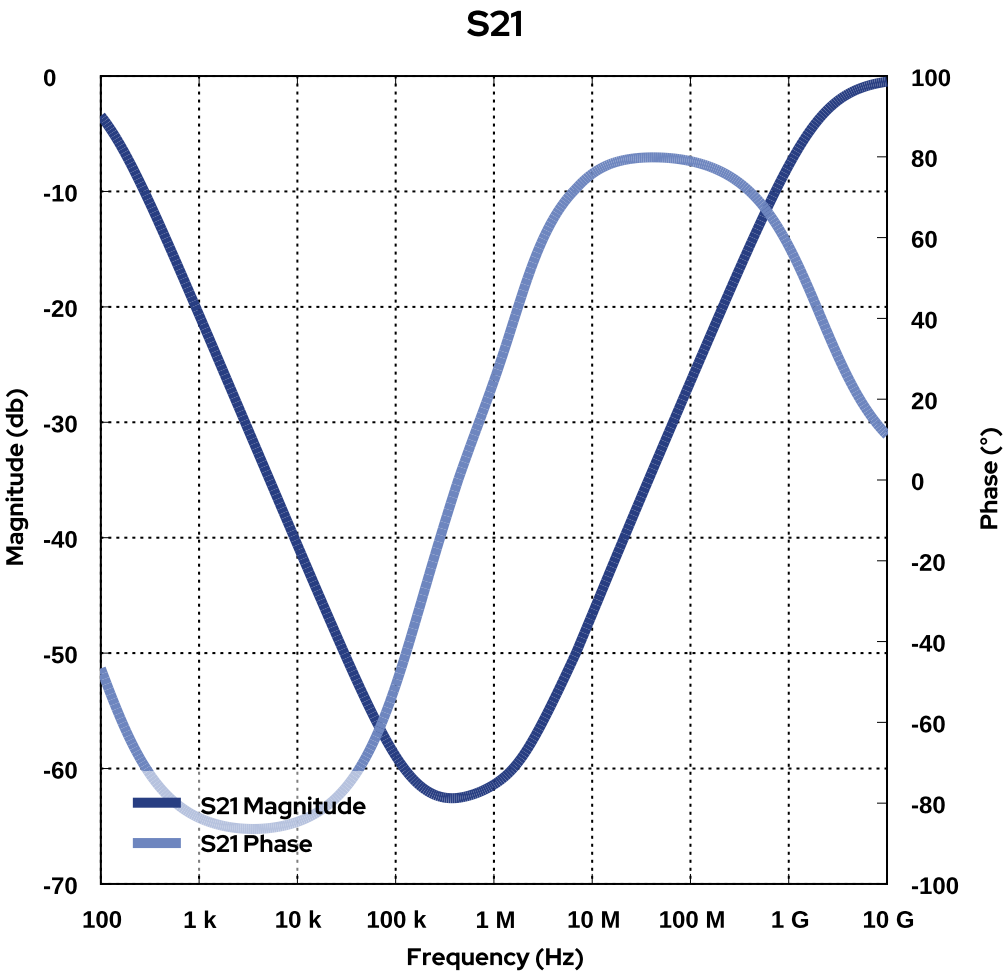
For the complete simulation environment please visit K-SIM.




Current and Voltage
10
1
100 m
10 m
Max Current (ARMS)
10
1
100 m
10 m
Max Voltage (VRMS)
Current
Voltage
100
1 k
10 k
100 k
1 M
10 M
100 M
1 G
10 G
Frequency (Hz)


Capacitance and Inductance
10 m
1 m
100 µ
10 µ
Capacitance (F)
10 n
1 n
100 p
10 p
Inductance (H)
Capacitance
Inductance
100
1 k
10 k
100 k
1 M
10 M
100 M
1 G
10 G
Frequency (Hz)


S11
0
-2
-4
-6
-8
-10
-12
-14
-16
200
150
100
50
0
-50
-100
-150
-200
Magnitude (db)
Phase (°)
S11 Magnitude
S11 Phase
100
1 k
10 k
100 k
1 M
10 M
100 M
1 G
10 G
Frequency (Hz)



These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple “Ripple Current/Voltage vs. Frequency” plots is the ESR at ambient temperature.
- The ESR in the “Temperature Rise vs. Ripple Current” plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
- The peak voltages generated in the “Temperature Rise vs. Combined Ripple Currents” plot are

calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the “Information”) are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

All Information given herein is believed to be accurate and reliable, but is presented without guarantee, warranty, or responsibility of any kind, expressed or implied. Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute – and we specifically disclaim – any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

If you have any questions please contact K-SIM.