PCN Number: 202				200622001.2 PC				CN Date:	June 23, 2020			
Title:	AD3421-	Q1 Die Re	vision Change, Marking Change, and Datashee				heet	et Updates				
Customer Contact:				PCN Manager			Dept: Qualit			Quality S	ality Services	
Proposed 1 st Ship Date:			Dec 23, 2020 Estimated Sample			mple	Date provided at sample request.					
Chan	ge Type:									1.240.220		
	Assembly Site		Assembly Process				Assembly Materials					
	Design		Electrical Specification					Mechanical Specification				
	Test Site		Packing/Shipping/Labeling				Test Proce	ess				
	Wafer Bump Site			Wafer Bump Material				Wafer Bun	np Process			
	Wafer Fab Site			Wafer Fab Materials			Wafer Fab	Process				
				Part num	iber c	<u>change</u>						
					PCN	Details						
This r to rer The D	notification is to i move the require Die Revision and	nform of ment to k the datas	a des ceep s heet	sign chang SEN at low number w	e to t v stat ill be	the AD3421- e while apply changing:	Q1 ⁄ing	device f J Hardwa	ami are i	ly. This is a reset.	a minor metal spin	
	Current				Nev	v						
	Die Revision	Datashe	et Nu	umber	Die	Revision		Datash	eet	Number		
	A0	SBAS95	8		A1			SBAS9	58A			
AD 4 Ro NOTE: Chang • Cha • Chang • Chang • Chang • Chang • Chang	Image: Stream of the Register Initialization section 27 Changed Figure 50 27 Express Changes may be reviewed at the datasheet links provided: 20											
Depe												
Impro	Improved product performance in certain applications											
Antic	Anticipated impact on Form, Fit, Function, Ouality or Reliability (positive / negative):											
None												
Chan Topsic	Changes to product identification resulting from this PCN: Topside Symbolization for the affected devices will be as shown below:											

QFN8x8:

Curr	ent		
+		· +	
! ()	1	TI = TI LETTERS
!	AZ3421Q	1	YM = YEAR MONTH DATE CODE
!	AØ	1	G = PRIMARY CODE FOR SCSAT
!	TI YMG	1	M = SECONDARY CODE FOR SCSAT
1	MLLL G4	1	LLL = ASSY LOT CODE
+		+	
0 -	PIN 1(MARKE	D)	10 CHARACTERS MAX LINES 1 & 2
New	,		
+		+	
! 0		1	TI = TI LETTERS
1	AZ34210	1	YM = YEAR MONTH DATE CODE
	c	1	G = PRIMARY CODE FOR SCSAT
1	TI YMG	1	M = SECONDARY CODE FOR SCSAT
1	MLLL G4	!	LLL = ASSY LOT CODE
+		+	
0 -	PIN 1(MARKE	ED)	10 CHARACTERS MAX LINES 1 & 2

Die Rev designator will change as shown in the table and sample label below:

Current	New			
Die Rev [2P]	Die Rev [2P]			
A0	A1			

Sample product shipping label (not actual product label)

TEXAS INSTRUMENTS MADE IN: Malaysia 2DC: 20: MSL '2 /260C/1 YEAR SEAL MSL '2 /260C/1 YEAR SEAL MSL '2 /260C/1 YEAR SEAL 03/2 07T: 03/2 0PT: 39 LBL: 5A (L)T0:17	Са рт э/оа 50	(1P) SN74LS07NSR (Q) 2000 (D) 0336 (31T)LOT: 3959047MLA (4W) TKY (1T) 7523483S12 (2P) REV: (V) 0033317 (20L) CS0: SHE (21L) CC0:USA (22L) AS0: MLA (23L) AC0: MYS
Product Affected:		
AD3421QRWERQ1	AD3421ORWETQ1	

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines) Approved 18-Jun-2020

Qualification Results Data Displayed as: Number of lots / Total sample size / Total failed

	Туре	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: ADC34210RWER01	QBS Product Reference: ADC34210RWER01
1	est Grou	рА–	Accelerated En	vironn	nent Stres	s Tests			
	PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Automotive Preconditioning Level 3	Level 3- 260C	-	3/770/0
	HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST, 130C/85%RH	96 Hours	-	3/231/0
	AC	A3	JEDEC JESD22- A102	3	77	Autoclave 121C	96 Hours	-	3/231/0
	TC	A4	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle, -65/150C	500 Cycles	-	3/231/0
	TC- WBP	A4	MIL-STD883 Method 2011	1	30	Auto Post TC Bond Pull	wires	-	1/30/0
	PTC	A5	JEDEC JESD22- A105	1	45	Power Temperature Cycle	1000 Cycles	N/A	-
	HTSL	A6	JEDEC JESD22- A103	1	45	High Temp Storage Bake 150C	1000 Hours	-	1/45/0
Т	Test Group B – Accelerated Lifetime Simulation Tests								
	HTOL	B1	JEDEC JESD22- A108	3	77	Life Test, 125C	1000 Hours	-	3/231/0
	EDR	B3	AEC Q100- 005	3	77	NVM Endurance, Data Retention, and Operational Life	-	N/A	-
1	est Grou	рС–	Package Assem	bly In	tegrity Te	sts			
	WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear (Cpk>1.67)	Wires	-	3/90/0
	WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull (Cpk>1.67)	Wires	-	3/90/0
	SD	C3	JEDEC JESD22- B102	1	15	Surface Mount Solderability	Pb	-	1/15/0
	SD	C3	JEDEC JESD22- B102	1	15	Surface Mount Solderability	Pb Free	-	1/15/0
	PD	C4	JEDEC JESD22- B100 and B108	3	10	Auto Physical Dimensions	Cpk>1.67	-	3/30/0
1	est Grou	p D –	Die Fabrication	n Relia	bility Test	ts			
	EM	D1	JESD61	-	-	Electromigration	-	Completed Per Process Technology Requirements	-
	TDDB	D2	JESD35	-	-	Time Dependant Dielectric Breakdown	-	Completed Per Process Technology Requirements	-
	НСІ	D3	JESD60 & 28	-	-	Hot Injection Carrier	-	Completed Per Process Technology Requirements	-
	NBTI	D4	-	-	-	Negative Bias	-	Completed Per	-

	Туре	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: ADC3421QRWERQ1	QBS Product Reference: <u>ADC34210RWER01</u>
						Temperature Instability		Process Technology Requirements	
	SM	D5	-	-	-	Stress Migration	-	Completed Per Process Technology Requirements	-
Τ	'est Grou	р E –	Electrical Verif	ication	1 Tests				
	HBM	E2	AEC Q100- 002	1	3	ESD - HBM - Q100	2500 V	-	1/3/0
	HBM	E2	AEC Q100- 002	1	3	ESD - HBM - Q100	3000 V	1/3/0	-
	CDM	E3	AEC Q100- 011	1	3	ESD - CDM - Q100	1500 V	1/3/0	1/3/0
	LU	E4	AEC Q100- 004	1	6	Latch-up	Per AEC- Q100- 004	1/6/0	1/6/0
	ED	E5	AEC Q100- 009	3	30	Auto Electrical Distributions	Cpk>1.67	3/90/0	3/90/0
A	dditional	l Test	S						
	MSL			-	-	Automotive L3 Powerpad Moisture Sensitivity	Level 3- 260C	-	1/12/0

A1 (PC): Preconditioning:

Performed for THB, Biased HAST, AC, uHAST, TC & PTC samples, as applicable.

Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40°C to +150°C Grade 1 (or Q): -40°C to +125°C Grade 2 (or T): -40°C to +105°C Grade 3 (or I): -40°C to +85°C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU Room : AC/uHAST

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

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