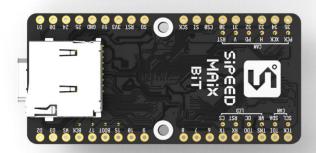
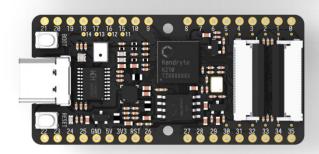
Sipeed Maix-BIT Specifications v2.0

Characteristic:

- CPU: RISC-V Dual Core 64bit, with FPU, 400Mhz standard Frequence(Can be overclocked)
- 24P DVP connector
- 24P 8bit MCU LCD connector
- MEMS microphone:
 MSM261S4030H0
 Seneitivity: -26(dB,dBFS
 @1kHz 1Pa)
- Compact size:
 53.3*25.4mm
 Can be connected to the breadboard directly
- Download circuit:
 Just connect the USB typeC
 cable to complete the
 download
- Onboard component:
 RGB LED \ RST button and USR
 button \ MicroSD card(TF card)
 slot







Version 2.0
Sipeed
Copyright © 2019
www.sipeed.com

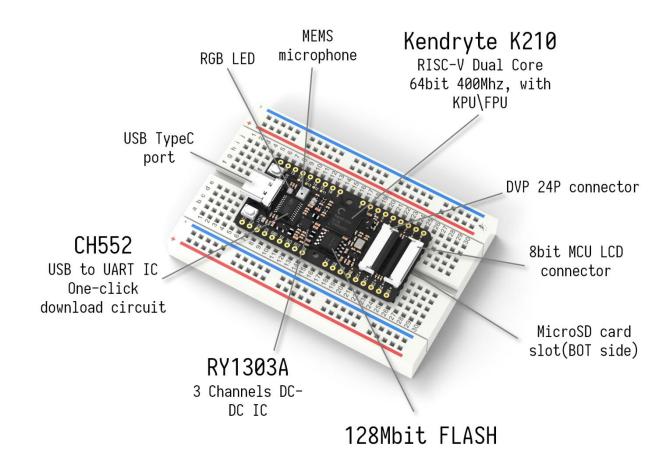
Update record		
V1.0	Edited on February 28, 2019 ; Original	
	document	
V2.0 (PCB was changed)	1、Added MEMS microphone	
	2、CH340 was changed to CH552	
	3、24P connectors(Front lock) were changed	
	to 24P connectors(rear lock)	
	4、Added two LEDs to display serial state	
	Added the website of Sipeed model shop	
	Updated "Overall description"	
	Updated the picture of Outlook information	

FEATURES OVERVIEW	
CPU : RISC-V Dual Core 64bit, 400Mh adjustable	Powerful dual-core 64-bit open architecture-based processor with rich community resources
Debugging Support	UART and JTAG interface for debugging
GPIO interface	All GPIOs were connected to 2.54mm pin header and pads
Micro SD card (TF card) slot	Support Self-elastic card holder
One-click Download circuit	Just connect the USB typeC cable to complete the download Onborad CH552T, which support Baudrate 1.5Mbps/750kbps/375kbps/187.5kbps/115200 bps and below
DVP Camera connector	24P 0.5mm FPC connector
LCD connector	8bit MCU LCD 24P 0.5mm FPC connector
Button	RST button and USR button
Onboard MEMS microphone	MSM261S4030H0 is an omnidirectional, Bottom-ported, I 2 S digital output MEMS Microphone. It has high performance and Reliability.

SOFTWARE FEATURES	
FreeRtos & Standard SDK	Support FreeRtos and Standrad development kit.
MicroPython Support	Support MicroPython on M1
Machine vision	Machine vision based on convolutional neural network
Machine hearing	High performance microphone array processor

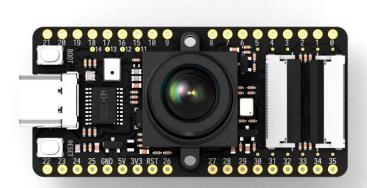
HARDWARE FEATURES		
Supply voltage of external power supply	4.8V ~ 5.2V	
Supply current of external power supply	>600mA	
Temperature rise	<30K	
Range of working temperature	-30°C ~ 85°C	

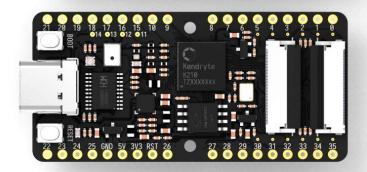
Overall description

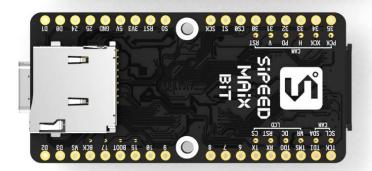


Outlook information

Size: 53.3x 25.4 x 13.0 mm







Maix-Bit-V2.0(Pin assignment table)				
Maix-Bit V2.0 silk	K210 IO	Function	Remark	IO Voltage
RST	Dedicated pin	K210_RST	10K pull up	1.8V
0	100	JTAG_TCK		
1	I01	JTAG_TDI		
2	102	JTAG_TMS		
3	103	JTAG_TD0		
4	104	K210_ISP_RX		
5	105	K210_ISP_TX		
6	106			
7	107			
8	108			
9	109			
10	IO10			
11	1011			
12	IO12	LED_G		
13	IO13	LED_R		
14	I014	LED_B		
15	1015	LLD_D		
16	1016	K210_B00T	10K pull up	
17	1017	KZIO_BOOT	Ισιν μαιτ αμ	
18	1018	MIC_BCK		3.3V
19	1019	MIC_WS	MEMS microphone	
20	1020	MIC_DAT3	MEMS IIIICI opiione	
21	1020	MIC_DATS		
22	1022			
23	1022			
24	1023			
25	1025			
26	1025	SPI0_MIS0		
27	1026	SPI0_SCLK		
28	1027	SPI0_SCLK SPI0_MOSI	TF card	
29	1028	SPIO_CS0		
30	1027	3510_030		
31	1030			
32	1032			
33	1032			
33	1033			
35	1035	I CD CC		
	1036	LCD_CS		
	1037	LCD_RST		
	1038	LCD_DC		
	1039	LCD_WR		
	1040	DVP_SDA	4.7K pull up	
	1041	DVP_SCL		1.8V
	1042	DVP_RST		
	1043	DVP_VSYNC		
	1044	DVP_PWDN		
	1045	DVP_HSYNC		
	1046	DVP_XCLK		
	1047	DVP_PCLK		

Resource	
Website	www.sipeed.com
Github	https://github.com/Lichee-Pi
BBS	http://bbs.sipeed.com
Wiki	maixpy.sipeed.com
Sipeed model shop	https://maixhub.com/
SDK Relevant information	dl.sipeed.com/MAIX/SDK
HDK Relevant information	dl.sipeed.com/MAIX/HDK
E-mail(Technical Support and Business Cooperation)	support@sipeed.com
telgram link	https://t.me/sipeed



Disclaimer and copyright notice

The information in this document, including the URL address for reference, is subject to change without notice.

The documentation is provided by Sipeed™ without warranty of any kind, including any warranties of merchantability, and any proposal, specification or sample referred to elsewhere. This document is not intended to be a liability, including the use of information in this document to infringe any patent rights.

Copyrights © 2019 Sipeed Limited. All rights reserved.