![Lattice Semiconductor logo] # Marketing Bulletin

# CrossLink-NX-33 OPN Removal

Lattice Semiconductor has decided to completely remove support for the Embedded Block Ram - Error Correction Code (EBR_ECC) feature from CrossLink-NX-33 FPGA devices. As a result, Lattice has deemed the -7 speed grade is no longer required. To optimize the available OPNs, Lattice will remove the following OPN:

| LIFCL-33-7USG84C |
|---|

Following the removal of the OPN above, the following CrossLink-NX-33 OPNs will be available going forward:

| LIFCL-33-8USG84C |
|---|
| LIFCL-33-8USG84I |

## Datasheet Update
Next update to the CrossLink-NX-33 Datasheet (revision 0.9) will reflect the above change.

## Software Update
Radiant version 2022.1 set to be released in late 4Q2022 will incorporate the changes listed above.

## Call to Action

1. Opportunities currently in the Sales Funnel using the LIFCL-33-7USG84C grade should be replaced by LIFCL-33-8USG84C ordering part number (OPN).

2. For approved quotes using LIFCL-33-7USG84C, the same price will be honored when the OPN is changed to LIFCL-33-8USG84C – please work with pricing team to update price quotes accordingly.

3. For sample requests / orders currently in the system for LIFCL-33-7USG84C, please switch to: LIFCL-33-8USG84C