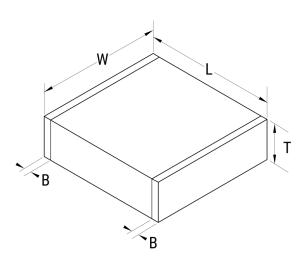


LDECC2100KA5N00

Aliases (DECC2100KA5N00)

LDE, Film, Metallized PEN Stacked, Automotive Grade, 0.01 uF, 10%, 40 VAC, 50 VDC, 125 $^{\circ}$ C, 1812, 1.7mm



Click here for the 3D model.

Dimensions	
Chip Size	1812
L	4.7mm +0.3/-0.2mm
W	3.3mm +/-0.3mm
Т	1.7mm MAX
В	0.4mm +0.5/-0.3mm

Packaging Specifications		
Packaging	T&R	
Packaging Quantity	4000	

General Information	
Series	LDE
Dielectric	Metallized PEN Stacked
Style	SMD Chip
Features	Automotive Grade
RoHS	Yes
Qualifications	AEC-Q200
AEC-Q200	Yes
Component Weight	0.175 g
Shelf Life	104 Weeks
MSL	3

Specifications	
Capacitance	0.01 uF
Capacitance Tolerance	10%
Voltage AC	40 VAC
Voltage DC	50 VDC
Temperature Range	-55/+125°C
Rated Temperature	125°C
Dissipation Factor	0.8% 1kHz
Insulation Resistance	1GOhms
Max dV/dt	100 V/us

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